

A semantic deconstruction of session types

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Abstract *Session types* are a well-established typing discipline for verifying that a *process* correctly implements a structured protocol (i.e., a *session*). Their main property is that typed processes “never go wrong”: they interact correctly, without communication mismatches.

In this paper, we revisit the theory of session types starting from their semantic foundations, with the goal of capturing and generalising their underlying notions of “correct interaction” and “safe process substitution”.

We develop a unified semantic framework based on *labelled transition systems*, encompassing session types and more general behaviours, endowed with both *synchronous* and *asynchronous* (i.e., buffered) semantics.

In this framework, we formalise the notion of “correct interaction” by introducing the *I/O compliance* and *safety* relations: *I/O compliance* refines the notion of *progress* between behaviours, while *safety* is inspired by Communicating Finite State Machines. We prove that *I/O compliance* and *safety* coincide on session types (both synchronous and asynchronous), and that they are preserved when passing from synchronous to asynchronous semantics.

We then study two preorders between behaviours, aimed at safe process substitution: the standard semantic subtyping, and the novel *I/O simulation*. The latter generalises the usual syntax-directed notions of typing

and subtyping on session types, and it coincides with semantic subtyping on synchronous session types. We show that such preorders are preserved (under suitable conditions) when passing from synchronous to asynchronous semantics.

Finally, we exploit the semantic properties of *I/O simulation* and *compliance* to construct a syntax-driven type system, combining typical session typing rules with a more flexible handling of buffered communication. Notably, we derive the type system correctness without an explicit subject reduction result, by relying instead on the general properties of *I/O simulation* and *compliance*.

1 Introduction

Session typing is a well-established approach to the design of concurrent applications [50, 51, 53, 72]. In a nutshell, a (binary) session type specifies a communication protocol, and dictates how two components are expected to interact through a bi-directional communication channel. The structure of the resulting interaction, called *session*, includes *choices* and *recursion*. In order to guarantee correct interaction, each endpoint of the channel is associated with a session type, so that the two endpoint types are *compliant*. Intuitively, this means that whenever one endpoint sends some data, the other endpoint is able to receive it — and conversely, whenever one endpoint expects to receive some data, the other one is willing to send something. While a session type specifies the interaction protocol between two components, the actual implementation of a component is represented by a *process*, usually formalised in a dialect of the π -calculus [60, 71].

The correspondence between a session type T and a process P can be statically established by a type system,

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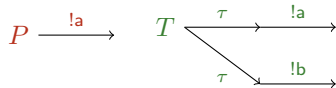
i.e. a set of syntax-driven rules that relate processes with types. Intuitively, the process P is typeable when it is possible to construct a proof of $\vdash P : T$, for some T , using the typing rules. If P is typeable, then its runtime interactions will respect those statically described by T . As a consequence, if both processes at the endpoints of a session type-check, and their types are compliant, then the interaction between the processes is deadlock-free.

Another relation typically studied in the session types community is that of *subtyping*: the *desideratum* is that if a session type T is subtype of U , and we have two processes P, Q such that $\vdash P : T$ and $\vdash Q : U$, then P can safely “replace” Q : i.e., any process that interacts correctly with Q will also interact correctly with P . Similarly to the typing relation, also the subtyping relation is typically defined through syntax-driven rules.

Now, assume that both processes and types are endowed with an operational semantics, in the form of a Labelled Transition System (LTS), as usually done in concurrency theory [59]. In this perspective, it is tempting to interpret the *syntactic* typing/subtyping relations as *semantic* relations, that would only depend on the LTS of process P and the LTS of type T , disregarding their syntax. This would follow the tradition of observational equivalences and preorders between processes, initiated by the study on bisimulation [68, 59], where the fundamental relations are given on arbitrary LTSs. This insight opens several research directions, that we discuss below.

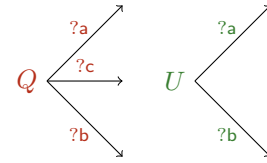
Question 1. *Can we reason on session-based interactions in a language-independent way?*

When $\vdash P : T$ holds, the relation between P and T may resemble a *simulation*: T simulates P iff, whenever $P \xrightarrow{\ell} P'$, then $T \xrightarrow{\ell} T'$, for some T' which still simulates P' . To elaborate further on this idea, consider a session type $T = !a \oplus !b$, which models an *internal* choice between two outputs $!a$ and $!b$. Further, consider the process $P = !a$ which just wants to output $!a$. Intuitively, the process P has type T , because any client who interacts correctly with T does so also with P . More concretely, assume that the processes P and T above are associated with the following LTSs:



We can observe that P is (weakly) simulated by T , in symbols $P \lesssim T$, because each move of P is matched by a (weak) move of T . This may suggest that weak simulation $P \lesssim T$ is a suitable semantic counterpart of the (syntactic) typing relation $\vdash P : T$.

To assess this semantic notion of typing, let us consider another example. Let $U = ?a \& ?b$ be the type modelling an *external* choice between two inputs $?a$ and $?b$, and let $Q = ?a + ?b + ?c$ (where $+$ is the standard CCS choice operator) be the process which allows for an additional input $?c$. Intuitively, Q has type U : indeed, any client interacting correctly with U will send either $!a$ or $!b$, and so it will interact correctly also with Q , since the process is able to receive those messages. Assume that Q and U are associated with the LTSs:



Differently from the previous example, we observe that Q is *not* weakly simulated by U (whereas the converse $U \lesssim Q$ holds). This dismantles our idea of semantically interpreting the session typing relation as weak simulation. On the positive side, the two examples above seem to suggest that a semantical typing relation should treat input and output capabilities differently: it should be *covariant* w.r.t. outputs and *contravariant* w.r.t. inputs.

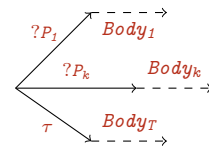
Question 2. *Can we devise new syntactic typing rules as instances of some general relation?*

A drawback of the syntax-driven approaches to session types is that they do not usually consider some common programming patterns for communication-oriented applications. For example, consider a server waiting some input which may not arrive on time. In Erlang [43], for instance, one can write:

```

receive P1 -> Body1...
      Pk -> Bodyk
after 10 -> BodyT
  
```

The `receive` statement is aborted if no messages matching P_1, \dots, P_k arrive within 10 milliseconds; in this case, $Body_T$ is executed. Such a program blurs the distinction between internal/external choices. Intuitively, its LTS has the form:



where the initial state has k input moves $?P_1, \dots, ?P_k$, and an internal τ -move which abstracts the timeout. This pattern eludes the notion of structured programming at the roots of the session types approach [49, 50]. However, the example above shows a use case that one would like to typecheck. This pattern cannot be usually

written in session calculi (like e.g. in many works extending [49, 50]), since their grammar does not include τ -branches. Can a syntax-independent approach handle patterns like the one above?

Question 3. *Can we reason on synchronous and asynchronous interactions in a uniform semantic setting?*

Distributed components usually interact in an asynchronous fashion via *FIFO* buffers. Now, consider a type $T = !a. ?b$, where $?b$ is fired *after* $!a$: the “natural” corresponding process is $P = !a. ?b$, which performs the expected actions in the same order. However, in an *asynchronous* setting, the sequentiality of the resulting interactions cannot be enforced: i.e., if P fires $!a$ asynchronously, then it will immediately start waiting for $?b$, without knowing whether $!a$ is still buffered, or was actually received by the other endpoint. Hence, one might argue that the parallel composition $P' = ?b \mid !a$ also conforms to T in the presence of asynchrony.

Although many works on asynchronous session types relate P with T , they do not usually relate P' with T . How can we formalise the intuition that both P and P' correctly implement T , in the asynchronous setting?

Question 4. *Is there a language-independent notion of “correct” interaction?*

In the discussion so far, we relied on the intuition of a “correct” interaction between processes. This notion can be defined in different ways, depending on the calculus in use, and what is considered to be an error. In the realm of synchronous session types a typical notion of correct interaction is *progress*, which holds when two processes keep interacting until they termination [4, 6]. Instead, in the realm of asynchronous interactions, like e.g. in Communicating Finite State Machines (CFSM) [19], progress is too large, since it relates e.g. two machines which just send outputs without ever synchronising. In the asynchronous setting, correct interactions are usually defined in terms of the absence of *orphan messages* [56, 42, 36] and *unspecified reception configurations* [34]. Can we achieve, in the semantic framework we are envisioning, a general notion of “correct” interaction which is coherent with the usual ones, *both* in the synchronous and in the asynchronous settings?

Contributions. To address the questions above, we introduce a behavioural theory of (first-order) session types, which unifies the notions of typing and subtyping, in both the synchronous and asynchronous cases.

We represent processes (called *behaviours* in this work) as states in an LTS whose labels are inputs, outputs, or τ -actions. We treat input and output labels in an asymmetric way, similarly to the semantics of CFSMs

and interface automata [3]. All the notions sought after in our questions are formalised as relations between behaviours, in a purely semantic fashion. To the best of our knowledge, ours is the first approach that extends session types theory to *arbitrary* behaviours in an LTS.

Our theory unifies synchronous and asynchronous behaviours within the same semantic framework, by defining relations which can be applied to both settings. This allows us to obtain several results about the *preservation* of our relations when passing from synchronous to asynchronous semantics.

More specifically, our main contributions are the following:

- a unifying semantic framework wherein we give synchronous/asynchronous semantics to session types (Definition 2.9) and CCS (Definition 2.14).
- a syntax-independent notion of “correct” interaction between behaviours, that we call *I/O compliance* (Definition 4.4). We prove that I/O compliance is stricter than progress, albeit coinciding with it on synchronous session types (Theorem 4.9).
- a formalisation in our semantic setting of the notion of *safety* in CFSMs [56] (Definition 5.15). We prove that I/O compliance is stricter than safety (Theorem 5.17), and that the two relations are equivalent on asynchronous session types (Theorem 5.20).
- preservation results showing that both I/O compliance and safety are preserved when passing from synchronous to asynchronous semantics of session types (Theorems 4.13 and 5.19).
- a syntax-independent notion of refinement between behaviours, that we call *I/O simulation* (Definition 6.4). We show that it is an I/O compliance-preserving preorder (Theorems 6.10 and 6.12), and that it coincides with the semantic subtyping relation [27, 44] on synchronous session types (Theorem 6.13). Under suitable conditions, I/O simulation is preserved when passing from synchronous to asynchronous semantics of session types (Theorem 6.20).
- a syntax-driven session-type systems derived from the semantic properties of I/O simulation (Section 7). We show in Corollary 7.11 that the typing relation obtained in this way is stricter than I/O simulation. The main correctness property of our type system is stated in Theorem 7.14: in the asynchronous setting, if two CCS processes are typeable with I/O compliant session types, then also the processes are I/O compliant, and therefore they can safely interact. This result does not require a proof of subject reduction, but it only relies upon the soundness of the type system w.r.t. the I/O simulation relation, and on the general semantic properties of I/O simulation.

We prove all our results either in the main text or in the appendix.

2 Behaviours

We consider a labelled transition system (LTS) where labels are partitioned into *internal*, *input*, and *output* actions. We call it *I/O LTS*, and we refer to its states as *behaviours*. In Section 2.1 we introduce some basic relations and operators on behaviours. We then define the synchronous and asynchronous semantics of binary session types (Section 2.2) and of CCS (Section 2.3) in terms of I/O LTSs.

2.1 The I/O LTS

We assume a *universal* I/O LTS, which includes all possible LTSs one can be interested in. We denote it as:

$$\left(\mathbb{U}, \mathbf{A}_\tau, \left\{ \xrightarrow{\ell_\tau} \mid \ell_\tau \in \mathbf{A}_\tau \right\} \right)$$

where:

- \mathbb{U} is the set of all *behaviours* (ranged over by p, q, \dots);
- \mathbf{A}_τ is the set of all *labels*;
- $\xrightarrow{\ell_\tau} \subseteq \mathbb{U} \times \mathbb{U}$ is the *transition relation*.

We partition \mathbf{A}_τ into *input actions* $\mathbf{A}^? = \{?a, ?b, \dots\}$, *output actions* $\mathbf{A}^! = \{!a, !b, \dots\}$, and the *internal action* τ . We postulate an involution $\text{co}(\cdot)$ such that $\text{co}(?a) = !a$ and $\text{co}(!a) = ?a$. Table 1 summarises the main syntactic categories and some notation. Hereafter, unless explicitly stated otherwise, we will always consider behaviours up to (label-preserving) isomorphism \cong .

Notation 2.1. *We write:*

- $p \xrightarrow{\ell_\tau} p'$ when $\exists p' . p \xrightarrow{\ell_\tau} p'$
- $p \rightarrow$ when $\exists \ell_\tau . p \xrightarrow{\ell_\tau}$
- $p \xrightarrow{!} q$ when $\exists a . p \xrightarrow{!a} q'$
- $\mathbf{0}$ for any behaviour without outgoing transitions
- $L^? = L \cap \mathbf{A}^?$ and $L^! = L \cap \mathbf{A}^!$, for all $L \subseteq \mathbf{A}$

Definition 2.2 (Parallel composition of behaviours). *For all $p, q \in \mathbb{U}$, we define the parallel composition $p \parallel q$ as the behaviour in \mathbb{U} with transitions given by the rules:*

$$\frac{p \xrightarrow{\ell_\tau} p'}{p \parallel q \xrightarrow{\ell_\tau} p' \parallel q} \quad \frac{q \xrightarrow{\ell_\tau} q'}{p \parallel q \xrightarrow{\ell_\tau} p \parallel q'} \quad \frac{p \xrightarrow{\ell} p' \quad q \xrightarrow{\text{co}(\ell)} q'}{p \parallel q \xrightarrow{\tau} p' \parallel q'}$$

We introduce below all the basic relations that will be used along the paper.

Definition 2.3 (Weak transitions). *We define the relation \Rightarrow as $(\xrightarrow{\tau})^*$, and the relation $\xRightarrow{\ell_\tau}$ as $\Rightarrow \xrightarrow{\ell_\tau} \Rightarrow$. Given a sequence of actions $w = \ell_1, \dots, \ell_n$, we write \xRightarrow{w} for $\xRightarrow{\ell_1} \dots \xRightarrow{\ell_n}$. Further, we write $\xrightarrow{!}$ for $\Rightarrow \xrightarrow{!} \Rightarrow$, and for a set of behaviours \mathbb{Q} , we write $\mathbb{Q} \xRightarrow{\ell_\tau} q'$ iff $\exists q \in \mathbb{Q} . q \xRightarrow{\ell_\tau} q'$, and similarly for $\mathbb{Q} \Rightarrow q'$.*

Definition 2.4 (Weak barbs). *We define $p \Downarrow$ as $\{\ell \mid p \xrightarrow{\ell}\}$. By extension, we define $\mathbb{Q} \Downarrow$ as $\bigcup_{q \in \mathbb{Q}} q \Downarrow$.*

Definition 2.5 (Weakly persistent inputs/outputs). *We write $p \xRightarrow{??a}$ (resp. $p \xRightarrow{!!a}$) iff for all p' , we have that $p \Rightarrow p'$ implies $p' \xRightarrow{??a}$ (resp. $p' \xRightarrow{!!a}$).*

Definition 2.6 (Weakly persistent barbs). *We define:*

$$p \Downarrow^{??} = \{?a \mid p \xRightarrow{??a}\} \quad p \Downarrow^{!!} = \{!a \mid p \xRightarrow{!!a}\}$$

and, by extension:

$$\mathbb{Q} \Downarrow^{??} = \bigcap_{q \in \mathbb{Q}} q \Downarrow^{??} \quad \mathbb{Q} \Downarrow^{!!} = \bigcap_{q \in \mathbb{Q}} q \Downarrow^{!!}$$

Definition 2.7 (Set transition relation). *We write $q \Rightarrow \mathbb{Q}$ iff $\emptyset \neq \mathbb{Q} \subseteq \{q' \mid q \Rightarrow q'\}$. We write $\mathbb{Q} \Rightarrow \mathbb{Q}'$ iff $\forall q' \in \mathbb{Q}' . \exists q \in \mathbb{Q} . q \Rightarrow q'$.*

Proposition 2.8 recalls inclusions between standard observational relations, where \sim/\approx stand for strong/weak bisimilarity, and $\overset{\sim}{\approx}$ is weak similarity [59].

Proposition 2.8. $\cong \subseteq \underset{\sim}{\subseteq} \sim \subseteq \underset{\approx}{\subseteq} \approx \subseteq \overset{\sim}{\approx}$.

2.2 Session types

A session type abstractly describes the behaviour of a process interacting with other parties. Here we consider the simple case of *binary* session types, where only two parties are involved. Our formalisation slightly adapts (and extends to the asynchronous case) the one in [4].

Formally, a session type is either an *internal choice* $\bigoplus_{i \in I} !a_i . T_i$, an *external choice* $\&_{i \in I} ?a_i . T_i$, or a recursion. In an internal choice, the process can choose one of the branches (say, the i -th), fire the output $(!a_i)$, and then proceed with the continuation T_i . In an external choice, instead, the branch is chosen by the context; the process waits until receiving an input $(?a_i)$, and then continues as T_i . Empty choices (either internal or external) represent successful termination.

Definition 2.9 (Session types). *Session types are terms with the syntax:*

$$T ::= \&_{i \in I} ?a_i . T_i \mid \bigoplus_{i \in I} !a_i . T_i \mid \text{rec}_X T \mid X$$

where (i) the set I is finite, (ii) actions in internal/external choices are pairwise distinct, and (iii) recursion is guarded. We omit the symbol $\oplus/\&$ in singleton choices. We write $\mathbf{0}$

$?a, ?b, \dots \in A^?$	Input actions	\mathbb{U}	Set of all behaviours:
$!a, !b, \dots \in A^!$	Output actions	\mathbb{U}_{ST}	Sync session behaviours
$A = A^? \cup A^!$	Set of I/O actions	\mathbb{U}_{aST}	Async session behaviours
$\text{co}(\cdot)$	Involution of I/O actions	\mathbb{U}_{aCCS}	Async CCS behaviours
$\ell, \ell', \dots \in A$	Actions (visible labels)	$p, q, r, \dots \in \mathbb{U}$	Behaviours
τ	Internal action	$T, U, V, \dots \in \mathbb{U}_{ST}$	Session types (sync)
$A_\tau = A \cup \{\tau\}$	Set of labels	$T[\sigma], U[\rho], \dots \in \mathbb{U}_{aST}$	Session types (async)
$\ell_\tau, \ell'_\tau, \dots \in A_\tau$	Labels	$P[\sigma], Q[\rho], \dots \in \mathbb{U}_{aCCS}$	Async CCS behaviours
		$\mathbb{P}, \mathbb{Q}, \mathbb{R}, \dots \subseteq \mathbb{U}$	Sets of behaviours
$\xrightarrow{\ell_\tau}, \rightarrow, \xrightarrow{!}$	Transitions		
\Rightarrow	Weak τ transition	$p \Downarrow$	Weak barbs \cong Isomorphism
$\xRightarrow{\ell_\tau}$	Weak transition	$p \Downarrow^?, p \Downarrow^!$	Weak I/O barbs \sim Strong bisimilarity
$\xRightarrow{??a}, \xRightarrow{!!a}$	Weakly persistent I/O actions	$p \Downarrow^{??}, p \Downarrow^{!!}$	Weakly persistent I/O barbs \approx Weak bisimilarity
\Rightarrow	Set transition		\approx Weak similarity

Table 1: Summary of notation.

for the empty (internal/external) choice, and will usually omit its trailing occurrences. Unless otherwise specified, we assume session types to be closed, i.e. without free recursion variables.

We present a *synchronous* semantics for session types (Definition 2.10), and one *asynchronous* (Definition 2.11). In both cases, internal choices first commit to one of the branches $!a.T$ before enabling $!a$, while external choices enable all their actions.

Definition 2.10 (Synchronous session behaviours). *We denote with \mathbb{U}_{ST} the set of behaviours of the form T , with transitions given by the rules:*

$$\frac{k \in I}{\&_{i \in I} ?a_i.T_i \xrightarrow{?a_k} T_k} \text{ (TEXT)} \quad \frac{k \in I \quad |I| > 1}{\bigoplus_{i \in I} !a_i.T_i \xrightarrow{\tau} !a_k.T_k} \text{ (TINT)}$$

$$\frac{}{!a.T \xrightarrow{!a} T} \text{ (TOUT)} \quad \frac{T[\text{rec}_X T/X] \xrightarrow{\ell_\tau} T'}{\text{rec}_X T \xrightarrow{\ell_\tau} T'} \text{ (TREC)}$$

For the asynchronous semantics, we consider behaviours of the form $T[\sigma]$ where σ is a finite sequence of output actions, modelling an unbounded *FIFO buffer*. We denote with ϵ the empty buffer, and with $!a.\sigma$ a buffer with head $!a$ and tail σ .

Definition 2.11 (Asynchronous session behaviours). *We denote with \mathbb{U}_{aST} the set of behaviours of the form $T[\sigma]$, with transition given by the rules:*

$$\frac{k \in I}{(\bigoplus_{i \in I} !a_i.T_i)[\sigma] \xrightarrow{\tau} T_k[\sigma. !a_k]} \text{ (TINTA)} \quad \frac{}{T[!a.\sigma] \xrightarrow{!a} T[\sigma]} \text{ (TOUTA)}$$

$$\frac{k \in I}{(\&_{i \in I} ?a_i.T_i)[\sigma] \xrightarrow{?a_k} T_k[\sigma]} \text{ (TEXTA)}$$

$$\frac{T[\text{rec}_X T/X][\sigma] \xrightarrow{\ell_\tau} T'[\sigma']}{\text{rec}_X T[\sigma] \xrightarrow{\ell_\tau} T'[\sigma']} \text{ (TRECA)}$$

Rule (TINTA) enqueues the selected output with a τ -move, while rule (TOUTA) dequeues an output $!a$ with a $!a$ -transition. The rules (TEXTA) and (TRECA) are similar to their synchronous counterparts.

Example 2.12. *Let $T_1 = !a. ?b \oplus !a'. ?b'$, and let $T_2 = ?a. (!b \oplus !c)$. Their LTSs are shown in Figure 1 (note that the sync/async behaviours of T_2 are isomorphic).*

Similarly to [36], we adopt an *equi-recursive* view [69], by considering session types equivalent (denoted by the relation \equiv) up-to unfolding of recursion (see Definition A.1 and Proposition A.2).

Proposition 2.13 says that (up-to isomorphism) asynchronous session behaviours are *not* more general than synchronous ones, and *vice versa*: e.g., for the session types in Example 2.12, $T_1 \notin \mathbb{U}_{aST}$ while $T_1 \sqcap \notin \mathbb{U}_{ST}$.

Proposition 2.13. $\mathbb{U}_{aST} \not\subseteq \mathbb{U}_{ST}$.

2.3 CCS

We consider a fragment of CCS [59] without delimitations and relabelling, and we provide it with synchronous and asynchronous semantics.

Definition 2.14. *CCS terms have the syntax:*

$$P, Q, R, S ::= \mathbf{0} \mid \ell_\tau.P \mid P+Q \mid P|Q \mid X \mid \mu_X P$$

where recursion is guarded.

Definition 2.15 (Synchronous CCS behaviours). *We denote with \mathbb{U}_{CCS} the set of behaviours of the form P , with transitions given by the following rules (symmetric ones are omitted):*

$$\frac{}{\ell_\tau.P \xrightarrow{\ell_\tau} P} \quad \frac{P \xrightarrow{\ell_\tau} P'}{P+Q \xrightarrow{\ell_\tau} P'} \quad \frac{P \xrightarrow{\ell_\tau} P'}{P|Q \xrightarrow{\ell_\tau} P'|Q} \quad \frac{P[\mu_X P/X] \xrightarrow{\ell_\tau} P'}{\mu_X P \xrightarrow{\ell_\tau} P'}$$

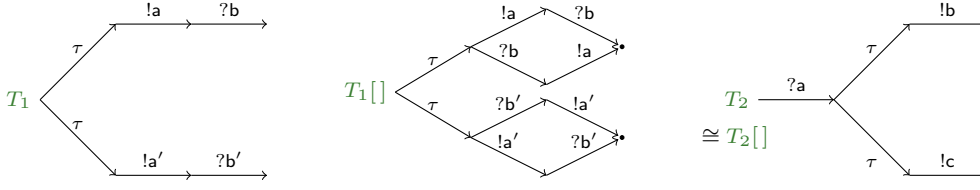


Figure 1: Three session behaviours.

The synchronous semantics of CCS terms is standard, except that the operator $|$ allows interleaving but not synchronisation, which instead is allowed by the LTS-level operator \parallel .

Definition 2.16 (Asynchronous CCS behaviours). We denote with \mathbb{U}_{aCCS} the set of behaviours of the form $P[\sigma]$, with transitions given by the following rules (symmetric ones are omitted):

$$\begin{array}{c} \frac{\ell_\tau \in \{\tau\} \cup \mathbf{A}^?}{(\ell_\tau.P)[\sigma] \xrightarrow{\ell_\tau} P[\sigma]} \quad \frac{}{(!a.P)[\sigma] \xrightarrow{\tau} P[\sigma.!a]} \\ \frac{P[\mu_X P/X][\sigma] \xrightarrow{\ell_\tau} P'[\sigma']}{\mu_X P[\sigma] \xrightarrow{\ell_\tau} P'[\sigma']} \quad \frac{}{P[!a.\sigma] \xrightarrow{!a} P[\sigma]} \\ \frac{P[\sigma] \xrightarrow{\ell_\tau} P'[\sigma']}{(P+Q)[\sigma] \xrightarrow{\ell_\tau} P'[\sigma']} \quad \frac{P[\sigma] \xrightarrow{\ell_\tau} P'[\sigma']}{(P|Q)[\sigma] \xrightarrow{\ell_\tau} (P'|Q)[\sigma']} \end{array}$$

As in async session behaviours, an output $!a$ is enqueued before it can be fired, and behaviours *cannot* read from their own buffers. In fact, in $P[\sigma] \parallel Q[\sigma']$ each behaviour reads from the other's buffer.

Example 2.17. The behaviour of $!a.\tau[]$ is shown as p_1 in Figure 2 at page 8.

Proposition 2.18 states that async CCS behaviours strictly include async session behaviours, while they do not include synch CCS, and synch session behaviours.

Proposition 2.18. $\mathbb{U}_{aST} \subsetneq \mathbb{U}_{aCCS} \not\supseteq \mathbb{U}_{CCS} \supsetneq \mathbb{U}_{ST}$, and $\mathbb{U}_{aCCS} \not\supseteq \mathbb{U}_{ST}$ and $\mathbb{U}_{CCS} \not\supseteq \mathbb{U}_{aST}$.

Session types can be encoded into CCS as follows.

Definition 2.19. We define an encoding $\llbracket \cdot \rrbracket$ of session types into CCS terms as follows:

$$\begin{array}{l} \llbracket \bigoplus_{i \in I} !a_i.T_i \rrbracket = \sum_{i \in I} !a_i.\llbracket T_i \rrbracket \quad \llbracket \text{rec}_X T \rrbracket = \mu_X \llbracket T \rrbracket \\ \llbracket \&_{i \in I} ?a_i.T_i \rrbracket = \sum_{i \in I} ?a_i.\llbracket T_i \rrbracket \quad \llbracket X \rrbracket = X \end{array}$$

Proposition 2.20 states that, in the asynchronous semantics, a session type is isomorphic to its encoding.

Proposition 2.20. $T[] = \llbracket T \rrbracket$.

Example 2.21. The session type $!a \oplus !b$ in \mathbb{U}_{ST} is encoded in \mathbb{U}_{CCS} as the isomorphic behaviour $\tau.!a + \tau.!b$, while $?a \& ?b$ is encoded as $?a + ?b$. By Definition 2.19,

the session type $!a \oplus !b[\sigma]$ in \mathbb{U}_{aST} is encoded in \mathbb{U}_{aCCS} as the isomorphic behaviour $!a + !b[\sigma]$, while $?a \& ?b[\sigma]$ is encoded as $?a + ?b[\sigma]$. Instead, $?a + !b$ in \mathbb{U}_{CCS} is not isomorphic to any session type in \mathbb{U}_{ST} (due to the mixed choice between an input and an output), nor to any behaviour in \mathbb{U}_{aCCS} (because buffered semantics would introduce a τ -transition on the $!b$ -branch). Moreover, $?a + \tau$ in \mathbb{U}_{CCS} and $?a + \tau[]$ in \mathbb{U}_{aCCS} are not isomorphic to any asynchronous session type in \mathbb{U}_{ST} , nor in \mathbb{U}_{aST} (due to the choice between an input and an internal move without continuation). Finally, $!a.?b$ from \mathbb{U}_{ST} is not isomorphic to any behaviour in \mathbb{U}_{aCCS} (because the buffered semantics of the latter do not allow the $!a$ -transition to preempt the $?b$ -transition).

3 A motivating example: Alice and bartender

We now introduce a running example, used throughout the paper. The following types describe the behaviours of a bartender (B), and of a customer Alice (A):

$$\begin{array}{l} U_B = \text{rec}_X (?a\text{Coffee}.!coffee.X \& ?a\text{Beer}.(!beer.X \oplus !no.X) \& ?pay) \\ T_A = !a\text{Coffee}.?coffee.!pay \oplus !a\text{Beer}.(?beer.!pay \& ?no.!pay) \end{array}$$

The bartender presents an external choice $\&$, allowing a customer to order either coffee or beer, or to eventually pay; in the first case, he will serve the coffee and then recursively wait for more orders; in the second case, he uses the internal choice \oplus to decide whether to serve the beer or not — and then waits for more orders; in the third case, after the due amount (possibly 0) is paid, the interaction ends.

Alice internally chooses between coffee or beer; in the first case, she waits to get the coffee and then pays; in the second case, she lets the bartender choose between serving the beer, or saying no — and in both cases, she will check out.

Intuitively, T_A and U_B are *compliant* — i.e., their parallel composition $T_A \parallel U_B$ (under Definitions 2.2 and 2.10) gives rise to a “correct” interaction such that:

1. each output of T_A is matched by an input of U_B (and vice versa), and
2. when they stop synchronising (after pay), they are both “successful” — i.e., they reach a final configuration $\mathbf{0} \parallel \mathbf{0}$ without further enabled transitions.

Moreover, the following processes type-check — roughly, because the pairs U_B, Q_B and T_A, P_A expose the same interaction labels, and have similar branching structures:

$$Q_B = \mu_Y (?aCoffee.!coffee.Y + ?aBeer.(!beer.Y + !no.Y) + ?pay)$$

$$P_A = !aCoffee.?coffee.!pay + !aBeer.(?beer.!pay + ?no.!pay)$$

From typing and compliance, we can deduce that $P_A \parallel Q_B$ synchronise “correctly” (reflecting the “correct” interaction of $T_A \parallel U_B$), and reach the successful configuration $\mathbf{0} \parallel \mathbf{0}$, where Alice and the bartender agree in stopping their interaction.

Alice may also implement a *subtype* of T_A only asking for coffee: $T'_A = !aCoffee.?coffee.!pay$, with a corresponding process $P'_A = !aCoffee.?coffee.!pay$: also in this case, we would have that T'_A and U_B are compliant, and thus $P'_A \parallel Q_B$ also gives rise to a “correct” interaction.

So far, the structure of A 's and B 's processes have matched the structure of the respective types. This is a common situation in the session types literature: processes are usually written using calculi inheriting the structured communication approach pioneered by Honda *et al.* [49, 50], thus reflecting the internal/external choices of types. However, sometimes things may be more complex. The bartender might have other incumbencies, and he may need to stop selling beer after a certain hour:

$$Q''_B = \mu_Y (?aCoffee.!coffee.Y + ?aBeer.(!beer.Y + !no.Y) + ?pay)$$

$$+ \tau.\mu_Z (?aCoffee.!coffee.Z + ?aBeer.!no.Z + ?pay)$$

This reminds us of the small Erlang code sample given in Question 1: the τ branch represents the bartender's decision to stop waiting for customer orders, perform some internal duties (e.g. clean up the bar) and then serve again — this time, refusing to sell beer. Intuitively, we would like Q''_B to still have the type U_B , since compliant customer processes (e.g. Alice's one) will still be able to interact (either before or after the τ). A process like Q''_B , however, cannot usually be written (and typed) using classical session calculi: their grammar does not offer a τ prefix, since it would allow for processes where the distinction between internal/external choices is blurred (contrary to the expected program structure).

Let us consider another scenario: Alice is late for work. But she realises that the bartender-customer system is *asynchronous*: the counter is a bidirectional *buffer* where drinks and money can be placed. Thus, she tries to save time by implementing the following type and process:

$$T''_A = !aCoffee.!pay.?coffee \quad P''_A = !aCoffee.(?coffee \mid !pay)$$

i.e., in her type she plans to order a coffee, put her money on the counter while the bartender prepares her drink, and take it as soon as it is ready; in her process, she orders a coffee, and tries to grab the coffee with one hand, while putting the money on the counter with the other. P''_A represents an optimised program exploiting

buffered communication semantics, thus diverging from the syntactic structure of T''_A , and reminds us of the issues discussed in Question 3. Therefore, is T''_A a type for P''_A ? Is T''_A compliant with U_B , and will P''_A interact smoothly with Q_B and Q''_B ? We shall answer these questions later on in Section 7.

4 I/O compliance

We introduce *I/O compliance*, a language-independent relation between arbitrary behaviours which formalises the intuitive notion of “correct interaction”. We start by reviewing *progress*, a standard notion of compliance which identifies “correct interaction” with the absence of deadlock. Then, we observe its limitations in Section 4.1, as well as some limitations of other well-known notions of compliance. In Sections 4.2 and 4.3 we show how I/O compliance overcomes these limitations.

4.1 Compliance as progress

We start by discussing *progress*, a notion of compliance which interprets correctness as the absence of (unsuccessful) stuck states. To model the client/server asymmetry [31, 4], we consider the *asymmetric* progress relation: a behaviour can stop interacting whenever it reaches success (regardless of the state of its counterpart).

Definition 4.1 (Progress). *We write $p \dashv q$ whenever $p \parallel q \Rightarrow p' \parallel q' \not\rightarrow$ implies $p' = \mathbf{0}$. We write \vdash for \dashv^{-1} , and $\dashv\vdash$ for $\dashv \cap \vdash$.*

Example 4.2. *We have the following relations:*

$$\begin{array}{ll} !a.?b \dashv\vdash ?a.!b & \text{rec}_X !a.X \dashv\vdash \text{rec}_Y ?a.Y \\ !a.?b \not\dashv\vdash !b.?a & (!a.?b) \parallel \dashv\vdash (!b.?a) \parallel \\ !a.!b \not\dashv\vdash ?c & (!a.!b) \parallel \dashv\vdash ?c \parallel \\ !a.?b \not\dashv\vdash ?a & (\text{rec}_X ?a.X) \parallel \not\dashv\vdash !b \parallel \\ \text{rec}_X !a.X \not\dashv\vdash ?a & (\text{rec}_X !a.X) \parallel \dashv\vdash ?b \parallel \end{array}$$

We now restate a result from [4]: progress between two synchronous session behaviours can be characterised in a syntactic way.

Proposition 4.3 ($\dashv/\dashv\vdash$ -induced shapes of session types). *If $T \dashv U$, then exactly one of the following cases holds:*

- $T = \mathbf{0}$;
- $T \equiv \&_{i \in I} ?a_i.T_i$ and $U \equiv \bigoplus_{j \in J} !a_j.U_j$, with $\emptyset \neq J \subseteq I$, and $\forall j \in J. T_j \dashv U_j$;
- $T \equiv \bigoplus_{i \in I} !a_i.T_i$ and $U \equiv \&_{j \in J} ?a_j.U_j$, with $\emptyset \neq I \subseteq J$, and $\forall i \in I. T_i \dashv U_i$.

Furthermore, if $T \dashv\vdash U$, we have either:

- $T = U = \mathbf{0}$;

- b. $T \equiv \&_{i \in I} ?a_i.T_i$ and $U \equiv \bigoplus_{j \in J} !a_j.U_j$, with $\emptyset \neq J \subseteq I$, and $\forall j \in J. T_j \dashv\vdash U_j$;
- c. $T \equiv \bigoplus_{i \in I} !a_i.T_i$ and $U \equiv \&_{j \in J} ?a_j.U_j$, with $\emptyset \neq I \subseteq J$, and $\forall i \in I. T_i \dashv\vdash U_i$.

Note that, when leaving synchronous session types, progress also relate behaviours with arguably incorrect interactions. For instance, we have $(\text{rec}_X !a.X) [] \dashv\vdash ?b []$, because the two asynchronous behaviours never reach a stuck state, even if no interaction ever happens. Ideally, we would like a stricter compliance relation, avoiding such “vacuous” progress.

To this aim one may consider, e.g., the *may-*, *must-* or *should-* testing compliance [10, 24, 38]. These relations do not hold in the above-mentioned case, since they all require that the success state $\mathbf{0} \parallel \mathbf{0}$ is reachable. On the other hand, these relations do not consider as compliant the asynchronous behaviours $(\text{rec}_X !a.X) []$ and $(\text{rec}_Y ?a.Y) []$, precisely because they never reach success. The last pair of behaviours models a producer/consumer system, where each produced message is consumed. Indeed, in the CFSM literature [19, 56] these two behaviours would be considered compliant.

4.2 I/O compliance: definition and examples

We now introduce I/O compliance (recall from Section 2 that $p \Downarrow^! = (p \Downarrow)^! = p \Downarrow \cap A^!$).

Definition 4.4 (I/O compliance). *We say that a relation \mathcal{R} is an I/O compliance iff, whenever $p \mathcal{R} q$:*

- $p \Downarrow^! \subseteq \text{co}(q \Downarrow^?)$ and $((p \Downarrow^! = \emptyset \wedge p \Downarrow^? \neq \emptyset) \text{ implies } \emptyset \neq q \Downarrow^! \subseteq \text{co}(p \Downarrow^?))$;
- $p \xrightarrow{\ell} p' \wedge q \xrightarrow{\text{co}(\ell)} q'$ implies $p' \mathcal{R} q'$;
- $p \xrightarrow{\tau} p'$ implies $p' \mathcal{R} q$;
- $q \xrightarrow{\tau} q'$ implies $p \mathcal{R} q'$.

We write $\dot{\prec}$ for the largest I/O compliance relation, $\ddot{\prec}$ for $(\dot{\prec})^{-1}$, and $\ddot{\asymp}$ for the largest symmetric I/O compliance. We say that p and q are I/O compliant iff $p \ddot{\asymp} q$.

Item *a* captures the different role of inputs and outputs: each weak output must be matched by a corresponding input, so that the output can be dispatched. Although it is not required to match *every* weak input, the presence of weak inputs (without weak outputs) requires the counterpart to offer some matching weak output. Similarly to Proposition 4.3, when $p \ddot{\asymp} q$, item *a* allows to enlarge the inputs of p (when non-empty), or to restrict its outputs (to a non-empty subset), while preserving I/O compliance. Items *b*, *c*, *d* require I/O compliance to be preserved if p and q synchronise or do internal moves.

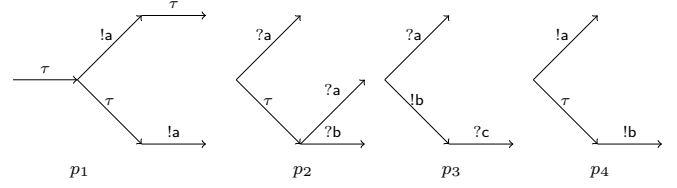


Figure 2: Some (non-session) behaviours.

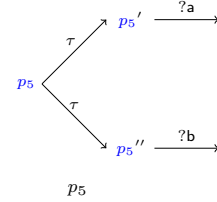


Figure 3: A behaviour without an I/O compliant one.

Lemma 4.5 states that the symmetric relation $\ddot{\asymp}$ can be obtained by intersecting the two asymmetric relations (unlike e.g. bisimilarity [59]).

Lemma 4.5. $\ddot{\asymp} = \dot{\prec} \cap \ddot{\prec}$.

Example 4.6. *Consider the behaviours in Figure 2. We have that $p_1 \ddot{\asymp} p_2$, $p_2 \ddot{\asymp} p_4$, $p_1 \dot{\prec} p_3$, and $p_2 \dot{\prec} p_3$, while all the other pairs of behaviours are not compliant.*

The following example shows that some behaviours do not admit a (symmetric) I/O compliant one.

Example 4.7. *Consider the behaviour p_5 in Figure 3. We show that, for all behaviours q , it must be $q \not\ddot{\asymp} p_5$. By contradiction, assume that $q \ddot{\asymp} p_5$, for some q . Since $p_5 \xrightarrow{\tau} p_5'$ and $p_5 \xrightarrow{\tau} p_5''$, by applying twice clause *d* of Definition 4.4 it follows that $q \ddot{\asymp} p_5'$ and $q \ddot{\asymp} p_5''$. Now, by applying twice the first part of clause *a*, we get:*

$$q \Downarrow^! \subseteq \text{co}(p_5' \Downarrow^?) \cap \text{co}(p_5'' \Downarrow^?) = \{?a\} \cap \{?b\} = \emptyset$$

*And since $p_5' \ddot{\asymp} q$ and $p_5' \Downarrow^! = \emptyset \neq p_5' \Downarrow^?$, by the second part of clause *a* we would get $\emptyset \neq q \Downarrow^!$ (contradiction).*

The following example shows that I/O compliance does not hold when the progress is vacuous.

Example 4.8. *We have that $(\text{rec}_X !a.X) [] \dashv\vdash ?b []$, but $(\text{rec}_X !a.X) [] \not\ddot{\asymp} ?b []$: in fact, the weak output barbs of the LHS contain $!a$, not matched by the weak input barbs of the RHS (that only contain $?b$) — thus violating item *a* of Definition 4.4.*

In CCS, let $P = !a + !b$, and let $Q = ?a$. We have that $P \dashv\vdash Q$ (because the LHS and RHS can synchronise on a , and then terminate), but $P \not\ddot{\asymp} Q$ (because $!b$ on the LHS is not matched by the inputs of the RHS).

4.3 Main properties

Theorem 4.9 relates I/O compliance with progress. If two behaviours are I/O compliant, then they enjoy progress, while the *vice versa* is false (see Example 4.8). Still, $\dot{\prec}$ and \dashv coincide for *synchronous* session behaviours. The intuition is the following. Take $T, U \in \mathbb{U}_{ST}$: by Definition 2.10, they cannot generate infinite τ -traces, and whenever they output, they always commit to a *single* output transition. These properties allow to avoid situations similar to the ones in Example 4.8.

Theorem 4.9. (a) If $p \dot{\prec} q$, then $p \dashv q$; (b) if $p, q \in \mathbb{U}_{ST}$ and $p \dashv q$, then $p \dot{\prec} q$.

Example 4.10 (Alice and bartender). Recall the types and processes in Section 3. In the *sync* case, $U_B \dashv T_A$, $U_B \dot{\times} T_A$, $U_B \dashv T'_A$ and $U_B \dot{\times} T'_A$. The same holds for their *async* versions. When Alice is late for work, in the *sync* case we have $U_B \not\dot{\dashv} T''_A$ and $U_B \not\dot{\times} T''_A$, due to the wrong order of Alice's actions. In the *async* case, instead, we have $U_B \dashv T''_A$ and $U_B \dot{\times} T''_A$. The latter relation is detailed in Table 2. Note that U_B has an input ($?aBeer$) which is not matched by an output of T''_A , and therefore leads to states not considered in the relation (omitted in the Table). The same goes for $U_B^{(3)}$, with two unmatched inputs ($?aBeer$ and $?aCoffee$).

When two synchronous session behaviours are I/O compliant, the following lemma characterises the form of buffers in their asynchronous computations. In particular, in each reachable configuration one of the queues must be empty (thus resulting in a *half-duplex* communication [34]).

Lemma 4.11. Let $T \circ U$, with $\circ \in \{\dashv, \vdash, \dashv\vdash, \dot{\prec}, \dot{\succ}, \dot{\times}\}$. If $T \parallel U \Rightarrow T'[\sigma] \parallel U'[\rho]$, then:

- (i) $\sigma = \epsilon$ or $\rho = \epsilon$.
- (ii) if $\sigma = \rho = \epsilon$ then $T' \circ U'$.

Lemma 4.12 states that if two synchronous session behaviours are I/O compliant, then they are such also when passing to the asynchronous semantics. The inverse implication is false: I/O compliance relates asynchronous session behaviours which are *not* compliant under the synchronous semantics, e.g. $(!a.?b) \parallel \dot{\times} (!b.?a) \parallel$.

Lemma 4.12. If $T \dot{\prec} U$, then $T \parallel \dot{\prec} U \parallel$.

The following theorem extends Lemma 4.12 to all the notions of compliance studied in this section.

Theorem 4.13. Let $\circ \in \{\vdash, \dashv\vdash, \dashv, \dot{\succ}, \dot{\times}, \dot{\prec}\}$. If $T \circ U$, then $T \parallel \circ U \parallel$.

Proof. The case $\circ = \dot{\prec}$ follows by Lemma 4.12. For $\circ = \dashv$, we have:

$$\begin{array}{ccc} T \dashv U & \dashrightarrow & T \parallel \dashv U \parallel \\ \text{Theorem 4.9(b)} \downarrow & & \uparrow \text{Theorem 4.9(a)} \\ T \dot{\prec} U & \xrightarrow{\text{Lemma 4.12}} & T \parallel \dot{\prec} U \parallel \end{array}$$

The case for $\circ \in \{\vdash, \dot{\succ}\}$ follow by symmetry. Instead, the cases for $\circ \in \{\dashv\vdash, \dot{\times}\}$ follow respectively from $\dashv\vdash = \vdash \cap \dashv$ (by Definition 4.1) and $\dot{\times} = \dot{\succ} \cap \dot{\prec}$ (by Lemma 4.5). \square

5 Safety

In the realm of Communicating Finite State Machines, where communication is asynchronous, the notion of correct interaction (called *safety* in [56]) is stricter than progress: e.g., it requires that enqueued messages are eventually consumed. In this section we interpret safety in our semantic setting, and we relate it with I/O compliance. The main result is that I/O compliance implies safety (Theorem 5.17), and that the two notions coincide on asynchronous session behaviours (Theorem 5.20).

5.1 Orphan messages

In CFSMs, *orphan messages* are outputs that are sent, but never received. To formalise this notion in our semantic setting, consider a parallel composition $p \parallel q$. If p always exposes a (weak) output transition $!a$ (i.e., $p \xrightarrow{!a}$, as per Definition 2.5) but q does not expose a matching input transition $?a$, then we can say that $!a$ is “orphan”.

Definition 5.1 (Orphan message configuration). We say that $p \parallel q$ is an orphan message configuration of p iff $\exists a. p \xrightarrow{!a}$ and $q \not\xrightarrow{?a}$. In this case we say that $!a$ is an orphan message of p .

Proposition 5.2. Let $p \parallel q$ be an orphan message configuration, with $!a$ orphan message of p . If $p \Rightarrow p'$ and $q \Rightarrow q'$, then $p' \parallel q'$ is an orphan message configuration, with $!a$ orphan message of p' .

Proof. By Definition 5.1, we have $p \xrightarrow{!a}$ and $q \not\xrightarrow{?a}$. From Definition 2.5, we have $p' \xrightarrow{!a}$; furthermore, $q \not\xrightarrow{?a}$ implies $q' \not\xrightarrow{?a}$. Therefore, by Definition 5.1 we conclude that $p' \parallel q'$ is an orphan message configuration, with $!a$ orphan message of p' . \square

Note that the usual notion of orphan message from CFSM literature [56, 42] requires that there exists at least a non-empty buffer, and each machine is in a final state. Our definition relaxes the second requirement, while it uses persistent outputs to formalise the first one.

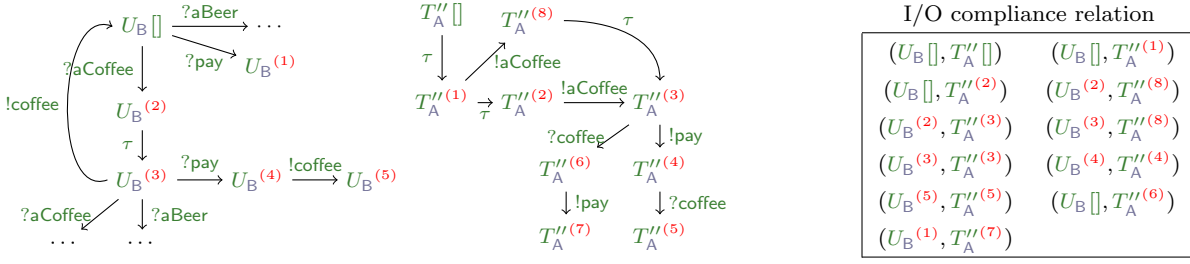


Table 2: I/O compliance for the asynchronous Alice and bartender.

The intuition is clear when considering asynchronous session behaviours: there, persistent outputs exist whenever the buffer is non-empty. In those behaviours, orphan messages are characterised by Lemma 5.3.

Lemma 5.3. $T[\sigma] \parallel U[\rho]$ is an orphan message configuration with $!a$ orphan message of $T[\sigma]$ iff both the following conditions hold:

- (i) $\sigma = !a.\sigma'$, or $\sigma = \epsilon$ and $T \equiv !a.T'$;
- (ii) $U[\rho] \not\stackrel{?a}{\Rightarrow}$.

Proof. Direct by Definition 5.1 and Proposition A.6. \square

Lemma 5.4 states that, in asynchronous session behaviours, orphan messages are preserved not only after individual τ -moves (as ensured by Proposition 5.2), but even after synchronisation.

Lemma 5.4. Let $T[\sigma] \parallel U[\rho]$ be an orphan message configuration, with $!a$ orphan message of $T[\sigma]$. Then, $T[\sigma] \parallel U[\rho] \Rightarrow T'[\sigma'] \parallel U'[\rho']$ implies that $T'[\sigma'] \parallel U'[\rho']$ is an orphan message configuration, with $!a$ orphan message of $T'[\sigma']$. Furthermore, $\sigma' = \sigma.\sigma''$ (for some σ'').

The following lemma states that I/O compliance does not hold in orphan message configurations.

Lemma 5.5. If $p \parallel q$ is an orphan message configuration for p , then $p \not\stackrel{?a}{\Rightarrow} q$.

Proof. By Definition 5.1, we have $p \stackrel{!a}{\Rightarrow}$ and $q \not\stackrel{?a}{\Rightarrow}$: this implies $!a \in p\Downarrow^! \not\subseteq q\Downarrow^?$, which violates clause a of Definition 4.4. Therefore, $p \not\stackrel{?a}{\Rightarrow} q$. \square

Example 5.6. The inverse implication of Lemma 5.5 does not hold in general. E.g., let $P = !a + \tau.0$. We have $P \not\stackrel{?a}{\Rightarrow} 0$, because $!a \in P\Downarrow^! \not\subseteq \text{co}(0\Downarrow^?) = \emptyset$. However, $P \parallel 0$ is not an orphan message configuration, because $P \stackrel{!a}{\Rightarrow}$.

5.2 Unspecified reception

In CFSMs, *unspecified receptions* are configurations where some machine can only perform receptions, but the heads of buffers do not match the required input. In

our setting, we say that a parallel composition $p \parallel q$ is an unspecified reception when p can only interact via input transitions, but q is not going to offer any matching output. Before formalising this in Definition 5.10, we study the auxiliary notion of *input behaviours*.

Definition 5.7 (Input behaviour). We say that p is an input behaviour (written $p \stackrel{??}{\Rightarrow}$) whenever:

$$p\Downarrow^! = \emptyset \quad \text{and} \quad p \Rightarrow p' \text{ implies } p'\Downarrow^? \neq \emptyset$$

Proposition 5.8 below states that an input behaviour can only τ -reduce to an input behaviour.

Proposition 5.8. If $p \stackrel{??}{\Rightarrow}$ and $p \Rightarrow p'$, then $p' \stackrel{??}{\Rightarrow}$.

Proof. From $p\Downarrow^! = \emptyset$ we have $p'\Downarrow^! = \emptyset$; moreover, since $\forall p'' . p \Rightarrow p''$ implies $p''\Downarrow^? \neq \emptyset$, and $p \Rightarrow p'$ (by hypothesis), we have $\forall p'' . p' \Rightarrow p''$ implies $p''\Downarrow^? \neq \emptyset$. Hence, by Definition 5.7, we conclude $p' \stackrel{??}{\Rightarrow}$. \square

In synchronous session types, input behaviours correspond to external choices; in *asynchronous* session types, they must also have an empty buffer.

Proposition 5.9. $T[\sigma] \stackrel{??}{\Rightarrow}$ iff $T \equiv \&_{i \in I} ?a_i.T_i$ with $I \neq \emptyset$, and $\sigma = \epsilon$.

Proof. Straightforward, by Definition 2.11. \square

We can now formalise unspecified reception.

Definition 5.10 (Unspecified reception configuration). We say that $p \parallel q$ is an unspecified reception configuration for p iff $p \stackrel{??}{\Rightarrow}$ and $\exists a . p \stackrel{?a}{\Rightarrow} \wedge q \stackrel{!a}{\Rightarrow}$.

Example 5.11. We have that:

- $\text{rec}_X !a.X[] \parallel ?b[]$ is an unspecified reception configuration for the RHS behaviour (and also an orphan message configuration for the LHS behaviour).¹
- $0[] \parallel ?b[]$ is an unspecified reception configuration for the RHS behaviour

¹ In [56, 42] orphan message configuration only contain final states, hence this configuration would not be considered an orphan message in CFSMs.

- $?a[] \parallel ?b[]$ is an unspecified reception configuration for both LHS and RHS behaviours.

Proposition 5.12 shows that unspecified reception is preserved by τ -transitions.

Proposition 5.12. *Let $p \parallel q$ be an unspecified reception configuration for p . If $p \parallel q \Rightarrow p' \parallel q'$ then $p' \parallel q'$ is an unspecified reception configuration for p' .*

Proof. Note that, by Definition 5.10, p and q cannot synchronise, because no output of q matches p 's inputs, — and p has no weakly reachable outputs. Therefore, the τ transitions along $p \parallel q \Rightarrow p' \parallel q'$ can only be originated by internal moves. Now, since $p \Rightarrow p'$, by Proposition 5.8 we have $p' \xrightarrow{??}$; furthermore, since $\nexists a.p \xrightarrow{?a} \wedge q \xrightarrow{!a}$, considering that $p' \Downarrow^? \subseteq p \Downarrow^?$ and $q' \Downarrow^! \subseteq q \Downarrow^!$ (because $q \Rightarrow q'$), we obtain $\nexists a.p' \xrightarrow{?a} \wedge q' \xrightarrow{!a}$. \square

Lemma 5.13 syntactically characterises unspecified reception configurations for asynchronous session types (similarly to Lemma 5.3 for orphan messages).

Lemma 5.13. *$T[\sigma] \parallel U[\rho]$ is an unspecified reception configuration for $T[\sigma]$ iff $T \equiv \&_{i \in I} ?a_i.T_i$ with $I \neq \emptyset$, $\sigma = \epsilon$ and one of the following holds:*

- $\rho = !b.p'$, for some p' and b such that $\forall i \in I. a_i \neq b$;
- $\rho = \epsilon$ and $U \equiv \bigoplus_{j \in J} !b_j.U_j$, where $\forall i \in I, j \in J. a_i \neq b_j$;
- $\rho = \epsilon$ and $U \equiv \&_{j \in J} ?b_j.U_j$.

Proof. The \Rightarrow direction follows from Proposition 5.9 and Definition 2.11, considering that, by Definition 5.10:

- since $T[\sigma] \xrightarrow{??}$, by Proposition 5.9 T can only be equivalent to a non-empty external choice, and $\sigma = \epsilon$;
- $U[\rho]$ does not offer any output matching T 's inputs: hence, either U is equivalent to a (possibly empty) external choice and $\rho = \epsilon$ (case *c.* in the statement), or ρ 's head exposes an unmatching output — either immediately (case *a.*) or after a τ -move (case *b.*).

The \Leftarrow direction follows from Definition 2.11: in all cases *a.–c.* of the statement, we conclude that $T[\sigma] \parallel U[\rho]$ is an unspecified reception configuration. \square

Lemma 5.14 shows that two behaviours in an unspecified reception configuration are not I/O compliant.

Lemma 5.14. *If $p \parallel q$ is an unspecified reception configuration for p , then $p \not\bowtie q$.*

Proof. By Definition 5.10 we have $p \xrightarrow{??}$ and $\nexists a.p \xrightarrow{?a} \wedge q \xrightarrow{!a}$. Then, by Definition 5.7 we have $p \Downarrow^! = \emptyset$ and $p \Downarrow^? \neq \emptyset$. Furthermore, $q \Downarrow^! \cap \text{co}(p \Downarrow^?) = \emptyset$: this violates clause *a* of Definition 4.4. Therefore, we have $p \not\bowtie q$. \square

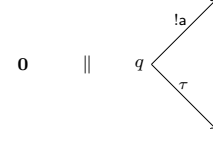


Figure 4: The behaviour $\mathbf{0} \parallel q$ is safe, but $\mathbf{0}$ and q are not I/O compliant.

5.3 Safety: definition and main properties

Definition 5.15 (Safety). *We say that $p \parallel q$ is safe iff $p \parallel q \Rightarrow p' \parallel q'$ implies that $p' \parallel q'$ is neither an orphan message nor an unspecified reception configuration.*

We relate safety with progress and I/O compliance.

Proposition 5.16. *If $p \parallel q$ is safe, then $p \dashv\vdash q$.*

Proof. We prove the contrapositive. Assume that $p \not\dashv\vdash q$. By definition of progress, there exist p' and q' such that $p \parallel q \Rightarrow p' \parallel q' \xrightarrow{\tau}$ and $p' \neq \mathbf{0}$ or $q' \neq \mathbf{0}$. Therefore:

$$p' \parallel q' \rightarrow \quad \text{and} \quad p' \xrightarrow{\tau} \quad \text{and} \quad q' \xrightarrow{\tau} \quad (1)$$

Since $p' \parallel q' \rightarrow$, at least one of the behaviours p', q' must take a move, which cannot be labelled τ . Without loss of generality, assume that p' moves. There are two possible (not mutually exclusive) cases:

- $p' \xrightarrow{!a}$. Since $p' \xrightarrow{\tau}$, then by Definition 2.5 it must be $p' \xrightarrow{!a}$. Now, by (1) it follows that $q' \xrightarrow{?a}$, otherwise p' and q' would synchronise. Since $q' \xrightarrow{\tau}$, this implies that $q' \xrightarrow{?a}$. By Definition 5.1, we obtain that $p' \parallel q'$ is an orphan message configuration, hence by Definition 5.15 we conclude that $p \parallel q$ is not safe.
- $p' \xrightarrow{?a}$. Since $p' \xrightarrow{\tau}$, then by Definition 5.7 it must be $p' \xrightarrow{?a}$. Now, by (1) it follows that $\nexists b : p' \xrightarrow{?b} \wedge q' \xrightarrow{!b}$, otherwise p' and q' would synchronise. By Definition 5.10, we obtain that $p' \parallel q'$ is an unspecified reception configuration, hence by Definition 5.15 we conclude that $p \parallel q$ is not safe. \square

Theorem 5.17. *If $p \bowtie q$, then $p \parallel q$ is safe.*

Proof. Let $p \parallel q \Rightarrow p' \parallel q'$. By Lemmas 4.5 and 5.5, $p' \parallel q'$ is not an orphan message configuration. By Lemmas 4.5 and 5.14, $p' \parallel q'$ is not an unspecified reception configuration. Hence, by Definition 5.15, $p \parallel q$ is safe. \square

The following example shows that the converse of Theorem 5.17 does not hold in general.

Example 5.18. *The behaviour $\mathbf{0} \parallel q$ in Figure 4 is safe:*

- neither $\mathbf{0}$ nor q have persistent outputs, hence $\mathbf{0} \parallel q$ has no orphan messages;

- neither $\mathbf{0}$ nor q is an input state, hence $\mathbf{0} \parallel q$ is not an unspecified reception configuration.

Note however that $\mathbf{0} \not\bowtie q$. Indeed, $!a \in q\Downarrow^1 \not\subseteq \text{co}(\mathbf{0}\Downarrow^2) = \emptyset$, and therefore item **a** of Definition 4.4 is false.

The following theorem extends Theorem 4.13 to the notion of safety. Namely, if the composition of two synchronous session behaviours is safe, then it is such also when passing to the asynchronous semantics.

Theorem 5.19. *If $T \parallel U$ is safe, then $T[] \parallel U[]$ is safe.*

Proof. If $T \parallel U$ is safe, then by Proposition 5.16 $T \dashv U$, and so by item (b) of Theorem 4.9 we also have $T \bowtie U$. By Theorem 4.13, $T[] \bowtie U[]$, and so by Theorem 5.17 we conclude that $T[] \parallel U[]$ is safe. \square

Theorem 5.20 shows that the converse of Theorem 5.17 holds for asynchronous session behaviours. Summing up, safety and I/O compliance coincide in the setting of asynchronous session types.

Theorem 5.20. *If $T[\sigma] \parallel U[\rho]$ is safe, then $T[\sigma] \bowtie U[\rho]$.*

Proof. We prove the contrapositive. Assume $T[\sigma] \not\bowtie U[\rho]$. By Definition 4.4, this implies that there exist T', U', σ', ρ' such that $T[\sigma] \parallel U[\rho] \Rightarrow T'[\sigma'] \parallel U'[\rho']$ and item **a** is false for $p = T'[\sigma']$ and $q = U'[\rho']$. There are the following two cases:

1. $p\Downarrow^1 \not\subseteq \text{co}(q\Downarrow^2)$. Then, there exists some a such that $!a \in p\Downarrow^1$ and $?a \notin q\Downarrow^2$. Since $!a \in p\Downarrow^1$, one of the following two cases must hold:
 - (i) $\sigma' = !a.\sigma''$. By Proposition A.6 it follows that $T'[\sigma'] \stackrel{!a}{\Rightarrow} T''[\sigma'']$. Since $?a \notin q\Downarrow^2$, we have that $U'[\rho'] \not\stackrel{?a}{\Rightarrow} U''[\rho'']$. Therefore, by Lemma 5.3 we obtain that $T''[\sigma''] \parallel U''[\rho'']$ is an orphan message configuration. By Definition 5.15, $T[\sigma] \parallel U[\rho]$ is not safe.
 - (ii) $\sigma' = \epsilon$ and $T' \equiv !a.T'' \oplus T'''$. By Definition 2.11 we have the transition:

$$T'[\epsilon] \parallel U'[\rho'] \xrightarrow{\tau} T''[!a] \parallel U'[\rho']$$

and we fall back to the previous case.

2. $((p\Downarrow^1 = \emptyset \wedge p\Downarrow^2 \neq \emptyset) \not\Rightarrow \emptyset \neq q\Downarrow^1 \subseteq \text{co}(p\Downarrow^2))$. Then, $p\Downarrow^1 = \emptyset \wedge p\Downarrow^2 \neq \emptyset$. This implies that T' is equivalent to an external choice, and that $\sigma' = \epsilon$. Further, one of the following two cases must hold:
 - $q\Downarrow^1 = \emptyset$. This implies that $\rho' = \epsilon$, and U' is equivalent to an external choice (possibly empty). By item **c**. of Lemma 5.13, it follows that $T'[\sigma'] \parallel U'[\rho']$ is an unspecified reception configuration. By Definition 5.15, $T[\sigma] \parallel U[\rho]$ is not safe.
 - there exists some a such that $!a \in q\Downarrow^1$ and $?a \notin p\Downarrow^2$. Therefore, either one of the following sub-cases applies:

- $\rho' = !a.\rho''$. Then, by item **a**. of Lemma 5.13, it follows that $T'[\sigma'] \parallel U'[\rho']$ is an unspecified reception configuration. By Definition 5.15 we conclude that $T[\sigma] \parallel U[\rho]$ is not safe.
- $\rho' = \epsilon$ and $U' \equiv !a.U'' \oplus U'''$. By Definition 2.11 we have the transition:

$$T'[\epsilon] \parallel U'[\epsilon] \xrightarrow{\tau} T'[\epsilon] \parallel U''[!a]$$

and we fall back to the previous case. \square

6 I/O simulation

We now introduce a preorder between behaviours that generalises the usual notions of session typing and subtyping, with the typical co/contra-variance of outputs and inputs. We define the relation in Section 6.1, and study its main properties in Sections 6.2 and 6.3 — notably, proving that it preserves I/O compliance. In Section 6.4, we study its preservation when passing to the asynchronous semantics.

We start by adapting to our framework a classical preorder from the literature on behavioural contracts: intuitively, p is a *subcontract* of q when p can replace q in all possible contexts while preserving I/O compliance².

Definition 6.1 (Subcontract relation). *We define the relation $\sqsubseteq_{\mathbb{R}}$ between behaviours as:*

$$p \sqsubseteq_{\mathbb{R}} q \quad \text{iff} \quad \forall r \in \mathbb{R}. q \bowtie r \text{ implies } p \bowtie r$$

We write $p \sqsubseteq q$ when $p \sqsubseteq_{\mathbb{R}} q$ with $\mathbb{R} = \mathbb{U}$.

The following lemma syntactically characterises the subcontract relation on synchronous session behaviours.

Lemma 6.2 ($\sqsubseteq_{\text{U}_{\text{ST}}}$ -induced shapes of session types). *$T \sqsubseteq_{\text{U}_{\text{ST}}} U$ implies either:*

- a. $T = U = \mathbf{0}$;
- b. $T \equiv \&_{k \in K} ?a_k.T_k$ and $U \equiv \&_{i \in I} ?a_i.T_i$, with $\emptyset \neq I \subseteq K$ and $\forall i \in I. T_i \sqsubseteq_{\text{U}_{\text{ST}}} U_i$;
- c. $T \equiv \oplus_{k \in K} !a_k.T_k$ and $U \equiv \oplus_{i \in I} !a_i.T_i$, with $\emptyset \neq K \subseteq I$, and $\forall k \in K. T_k \sqsubseteq_{\text{U}_{\text{ST}}} U_k$.

To determine if two synchronous session behaviours are in the subcontract relation (i.e., $T \sqsubseteq U$), we do *not* need to consider all possible behaviours $r \in \mathbb{U}$ such that $U \bowtie r$: indeed, we can restrict ourselves to the case where r is a synchronous session behaviour itself.

Lemma 6.3. $T \sqsubseteq U \iff T \sqsubseteq_{\text{U}_{\text{ST}}} U$.

² Note that the direction of \sqsubseteq is opposite w.r.t. the strong subcontract relation in [31]. The other difference between \sqsubseteq and the strong subcontract relation is that \sqsubseteq requires to preserve I/O compliance, while [31] requires progress.

6.1 I/O simulation: definition and examples

Despite its elegance, Definition 6.1 cannot be directly exploited to establish whether two behaviours are related, due to the universal quantification over all contexts. We aim at extending the syntax-based coinductive characterisation on session types (Lemma 6.2) to arbitrary behaviours, without resorting to a universal quantification over contexts. To do that, we define an *I/O simulation* relation on behaviours, denoted by $\dot{\leq}$. We show that it is a preorder (Theorem 6.10), and it preserves I/O compliance (Theorem 6.12). $\dot{\leq}$ is equivalent to the subtype relation on sync session behaviours (Theorem 6.13), albeit stricter on arbitrary behaviours.

Definition 6.4 (I/O simulation). *We say that $\ddot{\mathcal{R}}$ is an I/O simulation iff, whenever $p \ddot{\mathcal{R}} q$, then $\exists \mathbb{Q}$ (called predictive set) such that $q \Rightarrow \mathbb{Q}$, and:*

- $p \Downarrow^! = \emptyset$ implies $\mathbb{Q} \Downarrow^! = \emptyset$
- $\mathbb{Q} \Downarrow^{??} \subseteq p \Downarrow^? \wedge (\mathbb{Q} \Downarrow^? = \emptyset$ implies $p \Downarrow^? = \emptyset)$
- $p \xrightarrow{\tau} p'$ implies $\exists q' . \mathbb{Q} \Rightarrow q' \wedge p' \ddot{\mathcal{R}} q'$
- $p \xrightarrow{!a} p'$ implies $\exists q' . \mathbb{Q} \xrightarrow{!a} q' \wedge p' \ddot{\mathcal{R}} q'$
- $p \xrightarrow{?a} p' \wedge \mathbb{Q} \xrightarrow{??a} \text{ implies } \exists q' . \mathbb{Q} \xrightarrow{?a} q' \wedge p' \ddot{\mathcal{R}} q'.$

We write $\dot{\leq}$ for the largest I/O simulation, \approx for the largest symmetric I/O simulation, $\dot{\geq}$ for its inverse, and $\dot{=}$ for $\dot{\leq} \cap \dot{\geq}$.

Definition 6.4 generalises to our semantic setting the (syntax-based) coinductive subtyping rules for session types (Lemma 6.2). Similarly to Lemma 6.2, adding further external choices (inputs) or removing some internal choices (outputs) leads to a subtype. However, I/O simulation is more permissive than this, as it can relate general behaviours. These can have mixed choices, made of both inputs and outputs. Further, choices (possibly of inputs) are internal when led by τ transitions; dually, choices of outputs can be external. To cope with this complexity, in Definition 6.4 we use the predictive set \mathbb{Q} as a subset of the internal choices of q . Once this set is fixed, the simulation game occurs between p and \mathbb{Q} .

Example 6.5. *In Table 3 we exemplify $\dot{\leq}$. The first pair of behaviours correspond to the sync session types $T = ?a.!b \& ?c$ and $U = ?a.(!b \oplus !c)$. The other pairs deal with behaviours outside \mathbb{U}_{ST} . The third one corresponds to Alice's asynchronous type T''_A and her process P''_A , from Section 3: we show that the former I/O simulates the latter. The last one shows an I/O simulation where the predictive set for the pair $(p, q) \in \dot{\leq}$ contains more than one element.*

Example 6.6. *To establish whether, in Figure 5, $p \dot{\leq} q$, we choose a predictive set \mathbb{Q} that mandates the inputs of p , and includes its outputs (note that p has an additional*

input $?c'$ not offered by \mathbb{Q}). The same happens with the predictive set \mathbb{R} , establishing $q \dot{\leq} r$. Note that \mathbb{R} and the small set inside are also predictive sets for $p \dot{\leq} r$.

6.2 Basic properties

The following lemma ensures that the largest I/O simulation $\dot{\leq}$ indeed exists.

Lemma 6.7. *Let $\ddot{\mathcal{R}}$ be a set of I/O simulations. Then, $\bigcup \ddot{\mathcal{R}}$ is an I/O simulation.*

We now study preservation of I/O simulation under weak moves. When $p \dot{\leq} q$, the relation $\dot{\leq}$ is preserved by forward τ -moves of p and backward τ -moves of q .

Lemma 6.8. *If $p \dot{\leq} q$, $p \Rightarrow p'$ and $q' \Rightarrow q$, then $p' \dot{\leq} q'$.*

A consequence of Lemma 6.8 is that we can obtain a behaviour I/O simulated by p by reducing the internal non-determinism of p (i.e., by making p take τ moves).

Corollary 6.9. *If $p \Rightarrow p'$, then $p' \dot{\leq} p$.*

Proof. From reflexivity of $\dot{\leq}$ (Lemma D.1) we have $p \dot{\leq} p$. From Lemma 6.8 we conclude $p' \dot{\leq} p$. \square

Theorem 6.10 establishes that $\dot{\leq}$ is a preorder. The proof is not as straightforward as proving that \sqsubseteq is a preorder, because we have to deal with the existential quantification on the predictive set \mathbb{Q} . Note that constructing the set \mathbb{Q} is not always as easy as in Example 6.6, where the predictive set used for $q \dot{\leq} r$ was valid also for proving $p \dot{\leq} r$. Our general construction of \mathbb{Q} is detailed in the proof of Theorem 6.10.

Theorem 6.10. *$(\mathbb{U}, \dot{\leq})$ is a preorder.*

Weak simulation $\dot{\approx}$ and I/O simulation are unrelated, i.e. $\dot{\leq} \not\subseteq \dot{\approx}$ (see Proposition D.8). However, weak bisimulation \approx is strictly stronger than I/O bisimulation.

Theorem 6.11. $\approx \subseteq \dot{\approx}$

6.3 On I/O simulation and I/O compliance

Theorem 6.12 is one of our main results: it states that $\dot{\leq}$ preserves I/O compliance. Instead, $\dot{\leq}$ does not preserve progress \dashv . For instance, consider the behaviours in Figure 6. We have that $p_6 \dot{\leq} p_7$ and $p_7 \dashv p_8$. However, $p_6 \not\dashv p_8$: indeed, if p_8 chooses the branch $!b$, then p_6 is stuck waiting for $?c$.

Theorem 6.12. $p \dot{\leq} q \circ r \implies p \circ r$, for $\circ \in \{\dot{\dashv}, \dot{\bowtie}\}$.

	<table border="1"> <thead> <tr> <th>Relation</th> <th>Pred. set</th> </tr> </thead> <tbody> <tr> <td>(T, U)</td> <td>$\{U\}$</td> </tr> <tr> <td>$(T^{(1)}, U^{(1)})$</td> <td>$\{U^{(1)}\}$</td> </tr> <tr> <td>$(T^{(1)}, U^{(2)})$</td> <td>$\{U^{(2)}\}$</td> </tr> <tr> <td>$(T^{(2)}, U^{(4)})$</td> <td>$\{U^{(4)}\}$</td> </tr> </tbody> </table>	Relation	Pred. set	(T, U)	$\{U\}$	$(T^{(1)}, U^{(1)})$	$\{U^{(1)}\}$	$(T^{(1)}, U^{(2)})$	$\{U^{(2)}\}$	$(T^{(2)}, U^{(4)})$	$\{U^{(4)}\}$	<p>$T^{(1)}$ is in relation with $U^{(1)}, U^{(2)}$: both the latter match the outputs of the first. $T^{(3)}$ is <i>not</i> in the relation: it is reached via an input ?c unmatched by U. $U^{(3)}, U^{(5)}$ are <i>not</i> in the relation: they are reached via a τ and an output !c unmatched by T.</p>																
Relation	Pred. set																											
(T, U)	$\{U\}$																											
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Relation	Pred. set																											
(p, q)	$\{q\}$																											
$(p, q^{(3)})$	$\{q^{(3)}\}$																											
$(p^{(1)}, q^{(1)})$	$\{q^{(1)}\}$																											
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$(p^{(2)}, q^{(4)})$	$\{q^{(4)}\}$																											
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Relation	Pred. set																											
$(P''_A \square, T''_A \square)$	$\{T''_A^{(1)}\}$																											
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$(P''_A^{(3)}, T''_A^{(3)})$	$\{T''_A^{(3)}\}$																											
$(P''_A^{(4)}, T''_A^{(4)})$	$\{T''_A^{(4)}\}$																											
$(P''_A^{(5)}, T''_A^{(5)})$	$\{T''_A^{(5)}\}$																											
$(P''_A^{(6)}, T''_A^{(6)})$	$\{T''_A^{(6)}\}$																											
$(P''_A^{(7)}, T''_A^{(7)})$	$\{T''_A^{(7)}\}$																											
$(P''_A^{(8)}, T''_A^{(3)})$	$\{T''_A^{(3)}\}$																											
$(P''_A^{(9)}, T''_A^{(6)})$	$\{T''_A^{(6)}\}$																											
$(P''_A^{(10)}, T''_A^{(6)})$	$\{T''_A^{(6)}\}$																											
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	<table border="1"> <thead> <tr> <th>Relation</th> <th>Pred. set</th> </tr> </thead> <tbody> <tr> <td>(p, q)</td> <td>$\{q^{(01)}, q^{(02)}\}$</td> </tr> <tr> <td>$(p^{(1)}, q^{(1)})$</td> <td>$\{q^{(1)}\}$</td> </tr> <tr> <td>$(p^{(2)}, q^{(2)})$</td> <td>$\{q^{(2)}\}$</td> </tr> <tr> <td>$(p^{(3)}, q^{(3)})$</td> <td>$\{q^{(3)}\}$</td> </tr> <tr> <td>$(p^{(4)}, q^{(4)})$</td> <td>$\{q^{(4)}\}$</td> </tr> </tbody> </table>	Relation	Pred. set	(p, q)	$\{q^{(01)}, q^{(02)}\}$	$(p^{(1)}, q^{(1)})$	$\{q^{(1)}\}$	$(p^{(2)}, q^{(2)})$	$\{q^{(2)}\}$	$(p^{(3)}, q^{(3)})$	$\{q^{(3)}\}$	$(p^{(4)}, q^{(4)})$	$\{q^{(4)}\}$	<p>p is related with q, with a predictive set containing 2 elements. This is the only possible predictive set supporting the relation: in fact, if the predictive set included q or $q^{(5)}$, then p would be forced to offer some output (by clause a of Definition 6.4); moreover, if the predictive set did <i>not</i> contain $q^{(01)}$, then the ?a-branch of q would need to match the ?a-branch of $q^{(02)}$ (by clause e of Definition 6.4) — but in this case we would need $(p^{(1)}, q^{(31)}) \in \dot{\leq}$, which is false. A similar problem would arise if the predictive set did not contain $q^{(02)}$.</p>														
Relation	Pred. set																											
(p, q)	$\{q^{(01)}, q^{(02)}\}$																											
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Table 3: Examples of I/O simulation. For each pair of behaviours on the left, the table shows an I/O simulation relation, and the predictive sets supporting each pair of related states.

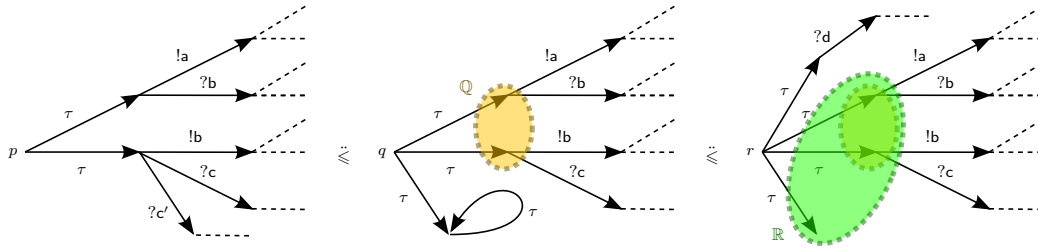


Figure 5: I/O simulation. \mathbb{Q} , \mathbb{R} are the predictive sets resp. for $p \dot{\leq} q$ and $q \ddot{\leq} r$.

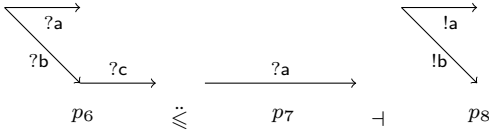


Figure 6: Progress is not preserved by I/O simulation (on general behaviours).

Theorem 6.13 below establishes that I/O simulation can be seen as a subtyping/subcontract relation on general behaviours, that is $p \dot{\leq} q$ allows p to be always used in place of q . For instance, assume that p is an asynchronous CCS process typed with a session type q , which in turn complies with the session type r . Then, Theorem 6.12 states that I/O compliance is preserved by $\dot{\leq}$, i.e. p is also I/O compliant with r , notwithstanding with the fact that p and r are specified in different calculi. Although I/O simulation is stricter than the subcontract relation on general behaviours, the two relations coincide on synchronous session behaviours, hence $\dot{\leq}$ can be interpreted as the subtyping relation in [44] (albeit in the inverse direction, corresponding to that adopted e.g. in [27]).

Theorem 6.13. (a) $\dot{\leq} \subseteq \sqsubseteq$. (b) $T \dot{\leq} U$ iff $T \sqsubseteq U$.

Theorem 6.12 above does *not* generally hold for $\ddot{\leq}$. Let $p \dot{\leq} q \ddot{\leq} r$: intuitively, $\ddot{\leq}$ does *not* guarantee that r 's outputs will be matched by q 's inputs; therefore, if p adds an input branch which is matched by an output of r , then their synchronisation may reduce to *non-compliant* behaviours, as shown in Example 6.14 below.

Example 6.14 (On $\dot{\leq}$ and $\ddot{\leq}$). *The behaviours p_{10} and p_{11} in Figure 7 are $\ddot{\leq}$ compliant. Indeed, even though p_{11} offers an additional $!c$ output, this is immaterial for I/O compliance, since it only checks that the $!a$ output of p_{10} is received by a corresponding $?a$ of p_{11} . If we add a $?c$ branch to p_{10} , e.g. as in p_9 , by $\dot{\leq}$ we obtain a behaviour which is simulated by p_{10} . However p_9 and p_{11} are not I/O compliant, since the c -branches now can synchronise, leading to a stuck output $!e$.*

The situation described in Example 6.14 does *not* arise on synchronous session behaviours, as established

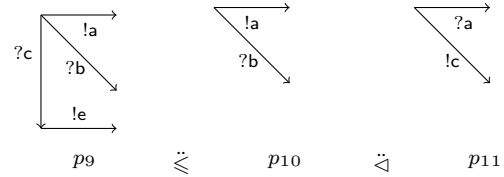


Figure 7: $\ddot{\leq}$ is not preserved by I/O simulation (on general behaviours).

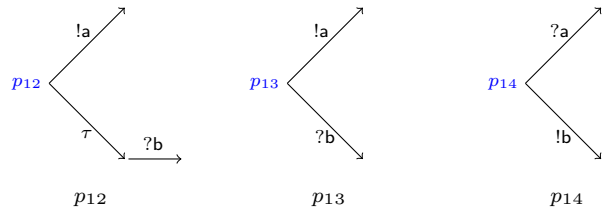


Figure 8: Three compliant-admitting behaviours showing that $\dot{\leq} \subseteq \ddot{\leq}$.

in Lemma 6.15 below, which extends Theorem 6.12. Its proof exploits the fact that synchronous session behaviours do not have *mixed choices*.

Lemma 6.15. $T \dot{\leq} U \ddot{\leq} V$ implies $T \ddot{\leq} V$.

6.4 I/O simulation and asynchrony

We now address the problem of establishing whether I/O simulation and the subcontract relation are preserved by buffering. That is, if $T \dot{\leq} U$, (resp. $T \sqsubseteq U$), is it the case that $T[] \dot{\leq} U[]$ ($T[] \sqsubseteq U[]$)? Were the answer positive, we could reason about I/O simulation/subcontracting in the (finite-state) synchronous semantics, and then transfer these results to the asynchronous setting, where both I/O simulation and subcontracting are undecidable.

Unfortunately, the answer to both questions is negative, as shown in the following examples.

Example 6.16. *Consider the following session types:*

$$T = \text{rec}_X !a.X \quad U = \text{rec}_Y !a.X \oplus !b.?c$$

We have $T \dot{\leq} U$, but $T[] \not\dot{\leq} U[]$. To prove it, assume by contradiction that $T[] \dot{\leq} U[]$, with some predictive set \mathbb{U} .

Since $U \Downarrow \stackrel{??c}{\Rightarrow}$, we must have $U \stackrel{??c}{\Rightarrow}$ (see Proposition D.6). Then, by the first part of clause **b** in Definition 6.4, it must be $?c \in T \Downarrow \stackrel{?}{=} \emptyset$ — contradiction.

Example 6.17. Let T, U as in Example 6.16, from which we know that $T \stackrel{\leq}{\leq} U$: hence, by item (a) of Theorem 6.13 we also know that $T \sqsubseteq U$. We now prove that $T \Downarrow \not\sqsubseteq U \Downarrow$. To this purpose, define:

$$V = !c.\text{rec}_Y ?a.Y \& ?b$$

Note that $U \Downarrow \bowtie V \Downarrow$ (albeit $U \bowtie V$). However, $T \Downarrow \not\bowtie V \Downarrow$, since clause **a** of Definition 4.4 is violated because $V \Downarrow \stackrel{!c}{\Rightarrow}$ but $T \Downarrow \not\stackrel{?c}{\Rightarrow}$ ³. Therefore, $T \Downarrow \not\sqsubseteq U \Downarrow$.

Intuitively, the non-preservation of $\stackrel{\leq}{\leq}$ under buffering is due to the fact that, in the asynchronous semantics, output prefixes can always be fired. Consequently, the inputs that are guarded by a sequence of outputs in a session type T may become weakly persistent (as per Definition 2.5), and thus relevant for clauses **b** and **e** of Definition 6.4. Similarly, \sqsubseteq is not preserved because output-guarded inputs may become weakly reachable in $T[\sigma]$ and thus relevant for clause **a** of Definition 4.4.

Following this intuition, we provide in Definition 6.18 below a sufficient condition which guarantees the preservation of $\stackrel{\leq}{\leq}$ when passing from the synchronous to the asynchronous semantics of session types.

Definition 6.18 (Input-preserving behaviours). We say that p is input-preserving w.r.t. q (in symbols $p \preceq_{?} q$) iff for all $w = \ell_1, \dots, \ell_n, p', q', a$, whenever

$$p \stackrel{w}{\Rightarrow} p' \text{ and } q \stackrel{w}{\Rightarrow} q' \quad (2a)$$

$$\text{and } \left(\forall q'' . q' \stackrel{!}{\Rightarrow}^* q'' \text{ implies } q'' \stackrel{!}{\Rightarrow}^* \stackrel{?a}{\rightarrow} \right), \quad (2b)$$

$$\text{then } p' \stackrel{!}{\Rightarrow}^* \stackrel{?a}{\rightarrow}. \quad (2c)$$

Intuitively, $p \preceq_{?} q$ requires that, whenever p and q reach some states p', q' through the *same* weak transitions and $?a$ is persistent in q' (possibly after some outputs), then it must also be reachable in p' (possibly after some outputs).

Example 6.19. Consider T and U from Example 6.16. We have that:

- a. $T \not\preceq_{?} U$
- b. $!a.!a.!b.?c \left(\stackrel{\leq}{\leq} \cap \preceq_{?} \right) U$
- c. $\text{rec}_X !a.?c'.X \left(\stackrel{\leq}{\leq} \cap \preceq_{?} \right) \text{rec}_Y !a.?c'.Y \oplus !b.?c$
- d. $!a.?c \preceq_{?} !b.?c' \oplus !b'$ (while $\stackrel{\leq}{\leq}$ does not hold).

When the relation $\preceq_{?}$ holds, both I/O simulation and the subcontract relation are preserved by buffering.

³ Note also that $T \Downarrow \parallel V \Downarrow$ is *not* safe: in fact, according to Definition 5.1, it is an orphan message configuration, with $!c$ orphan message of $V \Downarrow$.

Theorem 6.20. If $T (\circ \cap \preceq_{?}) U$ then $T \Downarrow \circ U \Downarrow$, for $\circ \in \{\stackrel{\leq}{\leq}, \sqsubseteq\}$.

Proof. The proof for $\circ = \stackrel{\leq}{\leq}$ exploits four key properties:

1. whenever $p \stackrel{\leq}{\leq} q$, each weak sequence of outputs of p is I/O simulated by q (Proposition D.12);
2. whenever $p \preceq_{?} q$, and the two behaviours reach p', q' through the same weak transitions, then $p' \preceq_{?} q'$ (Proposition D.13);
3. each weak sequence of outputs from a session behaviour T corresponds to a sequence of τ s in its asynchronous version $T[\sigma]$, for all σ (Proposition D.14);
4. *vice versa*, each sequence of τ s from $T[\sigma]$ corresponds to a weak sequence of outputs from T (Proposition D.15).

To prove $\circ = \sqsubseteq$ we exploit the previous case and Theorem 6.13. See Appendix D.3 for full details. \square

We now show how the three main notions studied so far (I/O simulation, I/O compliance and safety) can be combined in order to determine that two processes can safely interact. Assume that $T \stackrel{\leq}{\leq} U$ and $U \bowtie V$. Using Theorem 6.12 we can deduce $T \bowtie V$; then, from Lemma 4.12 we obtain $T \Downarrow \bowtie V \Downarrow$, and so by Theorem 5.17, we conclude that $T \Downarrow \parallel V \Downarrow$ is safe.

Corollary 6.21. If $T \stackrel{\leq}{\leq} U \bowtie V$, then $T \Downarrow \parallel V \Downarrow$ is safe.

7 Session types without types

In this section we show how typing rules can arise by applying I/O simulation on specific process and type languages — in our case, (a)sync CCS and session types.

7.1 From semantics to syntax

Our treatment so far does not depend on a syntactic representation of behaviours in \mathbb{U} . In the resulting unifying view, there are no inherent distinctions between processes and types: they are just behaviours in an LTS. This allows us to define relations between objects which belong to different realms: e.g. $p \stackrel{\leq}{\leq} q$ may relate, say, an async CCS process with a session type.

The price for this generalisation seems to be the loss of compositional type systems, which assign types to processes by only reasoning on their syntax. However, we can restore this feature in our framework as follows:

1. choosing a process language and a type language (with their syntax and semantics);
2. encoding types into processes;
3. devising a set of properties for $\stackrel{\leq}{\leq}$ on processes, and interpreting them as syntax-based typing rules.

$$\begin{array}{l}
P \approx P + \mathbf{0} \\
P + Q \approx Q + P \\
P + (Q + R) \approx (P + Q) + R \\
P \approx P + P
\end{array}
\quad
\begin{array}{l}
P \approx P | \mathbf{0} \\
P | Q \approx Q | P \\
P | (Q | R) \approx (P | Q) | R
\end{array}$$

Table 4: Some properties of \approx in \mathbb{U}_{aCCS} (recall that \approx is the largest symmetric I/O simulation, by Definition 6.4).

We apply this methodology to a concrete case, where at step 1 we choose async CCS (\mathbb{U}_{aCCS}) for processes⁴, and async session behaviours (\mathbb{U}_{aST}) for types. For step 2, the encoding $[\cdot]$ from types to processes is just the one given in Definition 2.19. To improve readability, hereafter we shall sometimes omit the buffers $[\sigma]$ appearing in \mathbb{U}_{aCCS} processes. Moreover, when $P, Q \in \mathbb{U}_{\text{aCCS}}$, we will write $P \lesssim Q$ to mean $\forall \sigma. P[\sigma] \lesssim Q[\sigma]$.

For step 3, we aim at a \lesssim -based inductive relation for \mathbb{U}_{aCCS} . We start by selecting in Table 4 a few semantic equivalences between \mathbb{U}_{aCCS} behaviours.

Lemma 7.1. *The relations in Table 4 hold in \mathbb{U}_{aCCS} .*

We then interpret the semantic equivalences in Table 4 between \mathbb{U}_{aCCS} behaviours as a set of syntactic axioms for a relation \equiv between \mathbb{U}_{aCCS} terms⁵.

Definition 7.2. *We define \equiv as the least congruence relation on \mathbb{U}_{aCCS} satisfying the axioms in Table 4 (by replacing \approx with \equiv).*

As usual, \equiv embodies the commutative monoidal laws for $+$ and $|$ (with $\mathbf{0}$ as neutral element), and the absorption law for $+$.

We now follow a similar approach, by selecting in Table 5 a set of properties satisfied by \lesssim on \mathbb{U}_{aCCS} behaviours. We present these properties as a set of inference rules, whose correctness is established by Lemma 7.3. The intuition behind these rules is the following:

- (+CTX) says that \lesssim is a pre-congruence with respect to guarded choices;
- (+INT) and (+EXT) with $K = \emptyset$ correspond to the typical session typing rules for internal/external choices, which add inputs and remove outputs on the LHS;
- (+EXT) with $K \neq \emptyset$ handles an external choice with timeout (modelled as a τ action), similarly to the Erlang-style `receive...after...` behaviour discussed in Question 2;

⁴ The process calculi used in session types literature are typically variants of the π -calculus. In this work, we focus on the behavioural properties of structured first-order interaction; we thus focus on (async) CCS, abstracting orthogonal aspects such as value and channel passing (see also Section 8.1).

⁵ We overload the symbol \equiv , which was already introduced as a relation between session types in Definition A.1. The relation being used is always clear from the context.

$$\begin{array}{c}
\frac{\forall i \in I. P_i \lesssim Q_i}{\sum_{i \in I} \ell \tau_i. P_i \lesssim \sum_{i \in I} \ell \tau_i. Q_i} \text{ (+CTX)} \quad \frac{Q = \sum_{i \in I} !a_i. Q_i \quad \forall i \in I. P_i \lesssim Q_i \quad I \neq \emptyset}{\sum_{i \in I} !a_i. P_i \lesssim Q + !b. Q'} \text{ (+INT)} \\
\\
\frac{Q = \sum_{i \in I} ?a_i. Q_i \quad \forall i \in I. P_i \lesssim Q_i \quad \emptyset \neq I \subseteq J \quad \forall k \in K. P_k \lesssim Q \quad \forall j \in J \setminus I. a_j \notin \{a_i\}_{i \in I}}{\sum_{j \in J} (?a_j. P_j) + \sum_{k \in K} \tau. P_k \lesssim Q} \text{ (+EXT)} \\
\\
\frac{\forall i \in I. P_i \lesssim Q \quad P \lesssim Q \quad R \lesssim S \quad \text{ins}(P) \cap \text{ins}(R) = \emptyset \quad \text{ins}(Q) \cap \text{ins}(S) = \emptyset}}{\sum_{i \in I} \tau. P_i \lesssim Q \quad P | R \lesssim Q | S} \text{ (LR)} \\
\\
\frac{Q = \sum_{i \in I} !c_i. Q_i \quad P' \lesssim !a. P'' \lesssim ?b}{P' | P'' \lesssim !a. ?b + Q} \text{ (L)} \quad \frac{P \lesssim ?b. Q \quad R = \sum_{i \in I} !c_i. R_i}{!a. P \lesssim !a + R | ?b. Q} \text{ (R)} \\
\\
\frac{P \equiv P' \quad P' \lesssim Q' \quad Q' \equiv Q}{P \lesssim Q} \text{ (CONGR)}
\end{array}$$

Table 5: Rules for \lesssim in \mathbb{U}_{aCCS} . We denote with $\text{ins}(P)$ the set of inputs appearing in the body of P .

- (+ τ) allows the LHS to aggregate internal choices, provided that all of them are I/O simulated by Q ;
- (LR) states that \lesssim is almost a pre-congruence w.r.t. parallel composition — but further requires that the inputs of the parallel components do not overlap (and thus, cannot compete for synchronising on the same output);
- (L) exploits the asynchronicity of \mathbb{U}_{aCCS} , by allowing an output and an input in parallel on the LHS, when they are sequentially composed in the RHS;
- (R) is roughly symmetrical, by allowing the LHS to be the sequential composition $!a.P$ when $!a$ and $?b$ are in parallel on the RHS.

Lemma 7.3. *The rules in Table 5 hold in \mathbb{U}_{aCCS} .*

As prescribed by step 3, we now interpret the properties in Table 5 as syntax-based typing rules for \mathbb{U}_{aCCS} .

Definition 7.4. *We define $\ddot{\lesssim}$ as the least relation between \mathbb{U}_{aCCS} terms satisfying the rules in Table 5.*

Example 7.5. *From Section 3, recall Alice's type T''_A and process P''_A when she is late for work.*

$$T''_A = !a\text{Coffee}.!pay.?coffee \quad P''_A = !a\text{Coffee}.(?coffee | !pay)$$

We have the following type encoding in CCS:

$$[[T''_A]] = !a\text{Coffee}.!pay.?coffee$$

We have the following typing derivation:

$$\begin{array}{c}
\frac{\mathbf{0} \ddot{\lesssim} [\mathbf{0}]}{!pay \ddot{\lesssim} [!pay]} \text{ (+CTX)} \quad \frac{\mathbf{0} \ddot{\lesssim} [\mathbf{0}]}{?coffee \ddot{\lesssim} [?coffee]} \text{ (+CTX)} \quad \frac{\mathbf{0} \equiv \sum_{i \in \emptyset} !c_i. Q_i}{?coffee | !pay \equiv !pay | ?coffee \ddot{\lesssim} [!pay.?coffee] + \mathbf{0} \equiv [!pay.?coffee]} \text{ (L)} \\
\frac{?coffee | !pay \equiv !pay | ?coffee \ddot{\lesssim} [!pay.?coffee] + \mathbf{0} \equiv [!pay.?coffee]}{!a\text{Coffee}.(?coffee | !pay) \ddot{\lesssim} [[T''_A]]} \text{ (CONGR)} \text{ (+CTX)}
\end{array}$$

7.2 Handling recursion

The typing rules introduced so far can be applied to all \mathbb{U}_{aCCS} terms (including those involving recursion), yet no rule allows to operate *under* recursion. In this section we show how to overcome this limitation. E.g., consider:

$$P = \mu_X!a.X \quad Q = \mu_X!a.X + !b \quad (3)$$

If we add the axiom $X \overset{\cdot}{\approx} X$ (based on $X \overset{\cdot}{\leq} X$, that holds since X has no transitions), by applying rule (+INT) we would obtain $!a.X \overset{\cdot}{\approx} !a.X + !b$; from this, we would like to deduce $P \overset{\cdot}{\approx} Q$ — coherently with the fact that $P \overset{\cdot}{\leq} Q$ holds. By considering this example, it would be tempting to also add a typing rule which allows to deduce $\mu_X P' \overset{\cdot}{\approx} \mu_X Q'$ whenever $P' \overset{\cdot}{\approx} Q'$. However, in the general case, this would be unsound. For instance:

$$P = \mu_X!a.X \quad R = \mu_X!a.X + !b.?c \quad (4)$$

would be a counterexample. Indeed, using the proposed axiom and rule, together with (+INT), we would obtain $P \overset{\cdot}{\approx} R$ — yet we do *not* have $P \overset{\cdot}{\leq} R$. Recall that we chose to operate in the asynchronous setting, and we have $R[\sigma] \xrightarrow{??\xi}$ but $P[\sigma] \not\xrightarrow{??\xi}$.

To rule out these problematic cases, we restrict our syntax-based typing to CCS^- , a fragment of async CCS which is expressive enough for the examples in Section 3.

Definition 7.6 (CCS^-). *CCS⁻ terms have the syntax:*

$$P, Q ::= \sum_{i \in I} \ell_{\tau_i} \cdot P_i \quad | \quad P | Q \quad | \quad X \quad | \quad \mu_X P$$

where we stipulate that:

- in $\mu_X P$, each X is guarded in some subterm $\ell.X$;
- if a choice $\sum_{i \in I} P_i$ appears under μ_X , and some P_i contains an input, then each X occurs after an input.

The semantics of CCS^- is inherited from that of CCS. We denote by $\mathbb{U}_{\text{aCCS}}^-$ the resulting async behaviours. CCS^- processes not containing $|$ are said sequential. Unless otherwise specified, hereafter we will use P, Q, R, S to denote CCS^- terms, only. We will also adopt the Barendregt convention [7, §2.1.13] that no bound X can occur free or in two different bindings.

The issue highlighted in Equation (4) is solved, because R does not satisfy condition *b*. Note that, e.g., both Q in Equation (3) and $Q' = \mu_X!a.\mathbf{0} + !b.?c.!d.X$ are CCS^- terms. Indeed, in Q' the branch without inputs is non-recursive, while X occurs after an input. The guardedness condition for recursion variables (clause *a*) comes from our original CCS fragment.

Lemma 7.7 provides an induction principle for $\overset{\cdot}{\leq}$.

Lemma 7.7. *Let $\mu_X P$ and Q be sequential CCS^- terms. If $P[Q/X] \overset{\cdot}{\leq} Q$, then $\mu_X P \overset{\cdot}{\leq} Q$.*

Intuitively, Lemma 7.7 is reminiscent of the standard argument in domain theory, stating that if x is a prefixed point of f , i.e. $f(x) \sqsubseteq x$, then $\text{fix } f \sqsubseteq x$.

We now extend the type system $\overset{\cdot}{\approx}$ of Section 7.1 to deal with recursive CCS^- terms.

Definition 7.8. *Let Γ be a mapping from recursion variables to CCS^- terms. Then, $\overset{\cdot}{\approx}_{\Gamma}$ is the relation between CCS^- terms inductively defined by the rules obtained by replacing $\overset{\cdot}{\leq}$ with $\overset{\cdot}{\approx}_{\Gamma}$ in Table 5, and by the following additional rules:*

$$\frac{\Gamma(X) = Q}{X \overset{\cdot}{\approx}_{\Gamma} Q} \text{ (S-VAR)}$$

$$\frac{P \overset{\cdot}{\approx}_{\Gamma, X:Q} Q \quad P, Q \text{ sequential}}{\mu_X P \overset{\cdot}{\approx}_{\Gamma} Q} \text{ (S-}\mu\text{L)} \quad \frac{P \overset{\cdot}{\approx}_{\Gamma} Q[\mu_X Q/X]}{P \overset{\cdot}{\approx}_{\Gamma} \mu_X Q} \text{ (S-}\mu\text{R)}$$

We will often write $P \overset{\cdot}{\approx} Q$ instead of $P \overset{\cdot}{\approx}_{\emptyset} Q$.

The rules in Definition 7.8 are mostly straightforward:

- (S-VAR) uses the environment, requiring the hypothesis that X expands into a behaviour I/O simulated by Q ;
- (S- μ L) consumes such an hypothesis, introducing recursion on the LHS: the intuition is that, in the rule premise, the LHS is I/O simulated by the RHS when Q replaces X in P 's body, similarly to the premise of Lemma 7.7;
- (S- μ R) allows to unfold a recursion on the RHS, when going upwards in a derivation.

The relation $\overset{\cdot}{\approx}$ satisfies the following weakening property.

Proposition 7.9 (Weakening for $\overset{\cdot}{\approx}$). *For all Γ such that $X \notin \text{dom}(\Gamma)$, $P \overset{\cdot}{\approx}_{\Gamma} Q$ implies $P \overset{\cdot}{\approx}_{\Gamma, X:R} Q$.*

Proof. By induction on the derivation of $P \overset{\cdot}{\approx}_{\Gamma} Q$, the result is immediate on all rules. \square

7.3 An I/O simulation-based type system

We can now show that the syntactic rules for $\overset{\cdot}{\approx}_{\Gamma}$ can be a basis for a type system for $\mathbb{U}_{\text{aCCS}}^-$. The correctness of $\overset{\cdot}{\approx}$ w.r.t. $\overset{\cdot}{\leq}$ is formalised in Corollary 7.11 below; this, in turn, is based on Theorem 7.10, which shows how the environment in each derivation step is used to construct an intermediate I/O simulation relation.

Theorem 7.10. *If $P \overset{\cdot}{\approx}_{\Gamma} Q$, then $PT \overset{\cdot}{\leq} QT$.*

When $\Gamma = \emptyset$, Theorem 7.10 has the following corollary:

Corollary 7.11. *If $P \overset{\cdot}{\approx} Q$, then $P \overset{\cdot}{\leq} Q$.*

We can now define a *syntax-directed* typing judgement, relating CCS^- terms with session types. To this purpose, we exploit the encoding in Definition 2.19.

Definition 7.12 (Type system). *When $\llbracket T \rrbracket$ is a CCS^- term, we write $\Gamma \vdash P : T$ whenever $P \preceq_{\Gamma} \llbracket T \rrbracket$.*

The condition on $\llbracket T \rrbracket$ in Definition 7.12 is necessary because the encoding of recursive session types does not always respect condition *b* of Definition 7.6: see, for instance, $U = \text{rec}_Y !a.X \oplus !b.?c$ (from Example 6.16) and its encoding $\llbracket U \rrbracket = \mu_X !a.X + !b.?c$.

Example 7.13. *From Section 3, recall the bartender's process Q_B'' that stops selling beer after a certain hour, the bartender type U_B , and its encoding in CCS (which is also a CCS^- term):*

$$\begin{aligned} Q_B'' &= \mu_Y (?a\text{Coffee}.\text{!coffee}.Y + ?a\text{Beer} . (!\text{beer}.Y + !\text{no}.Y) + ?\text{pay}) \\ &\quad + \tau.\mu_Z (?a\text{Coffee}.\text{!coffee}.Z + ?a\text{Beer}.\text{!no}.Z + ?\text{pay}) \\ U_B &= \text{rec}_X (?a\text{Coffee}.\text{!coffee}.X \& ?a\text{Beer} . (!\text{beer}.X \oplus !\text{no}.X) \& ?\text{pay}) \\ \llbracket U_B \rrbracket &= \mu_X (?a\text{Coffee}.\text{!coffee}.X + ?a\text{Beer} . (!\text{beer}.X + !\text{no}.X) + ?\text{pay}) \end{aligned}$$

We want to prove $\vdash Q_B'' : U_B$. First of all, for Q_B'' we show a typing derivation for the term under Z -recursion $\mu_Z ?a\text{Coffee} \dots$. Letting $\Gamma = Z : \llbracket U_B \rrbracket$, we have the derivation shown in Table 6. Secondly, let \mathcal{D} indicate such a derivation, starting from the instance of rule (S- μ L) (i.e., excluding the application of Definition 7.12). We reuse \mathcal{D} in the derivation for the term under Y -recursion in Q_B'' , where it provides a premise for applying rule (+EXT) from Table 5. Letting $\Gamma' = Y : \llbracket U_B \rrbracket$, we obtain the derivation in Table 7.

Theorem 7.14 below states the correctness of our typing discipline. Suppose you have a process P with type T , and a process Q with type U . If $T \llbracket \cdot \rrbracket$ and $U \llbracket \cdot \rrbracket$ are I/O compliant, then $P \llbracket \cdot \rrbracket$ and $Q \llbracket \cdot \rrbracket$ are I/O compliant, too. Thus, we have that $P \llbracket \cdot \rrbracket \parallel Q \llbracket \cdot \rrbracket$ is safe (by Theorem 5.17).

Theorem 7.14 (Correctness). *If $\vdash P : T$ and $\vdash Q : U$ with $T \llbracket \cdot \rrbracket \bowtie U \llbracket \cdot \rrbracket$, then $P \llbracket \cdot \rrbracket \bowtie Q \llbracket \cdot \rrbracket$.*

Proof. From Definition 7.12 we have $P \preceq \llbracket T \rrbracket$; by Proposition 2.20, Definition 7.8 and Corollary 7.11 it follows $P \llbracket \cdot \rrbracket \preceq \llbracket T \rrbracket$. Similarly, $Q \llbracket \cdot \rrbracket \preceq \llbracket U \rrbracket$. Since $P \llbracket \cdot \rrbracket \preceq \llbracket T \rrbracket \bowtie \llbracket U \rrbracket$, by Theorem 6.12 it follows $P \llbracket \cdot \rrbracket \bowtie \llbracket U \rrbracket$. Since $Q \llbracket \cdot \rrbracket \preceq \llbracket U \rrbracket \bowtie P \llbracket \cdot \rrbracket$, then by Theorem 6.12 we conclude $Q \llbracket \cdot \rrbracket \bowtie P \llbracket \cdot \rrbracket$. \square

We stress that the above result is obtained just by exploiting the properties of I/O simulation, without explicitly proving subject reduction.

In the synchronous setting we can deduce $T \bowtie U$ either via model checking (since T and U are finite state), or using syntax-driven techniques (e.g., those in [4]); this result is lifted to the async case by Lemma 4.12. If both T and U can be encoded in CCS^- , we can reason on $\vdash P : T$ and $\vdash Q : U$ on a syntax-driven basis, through the rules in Definition 7.8.

Note, however, that Theorem 7.14 does *not* require compliance between *synchronous* session types. Therefore, the result also holds e.g. for $T = !a.?b$ and $U = !b.?a$

— since in the asynchronous setting we have $T \llbracket \cdot \rrbracket \bowtie U \llbracket \cdot \rrbracket$ (even though $T \not\bowtie U$). This is a departure from traditional session typing systems, that restrict compliance to (synchronous) *duality*; for further discussion, see Examples 7.15 and 7.16 below.

Example 7.15. *Recall $\vdash P_A'' : T_A''$ from Example 7.5, and consider the bartender processes Q_B, Q_B'' and type U_B from Section 3. We can easily obtain $\vdash Q_B : U_B$. Therefore, since in Example 4.10 we determined $U_B \llbracket \cdot \rrbracket \bowtie T_A'' \llbracket \cdot \rrbracket$, by Theorem 7.14 we have $Q_B \llbracket \cdot \rrbracket \bowtie P_A'' \llbracket \cdot \rrbracket$; hence, by Theorem 5.17 we have that $Q_B \llbracket \cdot \rrbracket \parallel P_A'' \llbracket \cdot \rrbracket$ is safe. Note that this result exploits asynchrony both via I/O compliance and via typing: in fact, this is based on $U_B \llbracket \cdot \rrbracket \bowtie T_A'' \llbracket \cdot \rrbracket$, albeit $U_B \not\bowtie T_A''$ (as discussed in Example 4.10); moreover, the typing judgement $\vdash P_A'' : T_A''$ uses rule (L).*

Example 7.16. *Recall $\vdash P_A'' : T_A''$ from Example 7.5, and consider Example 7.13, where we show $\vdash Q_B'' : U_B$. By Theorem 7.14 we have $Q_B'' \llbracket \cdot \rrbracket \bowtie P_A'' \llbracket \cdot \rrbracket$, and by Theorem 5.17 we conclude that $Q_B'' \llbracket \cdot \rrbracket \parallel P_A'' \llbracket \cdot \rrbracket$ is safe. Note that this result, as in Example 7.15, is based on asynchronous semantics, and would not hold in the synchronous setting.*

The previous examples (in particular, Example 7.13) show that our syntax-driven rules allow to type an Erlang-style `receive...after...` behaviour, featured in the bartender process.

8 Related work

This work corresponds to a thoroughly revised and improved version of [12]. The main differences between the two papers are summarized as follows:

- the definition and results on safety (Section 5) are new;
- the I/O simulation \preceq is now larger, and thus able to relate more compliance-preserving behaviours;
- the typing relation in Section 7 has been also enlarged, and its definition streamlined and simplified, bringing it closer to an algorithm;
- we correct a mistake in [12], i.e. the fact that the counterpart of Theorem 6.20 lacked the $\preceq?$ relation in its hypotheses;
- we include the proofs of all results.

Session types were introduced by Honda *et al.* in [49, 50, 72], as a type system for communication channels in a variant of the π -calculus. The resulting concept of *structured communication-based programming* has been the cornerstone on which a thriving research trend has been developed throughout the following decades. Beyond the π -calculus, session types have been later on applied

$$\begin{array}{c}
\frac{\Gamma(Z) = [U_B] \quad (\text{S-VAR})}{Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [U_B]} \quad (+\text{CTX}) \\
\frac{\frac{\Gamma(Z) = [U_B] \quad (\text{S-VAR})}{Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [U_B]} \quad (+\text{CTX})}{!coffee.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!coffee.U_B]} \quad (+\text{CTX})}{?aCoffee.!coffee.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [?aCoffee.!coffee.U_B]} \quad (+\text{CTX}) \\
\frac{\Gamma(Z) = [U_B] \quad (\text{S-VAR})}{Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [U_B]} \quad (+\text{CTX})}{!no.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!no.U_B]} \quad (+\text{INT}) \\
\frac{\frac{\Gamma(Z) = [U_B] \quad (\text{S-VAR})}{Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [U_B]} \quad (+\text{CTX})}{!no.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!no.U_B]} \quad (+\text{INT})}{!beer.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!beer.U_B \oplus !no.U_B]} \quad (+\text{CTX})}{?aBeer.!no.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [?aBeer.(!beer.U_B \oplus !no.U_B)]} \quad (+\text{CTX}) \\
\frac{\Gamma(0) = [0] \quad (\text{S-VAR})}{0 \overset{\ddot{\sim}}{\sim}_{\Gamma} [0]} \quad (+\text{CTX})}{?pay \overset{\ddot{\sim}}{\sim}_{\Gamma} [?pay]} \quad (+\text{CTX}) \\
\frac{\frac{\Gamma(Z) = [U_B] \quad (\text{S-VAR})}{Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [U_B]} \quad (+\text{CTX})}{!no.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!no.U_B]} \quad (+\text{INT})}{!beer.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!beer.U_B \oplus !no.U_B]} \quad (+\text{CTX})}{?aBeer.!no.Z + ?pay \overset{\ddot{\sim}}{\sim}_{\Gamma} [?aBeer.(!beer.U_B \oplus !no.U_B) \& ?pay]} \quad (+\text{EXT}) \\
\frac{\frac{\Gamma(Z) = [U_B] \quad (\text{S-VAR})}{Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [U_B]} \quad (+\text{CTX})}{!no.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!no.U_B]} \quad (+\text{INT})}{!beer.Z \overset{\ddot{\sim}}{\sim}_{\Gamma} [!beer.U_B \oplus !no.U_B]} \quad (+\text{CTX})}{?aBeer.!no.Z + ?pay \overset{\ddot{\sim}}{\sim}_{\Gamma} [?aBeer.(!beer.U_B \oplus !no.U_B) \& ?pay]} \quad (+\text{EXT})}{\frac{?aCoffee.!coffee.Z + ?aBeer.!no.Z + ?pay \overset{\ddot{\sim}}{\sim}_{\Gamma} [?aCoffee.!coffee.U_B \& ?aBeer.(!beer.U_B \oplus !no.U_B) \& ?pay]} \quad (\text{S-}\mu\text{R})}{\frac{?aCoffee.!coffee.Z + ?aBeer.!no.Z + ?pay \overset{\ddot{\sim}}{\sim}_{\Gamma} [U_B]} \quad (\text{S-}\mu\text{L})}{\mu_Y ?aCoffee.!coffee.Z + ?aBeer.!no.Z + ?pay \overset{\ddot{\sim}}{\sim} [U_B]} \quad (\text{DEFINITION 7.12})}{\vdash \mu_Z ?aCoffee.!coffee.Z + ?aBeer.!no.Z + ?pay : U_B}
\end{array}$$

Table 6: Typing derivation for the running example (I).

$$\begin{array}{c}
\frac{\Gamma'(Y) = [U_B] \quad (\text{S-VAR})}{Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [U_B]} \quad (+\text{CTX}) \\
\frac{\Gamma'(Y) = [U_B] \quad (\text{S-VAR})}{Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [U_B]} \quad (+\text{CTX})}{!beer.Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [!beer.U_B]} \quad (+\text{CTX}) \\
\frac{\Gamma'(Y) = [U_B] \quad (\text{S-VAR})}{Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [U_B]} \quad (+\text{CTX})}{!no.Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [!no.U_B]} \quad (+\text{CTX}) \\
\frac{\frac{\Gamma'(Y) = [U_B] \quad (\text{S-VAR})}{Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [U_B]} \quad (+\text{CTX})}{!beer.Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [!beer.U_B]} \quad (+\text{CTX})}{!beer.Y + !no.Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [!beer.U_B \oplus !no.U_B]} \quad (+\text{CTX})}{?aBeer.(!beer.Y + !no.Y) \overset{\ddot{\sim}}{\sim}_{\Gamma'} [?aBeer.(!beer.U_B \oplus !no.U_B)]} \quad (+\text{CTX}) \\
\frac{\Gamma'(0) = [0] \quad (\text{S-VAR})}{0 \overset{\ddot{\sim}}{\sim}_{\Gamma'} [0]} \quad (+\text{CTX})}{?pay \overset{\ddot{\sim}}{\sim}_{\Gamma'} [?pay]} \quad (+\text{CTX}) \\
\frac{\frac{\Gamma'(Y) = [U_B] \quad (\text{S-VAR})}{Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [U_B]} \quad (+\text{CTX})}{!beer.Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [!beer.U_B]} \quad (+\text{CTX})}{!no.Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [!no.U_B]} \quad (+\text{CTX})}{!beer.Y + !no.Y \overset{\ddot{\sim}}{\sim}_{\Gamma'} [!beer.U_B \oplus !no.U_B]} \quad (+\text{CTX})}{?aBeer.(!beer.Y + !no.Y) + ?pay + \tau.\mu_Z ?aCoffee.!coffee.Z + ?aBeer.!no.Z + ?pay \overset{\ddot{\sim}}{\sim}_{\Gamma'} [U_B]} \quad (\text{S-}\mu\text{L})}{\frac{\mathcal{D}}{\vdash Q'_B : U_B} \quad (\text{DEFINITION 7.12})} \quad (+\text{EXT, S-}\mu\text{R})
\end{array}$$

Table 7: Typing derivation for the running example (II). \mathcal{D} is the derivation starting from (S- μ L) in Table 6. Note that in this derivation, the environment Γ' is larger than the empty environment at the root of \mathcal{D} . However, \mathcal{D} “still works”, and can be simply rewritten with Γ' at its root, by Proposition 7.9.

to other calculi and programming languages. However, most approaches do not allow to express the Erlang `receive...after...` construct discussed in Question 1. Also in [62], which devises a session type system for the featherweight Erlang language, such problematic pattern is omitted. While adapting the type system of [62] to cope with the `receive...after...` construct should be feasible, our approach allows the construction of the type system (in our case, the rules for $\overset{\ddot{\sim}}{\sim}$) to be driven by an explicit underlying semantic notion (I/O simulation).

8.1 Other foundational approaches to session types

Most approaches to session types (like e.g. [73]) focus on a specific syntax of processes, which constrains their LTS; further, they often give semantics to processes, but not to types. A more semantics-oriented approach is the one in [29]. While types and processes follow a given syntax, they are both equipped with a higher-order LTS semantics, and most definitions depend on the LTS semantics, only, without inspecting the syntax of types and processes. For instance, the duality and subtyping relations are based on the “*may output*” and “*must input*” relations, which only rely on the LTS semantics. Our approach shares similar insights: for instance, also

our theory deals with inputs and outputs in an asymmetric way, and uses “persistent inputs” in key definitions, such as I/O simulation and safety.

The main difference between our work and [29] is that we do not fix a syntax of processes/types, by focusing instead on a first-order I/O LTS populated by *both* processes and types. In this way, we can reason on behaviours which are not directly expressible in [29], e.g. the Erlang pattern `receive...after...`. However, while our types are *first-order* LTSs, those in [29] are *higher-order*, as action labels can be types themselves. Both our theory and [29] consider some form of asynchrony: while we deal (in the same framework) with synchronous and FIFO-buffered behaviours, [29] implements a weaker form of asynchrony, inspired by [33].

Our approach is similar to [28, 31, 32] with respect to various aspects: for instance, (a) the common inspiration to [39] for the (synchronous) session types semantics, (b) the idea of representing processes and contracts/types in the same LTS, (c) the aim to overcome the rigid internal/external choices dichotomy required by session types, emerging both at type and process levels. In [31], it is assumed that some type system can *abstract* processes P, Q (expressed in *any* calculus) into contracts. This type system must be “consistent” and “informative”, by preserving some essential properties

like e.g. visible actions and internal non-determinism. A result in [31] is that if the abstractions of P, Q are compliant, then P, Q will be compliant as well. A similar abstraction is at the root of our approach.

The works [47, 44] study subtyping for binary session types, focussing on *communication channel* replaceability [70]. The intuition is the following: consider a type U and its subtype T , and a program P interacting through a channel; then, a channel typed with T is “less demanding” for P w.r.t. a channel typed with U — i.e., if P correctly uses an U -typed channel, then it can also correctly use a T -typed channel. This holds because T allows P to choose among “more” possible outputs, and mandates “less” inputs to be enabled, w.r.t. U . In the present work, we look at behaviours as if we were on the other endpoint of a channel, and so the co/contravariance rules are reversed. As a consequence, a subtype of a behaviour q is I/O compliant with all the behaviours compliant with q (and possibly with more, see Theorem 6.12). Subtyping relations with the same ordering of \preceq are studied in [26, 27, 40], while the direction of the subcontract relations in [29, 31] is opposite to ours. A discussion on the channel-oriented and process-oriented views of subtyping is in [46]. Besides the direction of the relation, there are other differences between [31] and this paper. For instance, some desirable subtypings like $?a \& ?b \not\sqsubseteq ?a$ do not hold in the strong subcontract relation of [31]. However, these are restored through a “weak” subcontract relation, which exploits filters to resolve external non-determinism.

In [4, 5, 6], session types are equipped with first-order and higher-order LTS semantics, and studied under different notions of client-server compliance (e.g., allowing the client to terminate interaction or to skip messages). A similar line of research is taken in [14, 15], which shows an encoding of session types into the contracts of [31], which preserves (and reflects) a subtype relation. Our paper is also focussed on preorders, but compared to [4, 5, 6, 14, 15] we work in a language-independent setting, restricting to first-order behaviours.

8.2 Multiple participants and multiple sessions

Many recent works extend the session types discipline to the multiparty case, starting from [51]. In this setting, the application designer specifies the overall communication behaviour of a set of participants through a *choreography*, which enjoys some correctness properties (e.g., safety and progress). The overall application is the result of the composition of a set of processes, which are distributed over the network, and interact through a multiparty session. To ensure the correctness of this composition, the choreography is projected into a set of *local ses-*

sion types, which abstract the end-point communication behaviour of processes: if each process is type-checked against its session type, the composition of services preserves the properties enjoyed by the choreography. The crucial technical difference w.r.t. binary session types is that, in *local* multiparty session types, input/output actions target the specific participant from/to which a message should be received/sent; correspondingly, a process typechecks only if its input/output actions target the correct participants.

Extending our theory of binary session types to the multiparty case would require to extend the compliance and subtyping relations to capture the role of each type/process, and then to produce the syntax-based typing rules. Some insights come from [30], which aims at a semantic understanding of choreographies.

Another orthogonal issue of the multiparty case is how to guarantee correct interactions in the presence of multiple *interleaved* sessions. To deal with this case, one could take inspiration from [11, 67], which introduce type systems for ensuring liveness in the presence of multiple interleaved sessions, and [13], which relates deadlock-freedom within single sessions to deadlock-freedom of processes which juggle with multiple sessions.

8.3 Asynchrony and session types

Asynchronous binary session types have been addressed in [65], where type equivalence up-to buffering was defined over traces, and then approximated via syntax-based rules. In the setting of multiparty session types, [61, 63, 64] study subtyping with “safe” partial commutativity allowed by asynchrony. These works also tackle possible optimisations allowed by buffering, similarly to our asynchronous Alice-Bartender interaction (Section 3). In [35, 36] it is noted that the asynchronous subtyping of [61] guarantees progress, but does not offer guarantees about orphan messages: e.g., in case of recursive types, a program matching the subtype may never read some messages from its queues. For this reason, [35, 36] propose a new asynchronous subtyping relation for binary session types, and they prove it to be precise w.r.t. orphan messages and other errors.

While our focus is on preservation of compliance and subtyping under buffering, the work [54] studies a bisimulation to relate processes which communicate via unbounded buffers. The expressiveness of (multiparty) session types with different kinds of FIFO buffers is investigated in [41]. In particular, they prove that, in the binary case, one output FIFO buffer per participant (as in our Definition 2.11) allows to obtain the same communication traces as other, more complex representations — e.g., adding *both* input and output FIFOs,

as in [54]. Several other works address asynchronous session types, e.g. [45, 37, 18].

8.4 Compliance and safety

Many different notions of “correct interaction” have been considered in the literature. These notions typically restrict progress, by preventing infinite traces of two behaviours which never interact [55], or by exploiting *must*-testing [2] and *should*-testing [21, 1]. Our I/O compliance is related in [10] to some of these notions.

The notion of compliance in [66] can relate behaviours which enjoy progress after a (finite-state) orchestrator has rearranged their messages. This notion is unrelated to I/O compliance: on the one hand, the latter cannot rearrange messages; on the other hand, I/O compliance has no fixed bound on the size of the buffers. For instance, the async behaviours $!a. ?b. !a^2. ?b^2 \dots !a^n. ?b^n \dots$ and $!b. ?a. !b^2. ?a^2 \dots !b^n. ?a^n \dots$ (where $!a^m$ is a sequence of m $!a$) are I/O compliant, but they are *not* compliant according to [66], as orchestrators must be finite-state.

Several works define notions of compliance for multi-party interactions [22, 23, 9, 42]. For instance, the notions in [22, 23] are based on should-testing on CCS-like processes which interact through FIFO buffers. This approach is similar to our pairing of session types and CCS terms with queues, except that we embed buffers *within* processes, and represent enqueueing with an internal action, whereas [23] adds explicit labels signalling enqueueing/dequeueing of messages.

Compliance among CFSMs [19] is usually called *safety*, and it typically requires deadlock-freedom and the absence of orphan message and unspecified reception configurations. In our framework, two behaviours with unbounded buffers can be seen as a system of CFSMs, albeit the resulting LTS is slightly different: e.g., in our setting, an output is added to a buffer with a τ -transition, and consumed with a τ -synchronisation; in CFSMs, an output $!a$ is buffered with a visible $!a$ -transition, and is consumed with a visible $?a$ -transition.

The notion of safety introduced in Section 5 is inspired to those for CFSMs, although deadlock-freedom is not explicitly mentioned in Definition 5.15, as it is already implied by the other requirements (Proposition 5.16). As discussed in Example 5.11, our notion of orphan message is stricter than the ones in [56, 42]. For instance, consider $T = \text{rec}_X !a. X$ and $U = \text{rec}_Y !b. Y$. The composition $T \parallel U$ is deadlock-free and does not reach unspecified reception configurations; by Definition 5.1, however, it is an orphan message configuration (because outputs are sent but never received), and therefore not safe. According to the definitions in [56, 42],

instead, such a configuration is considered safe: in fact, a message is an “orphan” when the machines terminate with non-empty buffers.

A notion of “orphan message” closer to ours is studied in [36, 35], where a process is said to “orphan” a buffered message when it has no chance to read it in the future — even though the process itself is not terminated. A difference w.r.t. our approach is that, in [36, 35], buffers are not introduced at the types level, but at the level of π -calculus processes. The *asynchronous subtyping* in [36, 35] allows to swap the order of input/output actions under certain conditions. Our subtyping (i.e., I/O simulation $\dot{\leq}$) is generally more restrictive in the reordering of input/output actions, even in presence of buffers: e.g., $!a. ?b \not\dot{\leq} ?b. !a$, but the two behaviours would be related by the subtyping in [36, 35]. However, in our setting, we support a similar kind of swapping through I/O compliance: e.g., $!a. ?b \dot{\bowtie} !b. ?a$, which is *not* allowed by the “compliance” (i.e., the session types duality) adopted in [36, 35]. Hence, we conjecture that the type relations captured by type duality and asynchronous subtyping in [36, 35] correspond to those captured by combining $\dot{\bowtie}$ and $\dot{\leq}$ on session types with asynchronous semantics; the latter, however, can cover more general cases of asynchrony (e.g., the parallel composition in Question 3).

8.5 Timeouts and exceptions

The works [8, 17, 16, 25, 52] enrich session types with new constructs and messages for handling timeouts and exceptions. In particular, [25, 52] mark where an exceptional event might occur within a protocol, how it can be handled at runtime; moreover, they introduce run-time monitoring to ensure proper coordination of the communicating processes. The works [8, 17, 16] add timing constraints to, respectively, binary session types, multi-party session types, and CFSMs. Although the present work does not fully address these problems, we can model some simple cases of exception handling without requiring dedicated constructs. For instance, our running example in Section 3 shows a case in which a timeout event (abstracted as a τ -transition) interrupts an external choice. Such events can also be expressed in the CCS^- fragment used in Section 7, where subtyping is syntactically defined by the relation $\dot{\leq}_T$.

9 Conclusions

We have studied session types from a semantic perspective. To this purpose, we have interpreted some standard

notions on session types on the general semantic framework of Labelled Transition Systems. This allowed us to extend some notions and results beyond session types, in a syntax-agnostic way. While we mostly focused on behaviours arising from (synchronous and asynchronous) session types and CCS, we believe that our approach can be applied to analyse the properties of other behaviours — e.g. the LTS semantics of other process calculi and programming languages.

9.1 Summary of the main results

We now briefly recap the main results of our paper. For simplicity, here we only use the symmetric I/O compliance relation \bowtie . A first remarkable result, following from Theorem 4.9, is that I/O compliance is stricter than progress, and that the two relations coincide on synchronous session types:

Theorem. *If $p \bowtie q$, then $p \dashv\vdash q$. If p, q are synchronous session types and $p \dashv\vdash q$, then $p \bowtie q$.*

In Theorems 5.17 and 5.20 we have shown that, on general behaviours, I/O compliance implies safety, i.e. absence of deadlocks, orphan messages and unspecified reception configurations. Moreover, the two relations coincide for asynchronous session types:

Theorem. *If $p \bowtie q$, then $p \parallel q$ is safe. If p, q are asynchronous session types and $p \parallel q$ is safe, then $p \bowtie q$.*

In Theorems 6.10 and 6.12 we have shown that I/O simulation $\dot{\leq}$ is an I/O compliance-preserving (and thus, safety-preserving) preorder for general behaviours.

Theorem. *The relation $\dot{\leq}$ is a preorder, and $p \dot{\leq} q \bowtie r$ implies $p \bowtie r$.*

In Theorems 4.13 and 6.20 we have shown that I/O compliance is preserved when passing from synchronous to asynchronous semantics of session types; to preserve I/O simulation, an additional relation $\dot{\leq}?$ (Definition 6.18) is required.

Theorem. *For all session types T, U :*

- *If $T \bowtie U$, then $T \square \bowtie U \square$.*
- *If $T (\dot{\leq} \cap \dot{\leq}?) U$, then $T \square \dot{\leq} U \square$.*

Our typing system is semantically grounded on $\dot{\leq}$ and \bowtie , and it enjoys the following correctness property: in the asynchronous setting, if two CCS processes are typeable with session types which are I/O compliant, then also the processes are I/O compliant. Therefore, they can safely interact.

Theorem 7.14 (Correctness). *If $\vdash P : T$ and $\vdash Q : U$ with $T \square \bowtie U \square$, then $P \square \bowtie Q \square$.*

Remarkably, our type system allows to syntactically type our examples from Section 3. Since these examples feature asynchronous semantics and external choices mixed with τ -transitions, at least in this case we can say that our approach gives a positive answer to Questions 2 and 3 in Section 1.

9.2 Future work

A possible extension to our work is to allow for the communication of channels, which is a common feature in many works on session types [53]. This should be feasible by enriching the labels of I/O LTSs to also cater for channel transmission. Modelling higher-order calculi could be feasible by employing a translation of higher-order LTSs into first-order ones, as done e.g. in [58].

Some questions about I/O compliance/simulation and our type system remain open. For instance, in this work we have not dealt with the decidability of these notions. Clearly, I/O compliance/simulation are decidable for finite-state behaviours, while they seem to be undecidable in the general case, and also in the specific case of asynchronous session types. For instance, the works [48, 11] prove that, for (binary, first-order) asynchronous session types, some compliance relations which are close to our notion of safety are undecidable. Instead, the works [57, 20] prove undecidability of asynchronous subtyping. We do not expect that these results apply directly to $\dot{\leq}$ on asynchronous session types, since $\dot{\leq}$ is more restrictive and closer to the (decidable) synchronous subtyping (as discussed in Section 8). Some works propose decidable, sound approximate techniques to check safety in CFSMs [56]. Since asynchronous session types are a special case of CFSMs, these techniques can also be applied in this setting. We expect that analogous results can be obtained for I/O simulation: in particular, it should be possible to soundly approximate I/O simulation via an axiomatization, in the spirit of our syntactic relation $\dot{\leq}$, which we conjecture to be decidable.

Note that $\dot{\leq}$ is not a precongruence on CCS: for instance, $\tau. ?a.P \dot{\leq} ?a.P$ but $?b + \tau. ?a.P \not\dot{\leq} ?b + ?a.P$. A relevant question on I/O simulation is how to devise a stricter relation which is also a precongruence on an expressive calculus, like CCS or CCS⁻. In CCS⁻, the task is simplified by the requirement of guarded sums (Definition 7.6), that rules out the example above. The main difficulties, however, are due to parallel composition: e.g., we have $?a + ?b.0 \dot{\leq} ?a$ and $?b.!c \dot{\leq} ?b.!c$, but $?a + ?b.0 \mid ?b.!c \not\dot{\leq} ?a \mid ?b.!c$ — because the (persistent) $?b$ -prefix of $?b.0$ (on the LHS) and $?b.!c$ (on the RHS) cause Definition 6.4 to require $?b.!c \mid 0 \dot{\leq} ?a \mid !c$ (which is false). For this reason, rule (LR) (Table 5) constrains the

inputs of the composed processes; different approaches might be possible, and can be investigated — albeit they seem to lead to rather strict subsets of $\dot{\leq}$.

Finally, the syntactic relation $\dot{\leq}$ in Section 7.1 can be extended in several directions. The addition of new rules is generally possible and pretty straightforward, under the approach we followed in Table 5: assuming some I/O simulations in the premises, and constructing a new one in the conclusions. As an example, the “ τ laws” from [59, §3.2] can be integrated. Another possible extension to $\dot{\leq}$ is an additional rule for “interruptible” external choices, such as:

$$\frac{Q \equiv \sum_{i \in I} ?a_i.Q_i \quad \forall j \in J, i \in I. a_j = a_i \implies P_j \dot{\leq} Q_i \quad K \neq \emptyset \quad \forall k \in K. P_k \dot{\leq} Q}{\sum_{j \in J} (?a_j.P_j) + \sum_{k \in K} \tau.P_k \dot{\leq} Q} \text{ (+EXT2)}$$

The difference between (+EXT) in Table 5 and (+EXT2) above is that the latter does *not* require the “immediate” input transitions of P to be a superset of those of Q — provided that the continuations of overlapping inputs are within the relation, and also the continuation after each τ -branch (of which at least one must exist) is within the relation. With such rule, the following bartender process has also type U_B (Section 3):

$$\mu_Y (?aCoffee.!coffee.Y + \tau.\mu_Z (?aCoffee.!coffee.Z + ?aBeer.!beer.Z + ?pay))$$

This process models a bartender that initially only serves coffees, and after an internal move τ , starts serving both coffees and beers. The further investigation of these extensions of $\dot{\leq}$ is left as future work.

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A Proofs for Section 2

Definition A.1. \equiv is the largest relation between session types coinductively defined by the rules:

$$\frac{U \equiv T[\text{rec}_X T/X]}{U \equiv \text{rec}_X T} \text{ (Eq-RecR)} \quad \frac{T[\text{rec}_X T/X] \equiv U}{\text{rec}_X T \equiv U} \text{ (Eq-RecL)}$$

$$\frac{\forall i \in I. T_i \equiv T'_i}{\&_{i \in I} ?a_i.T_i \equiv \&_{i \in I} ?a_i.T'_i} \text{ (Eq-Ctx\&)} \quad \frac{\forall i \in I. T_i \equiv T'_i}{\oplus_{i \in I} !a_i.T_i \equiv \oplus_{i \in I} !a_i.T'_i} \text{ (Eq-Ctx\oplus)}$$

Proposition A.2. \equiv is an equivalence relation for session types.

Proof. Reflexivity of \equiv is proved by showing that each pair (T, T) in the identity relation between session types satisfies the rules in Definition A.1.

Symmetry of \equiv is proved by showing that each pair (T, U) in the relation $\mathcal{R} = \{(T, U) \mid U \equiv T\}$ satisfies the rules in Definition A.1.

Transitivity of \equiv is proved by showing that each pair (T, U) in the relation $\mathcal{R} = \{(T, U) \mid \exists T' : T \equiv T' \text{ and } T' \equiv U\}$ satisfies the rules in Definition A.1. \square

Proposition A.3 below shows that the syntactic form of synchronous session types can be determined by observing their transitions.

Proposition A.3. For all T :

- (i) $T \xrightarrow{?a} \text{iff } T \equiv ?a.T' \ \& \ \&_{i \in I} ?b_i.T_i$;
- (ii) $T \xrightarrow{\tau} \text{iff } T \equiv \oplus_{i \in I} !b_i.T_i$, with $|I| > 1$;
- (iii) $T \xrightarrow{!a} \text{iff } T \equiv !a.T'$.

Proof. For all items (i)–(iii), the \Leftarrow direction follows from Definition 2.10.

For the \Rightarrow direction, we proceed by induction on the rules in Definition 2.10 generating the transitions:

- item (i). The $?a$ -transition of T can be generated in two ways:
 - a. by rule (TEXT) (base case). Then, $T = ?a.T' \ \& \ \&_{i \in I} ?b_i.T_i$. We conclude by reflexivity of \equiv (Proposition A.2);
 - b. by rule (TREC) (inductive case). Then, $T = \text{rec}_X T''$, with premise $T''[\text{rec}_X T''/X] \xrightarrow{?a}$. By the induction hypothesis, we have $T''[\text{rec}_X T''/X] \equiv ?a.T' \ \& \ \&_{i \in I} ?b_i.T_i$; by reflexivity of \equiv (Proposition A.2) and (EQ-RECR), we have $T \equiv T''[\text{rec}_X T''/X]$ — and we conclude by transitivity of \equiv (Proposition A.2);
- item (ii). The τ -transition of T can be generated in two ways:
 - a. by rule (TINT) (base case). Then, $T = \oplus_{i \in I} !b_i.T_i$, with $|I| > 1$. We conclude by reflexivity of \equiv (Proposition A.2);

- b. by rule (TREC) (inductive case). Then, $T = \text{rec}_X T''$, with premise $T''[\text{rec}_X T''/X] \xrightarrow{\tau}$. By the induction hypothesis, we have $T''[\text{rec}_X T''/X] \equiv \oplus_{i \in I} !b_i.T_i$, with $|I| > 1$; by reflexivity of \equiv (Proposition A.2) and (EQ-RECR), we have $T \equiv T''[\text{rec}_X T''/X]$ — and we conclude by transitivity of \equiv (Proposition A.2);
- item (iii). The $!a$ -transition of T can be generated in two ways:
 - a. by rule (TOUT) (base case). Then, $T = !a.T'$. We conclude by reflexivity of \equiv (Proposition A.2);
 - b. by rule (TREC) (inductive case). Then, $T = \text{rec}_X T''$, with premise $T''[\text{rec}_X T''/X] \xrightarrow{!a}$. By the induction hypothesis, we have $T''[\text{rec}_X T''/X] \equiv !a.T'$; by reflexivity of \equiv (Proposition A.2) and (EQ-RECR), we have $T \equiv T''[\text{rec}_X T''/X]$ — and we conclude by transitivity of \equiv (Proposition A.2). \square

The main semantic differences between synchronous and asynchronous session types are formalised in Proposition A.4 below: roughly, asynchrony turns sequences of outputs into sequences of τ s, but input transitions and τ -transitions are preserved. For example, we have:

- (i) $?a.0 \xrightarrow{?a} 0$ becomes $?a.0[] \xrightarrow{?a} 0[]$ (i.e., input transitions are preserved by buffering);
- (ii) $!a0 \oplus !b0 \xrightarrow{\tau} !a0$ becomes $!a0 \oplus !b0[] \xrightarrow{\tau} 0[!a]$, and similarly for the $!b$ -branch (i.e., the τ -transition of a non-deterministic internal choice is preserved in the asynchronous behaviour — where it signals the buffering of the selected prefix);
- (iii) $!a.b.0 \xrightarrow{!a} !b.0 \xrightarrow{!b} 0$ becomes $!a.b.0[] \xrightarrow{\tau} !b.0[!a] \xrightarrow{\tau} 0[!a.b]$ (i.e., deterministic output transitions are “replaced” with τ s in the asynchronous behaviour).

By observing the transitions and the buffer of an asynchronous session behaviour, we can recover information about the corresponding synchronous behaviour: e.g., from $T[\sigma] \xrightarrow{?a} T'[\sigma]$ we can infer $T \xrightarrow{?a} T'$ (and therefore, by item (i) of Proposition A.3, T is an external choice).

Proposition A.4. For all T, T' and σ ,

- (i) $T \xrightarrow{?a} T' \text{ iff } T[\sigma] \xrightarrow{?a} T'[\sigma]$;
- (ii) for all $a \neq b$, $T \xrightarrow{\tau} !a.T'$ and $T \xrightarrow{\tau} !b.T'' \text{ iff } T[\sigma] \xrightarrow{\tau} T'[\sigma.!a] \wedge T[\sigma] \xrightarrow{\tau} T''[\sigma.!b]$;
- (iii) $T \xrightarrow{!a} T' \text{ iff } T[\sigma] \xrightarrow{\tau} T'[\sigma.!a] \wedge \nexists b \neq a. T[\sigma] \xrightarrow{\tau} T'[\sigma.!b]$.

Proof. \Rightarrow direction:

- item (i): by Proposition A.3 (item (i)), T is equivalent to a (possibly recursive) external choice with a $?a$ -branch. From such T , by rules (RULECEXTA) and (TRECA), we obtain the thesis;
- item (ii): by Proposition A.3 (item (ii)), T is equivalent to a (possibly recursive) internal choice with 2 or more branches. From such T , by rules (TINTA) and (TRECA), we obtain the thesis, with $!a, !b$ being the distinct guards of two of the branches of T ;
- item (iii): by Proposition A.3 (item (iii)), T is equivalent to a (possibly recursive) single-branch internal choice $!a.T''$. From such T , by rules (TOUTA) and (TRECA), we obtain the thesis.

For the \Leftarrow direction, we proceed by induction on the rules in Definition 2.11 generating the transitions. The development is similar to the \Rightarrow direction in the proof of Proposition A.3: we determine the possible syntactic form of T , which may be (up-to recursion) an external choice for item (i), or an internal choice for items (ii)–(iii) (resp. with

multiple branches including $!a$ and $!b$, or with just one $!a$ -branch). Then, we conclude by the rules in Definition 2.10 (i.e., (TEXT) for item (i), (TINT) for item (ii), and (TOUT) for item (iii)). \square

The following propositions state that, in an asynchronous session behaviour, an output is only visible when it is at the head of the buffer, and is persistent.

Proposition A.5. $T[\sigma] \xrightarrow{!a}$ iff $\exists \sigma'. \sigma = !a.\sigma'$. If $T \xrightarrow{!b} T'$, then $\exists a. T[\sigma] \xrightarrow{\tau} T'[\sigma. !b] \xrightarrow{!a}$.

Proof. The first part of the statement follows by Definition 2.11. For the second part, since $T \xrightarrow{!b} T'$, then by Definition 2.10 it must be $T \equiv !b.T' \oplus \bigoplus_{i \in I} !c_i.T_i$ (and in particular, $I = \emptyset$ iff $T \xrightarrow{!b} T'$). We then conclude by Definition 2.11. \square

Proposition A.6. $T[\sigma] \xrightarrow{!a}$ iff $\exists \sigma'. \sigma = !a.\sigma'$, or $\sigma = \epsilon$ and $\exists T'. T \equiv !a.T'$

Proof. See page 27. \square

Proof. For the \Leftarrow direction, we have the following two cases:

- $\sigma = !a.\sigma'$. By Proposition A.5, whenever $T[!a.\sigma'] \Rightarrow T'[\sigma'']$, each τ -transition corresponds to an output of T being enqueued to the buffer. Therefore, $\sigma'' = !a.\sigma'.\sigma'''$ (for some σ'''), and we have $T'[\sigma''] \xrightarrow{!a}$, from which the thesis follows.
- $\sigma = \epsilon$ and $T \equiv !a.T'$. The thesis follows from Definition 2.11.

For the \Rightarrow direction, we proceed by cases on the structure of σ :

- $\sigma = !a.\sigma'$. The thesis follows trivially;
- $\sigma = !b.\sigma'$ (with $b \neq a$). In this case, by Definition 2.11 we have $T[\sigma] \not\xrightarrow{!a}$ — and therefore, $T[\sigma] \not\xrightarrow{!a}$ (contradiction);
- $\sigma = \epsilon$. Then either:
 - $T \equiv \&_{i \in I} ?b_i.T_i$. Then, by Definition 2.11, $\forall c. T[\sigma] \not\xrightarrow{!a}$ — and therefore, $T[\sigma] \not\xrightarrow{!a}$ (contradiction);
 - $T \equiv \bigoplus_{i \in I} !b_i.T_i$. If $|I| \neq 1$, we have either:
 - $|I| = 0$. Then, $T = \mathbf{0}$ (since $I = \emptyset$), and therefore $T[\sigma] \not\xrightarrow{!a}$ (contradiction);
 - $|I| > 1$. Then, since the guards of \bigoplus are pairwise distinct, $\exists i \in I. b_i \neq a$. Hence, $T[\sigma] \xrightarrow{\tau} T_i[!b_i] \not\xrightarrow{!a}$ (by Definition 2.11), and therefore $T[\sigma] \not\xrightarrow{!a}$ (contradiction).

We are left to examine the case $|I| = 1$: we have $T[\sigma] \equiv !b.T'[\sigma] \xrightarrow{\tau} T'[\sigma. !b]$ for some $b \in I = \{b\}$ and T' . If $b \neq a$, by Definition 2.11 we have $T'[\sigma. !b] \not\xrightarrow{!a}$, and thus $T[\sigma] \not\xrightarrow{!a}$ (contradiction). Therefore, we conclude that $T \equiv !a.T'$ (for some T'). \square

B Proofs for Section 4

Proposition 4.3 (\dashv/\dashv -induced shapes of session types). If $T \dashv U$, then exactly one of the following cases holds:

- a. $T = \mathbf{0}$;
- b. $T \equiv \&_{i \in I} ?a_i.T_i$ and $U \equiv \bigoplus_{j \in J} !a_j.U_j$, with $\emptyset \neq J \subseteq I$, and $\forall j \in J. T_j \dashv U_j$;
- c. $T \equiv \bigoplus_{i \in I} !a_i.T_i$ and $U \equiv \&_{j \in J} ?a_j.U_j$, with $\emptyset \neq I \subseteq J$, and $\forall i \in I. T_i \dashv U_i$.

Furthermore, if $T \dashv U$, we have either:

- a. $T = U = \mathbf{0}$;
- b. $T \equiv \&_{i \in I} ?a_i.T_i$ and $U \equiv \bigoplus_{j \in J} !a_j.U_j$, with $\emptyset \neq J \subseteq I$, and $\forall j \in J. T_j \dashv U_j$;
- c. $T \equiv \bigoplus_{i \in I} !a_i.T_i$ and $U \equiv \&_{j \in J} ?a_j.U_j$, with $\emptyset \neq I \subseteq J$, and $\forall i \in I. T_i \dashv U_i$.

Proof. See [4]: the statement for \dashv derives from the proof of Proposition 2.9 therein, while the statement for \dashv derives from Lemma 3.3 in the same work (modulo two minor differences: in this paper, we postulate that $\mathbf{0}$ is the success state, and the unfolding of $\text{rec}_X \dots$ does not emit a τ -transition). \square

Lemma 4.5. $\bowtie = \dot{\bowtie} \cap \check{\bowtie}$.

Proof. By Definition 4.4, we have $\bowtie \subseteq (\dot{\bowtie} \cap \check{\bowtie})$. To prove the inverse inclusion, let $\mathcal{R} = (\dot{\bowtie} \cap \check{\bowtie})$. Whenever $(p, q) \in \mathcal{R}$, we have $p \dot{\bowtie} q$ and $q \check{\bowtie} p$, and:

- a. $(p\psi^! \subseteq \text{co}(q\psi^?)$ and $(p\psi^! = \emptyset \wedge p\psi^? \neq \emptyset$ implies $\emptyset \neq q\psi^! \subseteq \text{co}(p\psi^?)$) and $(q\psi^! \subseteq \text{co}(p\psi^?)$ and $(q\psi^! = \emptyset \wedge q\psi^? \neq \emptyset$ implies $\emptyset \neq p\psi^! \subseteq \text{co}(q\psi^?)$);
- b. $p \xrightarrow{\ell} p' \wedge q \xrightarrow{\text{co}(\ell)} q'$ implies $p' \dot{\bowtie} q'$ and $q \xrightarrow{\ell'} q' \wedge p \xrightarrow{\text{co}(\ell')} p'$ implies $q' \check{\bowtie} p'$;
- c. $p \xrightarrow{\tau} p'$ implies $p' \dot{\bowtie} q$ and $q \xrightarrow{\tau} q'$ implies $q' \check{\bowtie} p$;
- d. $q \xrightarrow{\tau} q'$ implies $p \dot{\bowtie} q'$ and $p \xrightarrow{\tau} p'$ implies $q \check{\bowtie} p'$.

With some simplifications and rearrangements, for all $(p, q) \in \mathcal{R}$, we have:

- a. $p\psi^! \subseteq \text{co}(q\psi^?)$ and $q\psi^! \subseteq \text{co}(p\psi^?)$ and $(p\psi^! = \emptyset \wedge p\psi^? \neq \emptyset$ implies $\emptyset \neq q\psi^! \subseteq \text{co}(p\psi^?)$) and $(q\psi^! = \emptyset \wedge q\psi^? \neq \emptyset$ implies $\emptyset \neq p\psi^! \subseteq \text{co}(q\psi^?)$);
- b. $p \xrightarrow{\ell} p' \wedge q \xrightarrow{\text{co}(\ell)} q'$ implies $p' \dot{\bowtie} q' \wedge q' \check{\bowtie} p'$, i.e. $p' \mathcal{R} q'$;
- c. $p \xrightarrow{\tau} p'$ implies $p' \dot{\bowtie} q \wedge q \check{\bowtie} p'$, i.e. $p' \mathcal{R} q$;
- d. $q \xrightarrow{\tau} q'$ implies $p \dot{\bowtie} q' \wedge q' \check{\bowtie} p$, i.e. $p \mathcal{R} q'$.

Therefore, each $(p, q) \in \mathcal{R}$ satisfies items a–d of Definition 4.4 — hence, \mathcal{R} is an I/O compliance relation. Furthermore, \mathcal{R} is symmetric. Thus, $\mathcal{R} = (\dot{\bowtie} \cap \check{\bowtie}) \subseteq \bowtie$. \square

Proposition B.1. Let $p \circ q$, with $\circ \in \{\dot{\bowtie}, \check{\bowtie}, \bowtie\}$. Then, $p \Rightarrow q'$ implies $p' \circ q$.

Proof. We first prove the statement for $\circ = \dot{\bowtie}$. We proceed by induction on the length of the sequence of τ -transitions in $p \Rightarrow p'$. The base case ($n = 0$) follows from the hypothesis. For the inductive case, let $p \Rightarrow p^* \xrightarrow{\tau} p'$: by the induction hypothesis, we have $p^* \dot{\bowtie} q$ — and we conclude by item c of Definition 4.4.

The proof for $\circ = \check{\bowtie}$ is similar, except that we conclude by item d of Definition 4.4.

Finally, the thesis for $\circ = \bowtie$ follows from Lemma 4.5. \square

Proposition B.2. Let $p \circ q$, with $\circ \in \{\dot{\bowtie}, \check{\bowtie}, \bowtie\}$. Then, $p \xrightarrow{!a} p' \wedge q \xrightarrow{?a} q'$ implies $p' \circ q'$.

Proof. Let:

$$p \Rightarrow p_0 \xrightarrow{!a} p'_0 \Rightarrow p'$$

$$q \Rightarrow q_0 \xrightarrow{?a} q'_0 \Rightarrow q'$$

We first consider the case $\circ = \dot{\bowtie}$. By Proposition B.1 on $p \Rightarrow p_0$, we have $p_0 \dot{\bowtie} q$; then, by applying Proposition B.1 on $q \Rightarrow q_0$, we have $p_0 \dot{\bowtie} q_0$. Now, by item b of Definition 4.4), we obtain

$p'_0 \dot{\prec} q'_0$; finally, again by applying Proposition B.1 on $p'_0 \Rightarrow p'$ and then on $q'_0 \Rightarrow q'$, we conclude $p' \dot{\prec} q'$.

The proof for $\circ = \dot{\succ}$ is similar.

Finally, the thesis for $\circ = \dot{\asymp}$ follows from Lemma 4.5. \square

Proposition B.3. *Let $p \dot{\prec} q$ and $p \xrightarrow{!a}$. Then, $q \xrightarrow{??a}$.*

Proof. Let q' be such that $q \Rightarrow q'$. By Proposition B.1 we have that $p \dot{\prec} q'$; thus, by item *a* of Definition 4.4, it follows that $p\Downarrow^! \subseteq \text{co}(q'\Downarrow^?)$. Therefore, $q' \xrightarrow{??a}$ — from which we conclude that $q \xrightarrow{??a}$. \square

Corollary B.4. *Let $w \in (A^!)^*$, and let $p \xrightarrow{w}$. Then, $p \dot{\prec} q$ implies $q \xrightarrow{\text{co}(w)}$. Moreover, $\forall p', q'. p \xrightarrow{w} p' \wedge q \xrightarrow{\text{co}(w)} q'$ implies $p' \dot{\prec} q'$.*

Proof. The first part of the statement is proved by induction on w , and follows from Proposition B.1, Proposition B.3 and Proposition B.2.

The “moreover...” part, again by induction on w , follows from Proposition B.3. \square

Proposition B.5. *Let $p \dot{\prec} q$, with $p\Downarrow^! = \emptyset$, $p\Downarrow^? \neq \emptyset$ and $q \xrightarrow{!a}$: then, $p \xrightarrow{??a}$.*

Proof. $\forall p'. p \Rightarrow p'$, by Proposition B.1 we have $p\Downarrow^! = \emptyset$ and $p' \dot{\prec} q$; hence, by item *a* of Definition 4.4, we have $\forall p'. p \Rightarrow p'$ implies $q\Downarrow^! \subseteq \text{co}(p'\Downarrow^?)$, i.e. $p' \xrightarrow{??a}$. We conclude that $p \xrightarrow{??a}$. \square

Theorem 4.9. (a) *If $p \dot{\prec} q$, then $p \dashv q$; (b) if $p, q \in \mathbb{U}_{ST}$ and $p \dashv q$, then $p \dot{\prec} q$.*

Proof. We first prove $\dot{\prec} \subseteq \dashv$. Let:

$$F(\mathcal{X}) = \left\{ (p, q) \left| \begin{array}{l} p \parallel q \xrightarrow{\bar{r}} \text{ implies } p \cong \mathbf{0} \quad \text{and} \\ p \parallel q \xrightarrow{r} p' \parallel q' \text{ implies } (p', q') \in \mathcal{X} \end{array} \right. \right\}$$

It is easy to check that $\text{gfp}(F) = \dashv$. By the coinduction proof principle, if $\dot{\prec} \subseteq F(\dot{\prec})$, we can deduce that $\dot{\prec} \subseteq \text{gfp}(F) = \dashv$. So, we show that $\dot{\prec} \subseteq F(\dot{\prec})$. Let $p \dot{\prec} q$.

For the first clause of the definition of F , assume $p \parallel q \xrightarrow{\bar{r}}$. We show that p has no outgoing transitions (i.e., $p \cong \mathbf{0}$) by showing the absurdity of the following cases:

- $p \xrightarrow{\bar{r}}$. Not possible, because it would contradict the assumption $p \parallel q \xrightarrow{\bar{r}}$.
- $p \xrightarrow{!a}$. By item *a* of Definition 4.4, we have that $p\Downarrow^! \subseteq \text{co}(q\Downarrow^?)$, hence $q \xrightarrow{??a}$. Note that $q \xrightarrow{\bar{r}}$, because otherwise we would contradict the assumption $p \parallel q \xrightarrow{\bar{r}}$. Therefore, it must be $q \xrightarrow{??a}$, and so by definition of \parallel we would obtain $p \parallel q \xrightarrow{\bar{r}}$ — contradiction.
- $p \xrightarrow{??a}$ and $\#b. p \xrightarrow{!b}$. Note that $p \xrightarrow{\bar{r}}$ and $q \xrightarrow{\bar{r}}$, because otherwise we would contradict the assumption $p \parallel q \xrightarrow{\bar{r}}$. Since $p\Downarrow^! = \emptyset$ and $p\Downarrow^? \neq \emptyset$, by item *a* of Definition 4.4 we have that $\emptyset \neq q\Downarrow^! \subseteq \text{co}(p\Downarrow^?)$. Since $q \xrightarrow{\bar{r}}$, it must be $q \xrightarrow{!a}$, and so by definition of \parallel we would obtain $p \parallel q \xrightarrow{\bar{r}}$ — contradiction.

Thus, since p cannot have outgoing transitions, we conclude that $p \cong \mathbf{0}$.

For the second clause of the definition of F , assume that $p \parallel q \xrightarrow{r}$. We have the following three cases:

- $p \xrightarrow{r} p'$. By definition of \parallel , this implies $p \parallel q \xrightarrow{r} p' \parallel q$. By item *c* of Definition 4.4, we conclude that $p' \dot{\prec} q$.
- $q \xrightarrow{r} q'$. Similar to the previous case.

- $p \xrightarrow{\ell} p'$ and $q \xrightarrow{\text{co}(\ell)} q'$, for some label ℓ . By definition of \parallel , this implies $p \parallel q \xrightarrow{r} p' \parallel q'$. By item *b* of Definition 4.4, we conclude that $p' \dot{\prec} q'$.

In all three cases, we have concluded that $p' \dot{\prec} q'$, thus satisfying the second clause of F .

We now prove that $T \dashv U$ implies $T \dot{\prec} U$, for all session types T, U . To do that, it suffices to show that \dashv is an I/O compliance relation. Assume that $T \dashv U$. We show that all the clauses of Definition 4.4 are satisfied:

- recall the 3 possible forms of T and U from Proposition 4.3: modulo unfolding, either $T = \mathbf{0}$, or $T = \bigoplus_{i \in I} !a_i.T_i$ (with $|I| \geq 1$) and $U = \&_{j \in J} ?a_j.U_j$ with $I \subseteq J$, or vice versa (i.e., T is an external choice and U is an internal choice). Each of these three possible forms satisfies item *a* in Definition 4.4;
- when the premise of item *b* of Definition 4.4 holds, we have $T \parallel U \xrightarrow{r} T' \parallel U'$; by Definition 4.1, we conclude $T' \dashv U'$;
- when $T \xrightarrow{r} T'$ (premise of item *c* of Definition 4.4), by Definition 4.1, we conclude $T' \dashv U$;
- similarly, when $U \xrightarrow{r} U'$ (premise of item *d* of Definition 4.4), by Definition 4.1, we conclude $T \dashv U'$. \square

B.1 I/O compliance and asynchrony

Notation B.6. *We write $\sigma.T'$ for the session type obtained by prefixing T' with the sequence of outputs σ .*

Lemma B.7 (Sync session behaviours and async I/O compliance (I)). *Let $T \circ U$, for some $\circ \in \{\vdash, \dashv, \dashv\!, \dot{\prec}, \dot{\asymp}, \dot{\succ}\}$. Then, $T \parallel U \Rightarrow T'[\sigma]$ implies $\sigma.T' \circ U$.*

Proof. We first prove the statement for $\circ = \dashv$, and therefore we show that:

$$T \dashv U \wedge T \parallel U \Rightarrow T'[\sigma] \text{ implies } \sigma.T' \dashv U \quad (5)$$

If T is equivalent to a (possibly empty) external choice, we have $T \parallel U \xrightarrow{\bar{r}}$, and hence $T' = T$ and $\sigma = \epsilon$: therefore, the thesis coincides with the hypothesis.

Otherwise, if T is a non-empty internal choice, we proceed by induction on the length of σ , which (by the semantics in Definition 2.11) corresponds to the number of τ -transitions along $T \parallel U \Rightarrow T'[\sigma]$:

- base case: $\sigma = \epsilon$. Then, $T' = T$ and $\sigma = \epsilon$, and therefore the thesis coincides with the hypothesis;
- inductive case: $\sigma = \sigma'.!a$, with $\sigma' = !b_1 \dots !b_n$. In this case, we have $T \parallel U \Rightarrow T''[\sigma'] \xrightarrow{\tau} T'[\sigma'.!a]$, and by applying the induction hypothesis, $\sigma'.T'' \dashv U$. Therefore, by applying Proposition 4.3 for n times along the synchronisations of $\sigma'.T''$ and U , we have:

$$\begin{aligned} !b_1.!b_2 \dots !b_n.T'' \dashv U &= U_1 = ?b_1.U_2 \& \&_{i \in I_1} ?b_i.U_i \\ !b_2 \dots !b_n.T'' \dashv U_2 &= ?b_2.U_3 \& \&_{i \in I_2} ?b_i.U_i \\ &\dots \\ !b_n.T'' \dashv U_n &= ?b_n.U_{n+1} \& \&_{i \in I_n} ?b_i.U_i \\ T'' \dashv U_{n+1} & \end{aligned}$$

At this point, since $T''[\sigma'] \xrightarrow{\tau} T'[\sigma'.!a]$ (i.e., T'' enqueues an output in its buffer), by the semantics in Definition 2.11 we have:

$$T''[\sigma'] = \left(!a.T' \oplus \bigoplus_{i \in I} !a_i.T_i \right) [\sigma'] \xrightarrow{\tau} T'[\sigma'.!a]$$

i.e., T'' must be a non-empty internal choice with a $!a$ -guarded branch; correspondingly, from $T'' \dashv U_{n+1}$ and Proposition 4.3, we have:

$$U_{n+1} = ?a.U' \& \bigotimes_{k \in K} ?a_k.U'_k \quad \text{with } I \subseteq K \text{ and } T' \dashv U'$$

But then, $\sigma'.!a.T' = \sigma.T' \dashv U$. This concludes the proof of Equation (5).

With the proof above, the other possible values of \circ can be verified in a straightforward way:

- case $\circ = \vdash$ can be proved in a similar way, noticing that in the inductive case, U may also become $\mathbf{0}$, i.e. it may terminate at any point without consuming T 's output with an external choice;
- case $\circ = \dashv$ holds because (by Definition 4.1) $\dashv = \vdash \cap \dashv$;
- case $\circ = \dot{\leftarrow}$ follows by Equation (5) and Theorem 4.9; case $\circ = \dot{\rightarrow}$ is similar;
- finally, case $\circ = \dot{\bowtie}$ follows by the previous item, because (by Lemma 4.5) $\dot{\bowtie} = \dot{\rightarrow} \cap \dot{\leftarrow}$.

□

Notation B.8. Given a sequence of outputs $\sigma = !a_1 \dots !a_n$, we write $\sigma \Rightarrow$ for $\xrightarrow{!a_1} \dots \xrightarrow{!a_n}$.

Lemma B.9 (Diamond lemma for asynchronous session types). Let:

$$T_0 \parallel U_0 \parallel \xrightarrow{\tau}^n T_n[\sigma_n] \parallel U_n[\rho_n]$$

with $\sigma_n \neq \epsilon$, $\rho_n = \epsilon$, and $\forall i \leq n : (\sigma_i = \epsilon \text{ or } \rho_i = \epsilon)$. Let m be the length of σ_n . Then, there exists some $k \leq n$ such that:

1. $\sigma_k = \epsilon$ and $\forall i > k : \sigma_i \neq \epsilon$
2. $\forall i \geq k : \rho_i = \epsilon$
3. there exist T'_j, U'_j (for all $j \in k..n$) such that:

$$\begin{aligned} T_k[\sigma_k] \parallel U_k[\rho_k] &= T'_k \parallel U'_k \\ &\xrightarrow{\tau}^{n-k-m} T'_{n-m} \parallel U'_{n-m} \\ &\xrightarrow{\tau}^m T'_n[\sigma_n] \parallel U'_n = T_n[\sigma_n] \parallel U_n \end{aligned}$$

where $\forall i \in 0..m : U'_{(n-m)+i} = U_n$

4. $T'_{n-m} \xrightarrow{\sigma_n} T_n$

Proof. (Sketch) Let k be the greatest index such that both queues σ_k and ρ_k are empty. Since σ_n is not empty, then Items 1 and 2 follow. For Item 3, we observe that T_k performs some outputs (and no inputs, since the queues ρ_i are persistently empty for $i \geq k$). These outputs can be split in two parts: in the first one (called $\tilde{\sigma}$) the inputs are read later on by U_k , while in the second part (called $\tilde{\sigma}'$) the inputs remain enqueued (and so, $\tilde{\sigma}' = \sigma_n$). It is possible to reorder the moves without altering the length of the computation, and so that: (i) at the beginning, $\tilde{\sigma}$ is enqueued and read by U ; (ii) then, the second part is enqueued, and never read. The terms T'_j, U'_j (for all $j \in k..n$) are defined according to this reordering. After the steps (i), both queues are empty, and the component U no longer moves, hence it is equal to U_n until the end of the computation.

Item 4 is a direct consequence of Item 3. □

Lemma 4.11. Let $T \circ U$, with $\circ \in \{\dashv, \vdash, \dashv, \dot{\leftarrow}, \dot{\rightarrow}, \dot{\bowtie}\}$. If $T \parallel U \Rightarrow T'[\sigma] \parallel U'[\rho]$, then:

- (i) $\sigma = \epsilon$ or $\rho = \epsilon$.
- (ii) if $\sigma = \rho = \epsilon$ then $T' \circ U'$. □

Proof. We first consider the case $\circ = \dashv$, showing that:

$$T \dashv U \wedge T \parallel U \Rightarrow T'[\sigma] \parallel U'[\rho] \text{ implies } \sigma = \epsilon \text{ or } \rho = \epsilon \quad (6)$$

$$\text{the premise above and } \sigma = \rho = \epsilon \text{ implies } T' \dashv U' \quad (7)$$

We proceed by induction on the length of the sequence of τ -transitions in $T \parallel U \Rightarrow T'[\sigma] \parallel U'[\rho]$. In the base case (i.e., when there are no transitions) the thesis coincides with the hypothesis, and both Equations (6) and (7) trivially hold.

In the inductive case, let:

$$T \parallel U \Rightarrow T''[\sigma''] \parallel U''[\rho''] \xrightarrow{\tau} T'[\sigma'] \parallel U'[\rho'] \quad (8)$$

and by the induction hypothesis,

$$(\sigma'' = \epsilon \text{ or } \rho'' = \epsilon) \text{ and } (\sigma'' = \rho'' = \epsilon \text{ implies } T'' \dashv U'') \quad (9)$$

We have the following cases:

- $\sigma'' \neq \epsilon$ and $\rho'' = \epsilon$. By Lemma B.9, the computation $T \parallel U \Rightarrow T''[\sigma''] \parallel U''[\rho'']$ can be rearranged as follows (while maintaining the original length):

$$T \parallel U \Rightarrow T'''' \parallel U'''' \Rightarrow T''[\sigma''] \parallel U'' \parallel$$

where $T'''' \xrightarrow{\sigma''} T''$ (by item 4 of Lemma B.9). Since the length of the computation

$$T \parallel U \Rightarrow T'''' \parallel U''''$$

is shorter than the the length of the original computation, by the induction hypothesis we have $T'''' \dashv U''$. Let $\sigma'''' = !a.\sigma''''$. Then, by Proposition 4.3, U'' must be (up-to unfolding) an *external* choice with a $?a$ -branch. Hence, the rightmost τ -transition in Equation (8) can only be generated in two ways:

- via synchronisation, i.e.:

$$T''[!a.\sigma'''' \parallel U'' \parallel \xrightarrow{\tau} T'[\sigma'''] \parallel U' \parallel \quad (\text{with } \sigma''' = \sigma')$$

where $T' = T''$. We can notice that item (i) of the thesis is already satisfied. For item (ii), assume that $\sigma''' = \epsilon$. Then, $!a$ is the only output transition from T'''' to T'' — i.e., $T'''' \xrightarrow{!a} T'' = T'$; and since $U'' \xrightarrow{?a} U'$, from $T'' \dashv U''$, we deduce $T' \dashv U'$.

- via buffering, i.e., for some b :

$$T''[!a.\sigma'''' \parallel U'' \parallel \xrightarrow{\tau} T'[!a.\sigma''''.!b] \parallel U' \parallel \quad (\text{where } U' = U'')$$

Then, we satisfy item (i) and (vacuously) item (ii) of the thesis;

- $\sigma'' = \epsilon$ and $\rho'' \neq \epsilon$. The proof is similar to the previous case, by swapping the roles of T'' and U'' , and the roles of σ and ρ .
- $\sigma'' = \rho'' = \epsilon$. Item (i) holds because in a single τ move, at most one of the two queues can become non-empty. Item (ii) holds vacuously, because the only way of reaching $\sigma' = \rho' = \epsilon$ would be through a synchronisation, which is not possible because $\sigma'' = \rho'' = \epsilon$.

This concludes the proof of Equations (6) and (7), in case $\circ = \dashv$. The other possible values of \circ can be verified as follows:

- $\circ = \dot{\leftarrow}$ follows by Equation (6) and $\dashv = \dot{\leftarrow}$ (Theorem 4.9);
- $\circ \in \{\vdash, \dot{\rightarrow}\}$ follow by symmetry;
- $\circ = \dashv$ holds because (by Definition 4.1) $\dashv = \vdash \cap \dashv$;
- finally, $\circ = \dot{\bowtie}$ holds because $\dot{\bowtie} = \dot{\rightarrow} \cap \dot{\leftarrow}$ (by Lemma 4.5). □

Lemma 4.12. *If $T \dot{\prec} U$, then $T \square \dot{\prec} U \square$.*

Proof. Assume that $T \dot{\prec} U$, and let:

$$\mathcal{R} = \{(T'[\sigma], U'[\rho]) \mid T \square \parallel U \square \Rightarrow T'[\sigma] \parallel U'[\rho]\}$$

We prove that \mathcal{R} is an I/O compliance relation, i.e. it satisfies all the items of Definition 4.4. The thesis will then follow by the fact that $(T \square, U \square) \in \mathcal{R}$.

Let $(T'[\sigma], U'[\rho]) \in \mathcal{R}$. Note that items *b–d* of Definition 4.4 follow directly by definition of \parallel , so we only need to prove item *a*. By Lemma 4.11, we have $\sigma = \epsilon$ or $\rho = \epsilon$, and so we have the following three cases:

– $\sigma \neq \epsilon$ and $\rho = \epsilon$. Let $\sigma = !a.\sigma'$. Then:

$$T'[\sigma]\Downarrow^! = \{!a\} \quad (10)$$

By items *(ii)* and *(iii)* of Proposition A.4 we know that each output in σ has been generated by some T'' such that $T'' \xrightarrow{\sigma} T'$; let us take such T'' so that:

$$T \square \parallel U \square \Rightarrow T'' \square \parallel U' \square \Rightarrow T'[\sigma] \parallel U' \square$$

From $T \dot{\prec} U$, $T \square \parallel U \square \Rightarrow T'' \square \parallel U' \square$ and Lemma 4.11 we get $T'' \dot{\prec} U'$. By items *(ii)* and *(iii)* of Proposition A.3, we know that T'' is an internal choice, hence by Theorem 4.9 and Proposition 4.3 we know that U' must be a (larger) external choice, i.e. $T''\Downarrow^! \subseteq \text{co}(U'\Downarrow^?)$. Hence, from Equation (10) we obtain:

$$\{!a\} = T'[\sigma]\Downarrow^! \subseteq T''\Downarrow^! \subseteq \text{co}(U'\Downarrow^?) = \text{co}(U'\Downarrow^?)$$

This proves the first part of item *a* in Definition 4.4. The second part holds vacuously, since $T'[\sigma]\Downarrow^! \neq \emptyset$.

– $\sigma = \epsilon$ and $\rho \neq \epsilon$. If $T' = \mathbf{0}$, then item *a* in Definition 4.4 is trivially satisfied. Otherwise, the reasoning is similar to the case above, by swapping the role of T' and U' , and considering the queue ρ instead of σ . More in detail, we let $\rho = !a.\rho'$, and so we obtain:

$$U'[\rho]\Downarrow^! = \{!a\} \quad (11)$$

As in the previous item, via Proposition A.4 we take U'' such that $U'' \xrightarrow{\rho} U'$, from which we have:

$$T \square \parallel U \square \Rightarrow T' \square \parallel U'' \square \Rightarrow T' \square \parallel U'[\rho]$$

Since $T \dot{\prec} U$ and $T \square \parallel U \square \Rightarrow T' \square \parallel U'' \square$, from Lemma 4.11 we obtain that $T' \dot{\prec} U''$.

By Proposition A.3 we have that U'' is a non-empty internal choice, and by Theorem 4.9 and Proposition 4.3 we know that T' is a (larger) external choice, i.e. $\emptyset \neq U''\Downarrow^! \subseteq \text{co}(T'\Downarrow^?)$. Since T' is an external choice, then $T'\Downarrow^! = \emptyset$, which vacuously satisfies the first part of item *a* in Definition 4.4. For the second part of the item, from Equation (11) we have:

$$\emptyset \neq \{!a\} = U'[\rho]\Downarrow^! \subseteq U''\Downarrow^! \subseteq \text{co}(T'\Downarrow^?) = \text{co}(T'\Downarrow^?)$$

– $\sigma = \rho = \epsilon$. By Lemma 4.11, it follows that $T' \dot{\prec} U'$. Hence, by Theorem 4.9 and Proposition 4.3, we can proceed by cases on the form of T' and U' :

– $T' = \mathbf{0}$. Then, item *a* of Definition 4.4 is trivially satisfied.

– $T' = \&_{i \in I} ?a_i.T_i$ and $U' = \bigoplus_{j \in J} !a_j.U_j$, with $\emptyset \neq J \subseteq I$. Then, $T'\Downarrow^! = \emptyset$, which satisfies the first part of item *a*. Further, $\emptyset \neq U'\Downarrow^! \subseteq \text{co}(T'\Downarrow^?)$, thus satisfying the second part of the item.

– $T' = \bigoplus_{i \in I} !a_i.T_i$ and $U' = \&_{j \in J} ?a_j.U_j$, with $\emptyset \neq I \subseteq J$. Then, $T'\Downarrow^! \subseteq \text{co}(U'\Downarrow^?)$, which satisfies the first part of item *a*; the second part of the item is vacuously true, since $T'\Downarrow^! \neq \emptyset$. \square

C Proofs for Section 5

Lemma 5.4. *Let $T[\sigma] \parallel U[\rho]$ be an orphan message configuration, with $!a$ orphan message of $T[\sigma]$. Then, $T[\sigma] \parallel U[\rho] \Rightarrow T'[\sigma'] \parallel U'[\rho']$ implies that $T'[\sigma'] \parallel U'[\rho']$ is an orphan message configuration, with $!a$ orphan message of $T'[\sigma']$. Furthermore, $\sigma' = \sigma.\sigma''$ (for some σ'').*

Proof. By hypothesis and Definition 5.1 we have $T[\sigma] \xrightarrow{!a}$ and $U[\rho] \not\xrightarrow{!a}$. Moreover, by Lemma 5.3 we have either $\sigma = !a.\sigma''$ (for some σ''), or $\sigma = \epsilon$ and $T = !a.T'$: therefore, after at most one τ -step (when $\sigma = \epsilon$), $!a$ is at the head of T 's buffer and becomes *only* output (weakly) reachable from $T[\sigma]$ (by Proposition A.6). At that point, since $U[\rho] \not\xrightarrow{!a}$, the two behaviours cannot synchronise on such $!a$, and thus it cannot be removed from the head of the buffer. Hence, each τ -move along the trace $T[\sigma] \parallel U[\rho] \Rightarrow T'[\sigma'] \parallel U'[\rho']$ may be generated in only two ways, by Definition 2.11:

- T or U add new outputs to their respective buffers;
- T reaches an external choice which synchronises with the head of U 's queue, consuming it.

(Note that case *b*. prevents us from resorting to Proposition 5.2, since the latter only considers the *internal* τ -transitions of the behaviours, without synchronisations). Therefore, $\sigma' = \sigma.\sigma''$ for some σ'' deriving from case *a*. above (which proves the “*furthermore...*” part of the statement), and $U'[\rho'] \not\xrightarrow{!a}$: by Definition 5.1, we conclude that $T'[\sigma'] \parallel U'[\rho']$ is still an orphan message configuration, with $!a$ orphan message of $T'[\sigma']$. \square

Proposition C.1 below states that an input behaviour cannot stop interacting after some τ -moves.

Proposition C.1. *If p is an input behaviour, then $p \Rightarrow p'$ implies $p' \not\lesssim \mathbf{0}$.*

Proof. From Definition 5.7, we have that $\exists p'', a. p' \Rightarrow p'' \stackrel{?a}{\rightarrow}$; hence, $p'' \not\lesssim \mathbf{0}$, which in turn implies $p' \not\lesssim \mathbf{0}$. \square

D Proofs for Section 6

Lemma 6.2 ($\sqsubseteq_{\text{U}_{\text{ST}}}$ -induced shapes of session types). *$T \sqsubseteq_{\text{U}_{\text{ST}}} U$ implies either:*

- $T = U = \mathbf{0}$;
- $T \equiv \&_{k \in K} ?a_k.T_k$ and $U \equiv \&_{i \in I} ?a_i.T_i$, with $\emptyset \neq I \subseteq K$ and $\forall i \in I. T_i \sqsubseteq_{\text{U}_{\text{ST}}} U_i$;
- $T \equiv \bigoplus_{k \in K} !a_k.T_k$ and $U \equiv \bigoplus_{i \in I} !a_i.T_i$, with $\emptyset \neq K \subseteq I$, and $\forall k \in K. T_k \sqsubseteq_{\text{U}_{\text{ST}}} U_k$.

Proof. In the following, when no ambiguity arises, we will write \sqsubseteq instead of $\sqsubseteq_{\text{U}_{\text{ST}}}$.

When $T \sqsubseteq U$, by Definition 6.1 we know that $\forall V. U \bowtie V \implies T \bowtie V$. By Theorem 4.9, this is equivalent to saying that $\forall V. U \dashv V \implies T \dashv V$. Hence, the possible forms of the pairs U, V and T, V (up-to unfolding) are given by Proposition 4.3 (case for \dashv). Therefore, by cases on U , we have:

- $U = \mathbf{0}$. Then, we have $V = \mathbf{0}$, and $T = \mathbf{0}$.
- $U = \&_{i \in I} ?a_i.U_i$, with $I \neq \emptyset$. Then, we have $V = \bigoplus_{j \in J} !a_j.V_j$, with $\emptyset \neq J \subseteq I$, and $\forall j \in J. U_j \dashv V_j$ (i.e., $U_j \bowtie V_j$). Since $T \dashv V$, we also have $T = \&_{k \in K} ?a_k.T_k$, with $\emptyset \neq J \subseteq K$, and $\forall j \in J. T_j \dashv V_j$ (i.e., $T_j \bowtie V_j$). Furthermore, we have $I \subseteq K$: otherwise, $\exists i \in I. i \notin K$, and if we take a V such that $J = I$, we would have the contradiction $J \not\subseteq K$. Finally, since (as seen above) $\forall j \in J$ we have $U_j \bowtie V_j$ and $T_j \bowtie V_j$, we conclude $\forall j \in J. T_j \sqsubseteq U_j$;

$U = \bigoplus_{i \in I} ?a_i . U_i$, with $I \neq \emptyset$. Then, we have $V = \&_{j \in J} !a_j . V_j$, with $I \subseteq J$, and $\forall i \in I . U_i \dashv\vdash V_i$ (i.e., $U_i \bowtie V_i$). Since $T \dashv\vdash V$, we also have $T = \bigoplus_{k \in K} ?a_k . T_k$, with $\emptyset \neq K \subseteq J$, and $\forall k \in K . T_k \dashv\vdash V_k$ (i.e., $T_k \bowtie V_k$). Furthermore, we have $K \subseteq I$: otherwise, $\exists k \in K . k \notin I$, and if we take a V such that $J = I$, we would have the contradiction $K \not\subseteq J$. Finally, since (as seen above) $\forall k \in K$ we have $U_k \bowtie V_k$ and $T_k \bowtie V_k$, we conclude $\forall k \in K . T_k \sqsubseteq U_k$. \square

Lemma 6.3. $T \sqsubseteq U \iff T \sqsubseteq_{\mathbb{U}_{\text{ST}}} U$.

Proof. (\implies). Follows from Definition 6.1, since $\mathbb{U}_{\text{ST}} \subseteq \mathbb{U}$.
 (\impliedby). Let:

$\mathcal{R} = \{(T, r) \mid \exists U . T \sqsubseteq_{\mathbb{U}_{\text{ST}}} U \wedge U \bowtie r\} \cup \text{symmetric}$

We show that \mathcal{R} is a symmetric I/O compliance relation. For each $(T, r) \in \mathcal{R}$, there exists U such that $T \sqsubseteq_{\mathbb{U}_{\text{ST}}} U$ and $U \bowtie r$. For all such U , we have (from the proof of Lemma 4.5):

- a. $U \Downarrow^! \subseteq \text{co}(r \Downarrow^?)$ and $r \Downarrow^! \subseteq \text{co}(U \Downarrow^?)$
 and $(U \Downarrow^! = \emptyset \wedge r \Downarrow^? \neq \emptyset \implies r \Downarrow^! \neq \emptyset)$
 and $(r \Downarrow^! = \emptyset \wedge U \Downarrow^? \neq \emptyset \implies U \Downarrow^! \neq \emptyset)$;
- b. $U \xrightarrow{\ell} U' \wedge r \xrightarrow{\text{co}(\ell)} r' \implies U' \bowtie r'$;
- c. $U \xrightarrow{\tau} U' \implies U' \bowtie r$;
- d. $r \xrightarrow{\tau} r' \implies U \bowtie r'$.

Before proceeding, we also observe that, since $T \sqsubseteq_{\mathbb{U}_{\text{ST}}} U$ by hypothesis, by Lemma 6.2 and $U \bowtie r$ we have the following possibilities (up-to unfolding):

1. $U = T = \mathbf{0}$. Therefore, in this case, $r \Downarrow^! = r \Downarrow^? = r \Downarrow^! = \emptyset$;
2. $U = \&_{i \in I} ?a_i . T_i$ and $T = \&_{k \in K} ?a_k . T_k$, with $\emptyset \neq I \subseteq K$ and $\forall i \in I . (T_i, U_i) \in \sqsubseteq_{\mathbb{U}_{\text{ST}}}$. Therefore, in this case, $\emptyset \neq r \Downarrow^! \subseteq \text{co}(U \Downarrow^?) \subseteq \text{co}(T \Downarrow^?)$. Furthermore, $\forall i \in I . T \xrightarrow{?a_i} T_i$ and $r \xrightarrow{!a_i} r'$ implies $U_i \bowtie r'$ (from $U \xrightarrow{?a_i} U_i$ and Proposition B.2), while $\forall j \in K \setminus I . r \xrightarrow{!a_j} r'$ (from $U \bowtie r$ and Proposition B.3);
3. $U = \bigoplus_{i \in I} !a_i . T_i$ and $T = \bigoplus_{k \in K} !a_k . T_k$, with $\emptyset \neq K \subseteq I$, and $\forall k \in K . (T_k, U_k) \in \sqsubseteq_{\mathbb{U}_{\text{ST}}}$. Therefore, in this case, $T \Downarrow^! \subseteq U \Downarrow^! \subseteq \text{co}(r \Downarrow^?) \neq \emptyset$. Furthermore, $\forall k \in K . T \xrightarrow{!a_k} T_k$ implies $r \xrightarrow{?a_k} r'$ (from $U \xrightarrow{!a_k} U_k$ and Proposition B.3) and $\forall r' . r \xrightarrow{?a_k} r'$ implies $U_k \bowtie r'$ (from $U \bowtie r$ and Proposition B.2). Finally, by Proposition B.3, we have $r \Downarrow^! = \emptyset$.

We can now prove that \mathcal{R} is a I/O compliance relation, examining the clauses of Definition 4.4:

- a: we need to show that $T \Downarrow^! \subseteq \text{co}(r \Downarrow^?)$ and $r \Downarrow^! \subseteq \text{co}(T \Downarrow^?)$ and $(T \Downarrow^! = \emptyset \wedge r \Downarrow^? \neq \emptyset \implies r \Downarrow^! \neq \emptyset)$ and $(r \Downarrow^! = \emptyset \wedge T \Downarrow^? \neq \emptyset \implies T \Downarrow^! \neq \emptyset)$: these requirements are satisfied in all cases 1–3. above;
- b: assume that $T \xrightarrow{\ell} T'$ and $r \xrightarrow{\text{co}(\ell)} r'$. From cases 2. and 3. above, it follows that $\exists U' . U \xrightarrow{\ell} U'$ and $T' \sqsubseteq_{\mathbb{U}_{\text{ST}}} U' \bowtie r'$. Therefore, we conclude $(T', r') \in \mathcal{R}$ and, by symmetry of \mathcal{R} , $(r', T') \in \mathcal{R}$;
- c: assume that $T \xrightarrow{\tau} T'$. By the semantics in Definition 2.10 we are in case 3. above (i.e., both T and U are internal choices with multiple branches). Hence, $\exists U' . U \xrightarrow{\tau} U'$ and $T' \sqsubseteq_{\mathbb{U}_{\text{ST}}} U' \bowtie r$ (by Proposition B.1). Therefore, we conclude $(T', r) \in \mathcal{R}$ and, by symmetry of \mathcal{R} , $(r, T') \in \mathcal{R}$;
- d: assume that $r \xrightarrow{\tau} r'$. By Proposition B.1 we have $T \sqsubseteq_{\mathbb{U}_{\text{ST}}} U \bowtie r'$. Therefore, we conclude $(T, r') \in \mathcal{R}$ and, by symmetry of \mathcal{R} , $(r', T) \in \mathcal{R}$;

Hence, \mathcal{R} is a symmetric I/O compliance relation. Now, we observe that for all T, U and $r \in \mathbb{U}$, $T \sqsubseteq_{\mathbb{U}_{\text{ST}}} U$ and $U \bowtie r$ imply $(T, r) \in \mathcal{R} \ni (r, T)$, and so $T \bowtie r$. By Definition 6.1, we conclude $T \sqsubseteq U$. \square

Lemma 6.7. Let $\mathring{\mathcal{R}}$ be a set of I/O simulations. Then, $\bigcup \mathring{\mathcal{R}}$ is an I/O simulation.

Proof. We show that $\forall (p, q) \in (\bigcup \mathring{\mathcal{R}})$, all conditions in Definition 6.4 hold. We simply notice that when $p (\bigcup \mathring{\mathcal{R}}) q$, then there exists an I/O simulation $\mathring{\mathcal{R}} \subseteq (\bigcup \mathring{\mathcal{R}})$ such that $p \mathring{\mathcal{R}} q$, for some predictive set \mathbb{Q} ; using such a predictive set, clauses a–b of Definition 6.4 hold — and moreover, the pairs of reduces $(p', q') \in \mathring{\mathcal{R}}$ from clauses c–e also belong to $(\bigcup \mathring{\mathcal{R}})$. \square

D.1 On $\overset{\circ}{\leq}$ as a preorder for \mathbb{U}

Lemma D.1. $\overset{\circ}{\leq}$ is reflexive.

Proof. Consider the identity relation: $\mathring{\mathcal{R}} = \{(p, p) \mid p \in \mathbb{U}\}$. We can easily verify that that $\mathring{\mathcal{R}}$ is an I/O simulation, with $\{p\}$ being the predictive supporting each pair $p \mathring{\mathcal{R}} p$. \square

Lemma D.2 ($\overset{\circ}{\leq}$ and τ -moves (I)). Let $p \overset{\circ}{\leq} q$, with predictive set \mathbb{Q} . Then, $p \Rightarrow p'$ implies $p' \overset{\circ}{\leq} q$, with predictive set \mathbb{Q}' such that $\mathbb{Q} \Rightarrow \mathbb{Q}'$.

Proof. We proceed by induction on the length of the sequence of τ -transitions in $p \Rightarrow p'$. The base case ($n = 0$) is trivial. For the inductive case, let $p \Rightarrow p^* \xrightarrow{\tau} p'$. By the induction hypothesis, we have $p^* \overset{\circ}{\leq} q$ with predictive set \mathbb{Q}^* such that $\mathbb{Q} \Rightarrow \mathbb{Q}^*$. To show that $p' \overset{\circ}{\leq} q$, we observe that, (by item c of Definition 6.4) $p^* \xrightarrow{\tau} p'$ implies that $\exists q' . \mathbb{Q}^* \Rightarrow q' \wedge p' \overset{\circ}{\leq} q'$; furthermore, $p' \overset{\circ}{\leq} q'$ is supported by some predictive set \mathbb{Q}' such that $q' \Rightarrow \mathbb{Q}'$. Let now $\mathring{\mathcal{R}} = \overset{\circ}{\leq} \cup \{(p', q)\}$: we show that $\mathring{\mathcal{R}}$ is an I/O simulation. This claim trivially holds for the subset $\overset{\circ}{\leq} \subseteq \mathring{\mathcal{R}}$, and thus we only need to check whether the clauses of Definition 6.4 hold for the additional pair (p', q) . Since $q \Rightarrow q'$ and $q' \Rightarrow \mathbb{Q}'$, we have $q \Rightarrow \mathbb{Q}'$. Then, using \mathbb{Q}' as a predictive set for (p', q) , we have:

- item a: $p' \Downarrow^! = \emptyset \implies \mathbb{Q}' \Downarrow^! = \emptyset$ (since \mathbb{Q}' is a predictive set for $p' \overset{\circ}{\leq} q'$);
- item b: $\mathbb{Q}' \Downarrow^?? \subseteq p' \Downarrow^?$ and $\mathbb{Q}' \Downarrow^? = \emptyset \implies p' \Downarrow^? = \emptyset$ (since \mathbb{Q}' is a predictive set for $p' \overset{\circ}{\leq} q'$);
- item c let $p' \xrightarrow{\tau} p''$. Since $p' \overset{\circ}{\leq} q'$, we have that $\exists q'' . \mathbb{Q}' \Rightarrow q'' \wedge p'' \overset{\circ}{\leq} q''$, which is the thesis;
- item d let $p' \xrightarrow{!a} p''$. Since $p' \overset{\circ}{\leq} q'$, we have that $\exists q'' . \mathbb{Q}' \xrightarrow{!a} q'' \wedge p'' \overset{\circ}{\leq} q''$, which is the thesis;
- item e let $p' \xrightarrow{?a} p'' \wedge \mathbb{Q}' \xrightarrow{??a}$. Since $p' \overset{\circ}{\leq} q'$, we have that $\exists q'' . \mathbb{Q}' \xrightarrow{?a} q'' \wedge p'' \overset{\circ}{\leq} q''$, which is the thesis.

Therefore, $\mathring{\mathcal{R}}$ is an I/O simulation, and $(p', q) \in \mathring{\mathcal{R}}$.

We conclude by observing that, from $\mathbb{Q} \Rightarrow \mathbb{Q}^*$, $\mathbb{Q}^* \Rightarrow q'$ and $q' \Rightarrow \mathbb{Q}'$, we have $\mathbb{Q} \Rightarrow \mathbb{Q}'$. \square

Lemma D.3 ($\overset{\circ}{\leq}$ and τ -moves (II)). Let $p \overset{\circ}{\leq} q$, with predictive set \mathbb{Q} . Then, $q' \Rightarrow q$ implies $p \overset{\circ}{\leq} q'$, again with predictive set \mathbb{Q} .

Proof. We proceed by induction on the length of the sequence of τ -transitions in $q' \Rightarrow q$. The base case ($n = 0$) is trivial. For the inductive case, let $q' \xrightarrow{\tau} q^* \Rightarrow q$. By the induction hypothesis, we have $p \overset{\circ}{\leq} q^*$, with predictive set \mathbb{Q} . Let now $\mathring{\mathcal{R}} = \overset{\circ}{\leq} \cup \{(p, q')\}$: we show that $\mathring{\mathcal{R}}$ is an I/O simulation. This claim trivially holds for the subset $\overset{\circ}{\leq} \subseteq \mathring{\mathcal{R}}$, and thus we only

need to check whether the clauses of Definition 6.4 hold for the additional pair (p, q') . Since $q' \xrightarrow{\tau} q^*$ and $q^* \Rightarrow \mathbb{Q}$, we have $q' \Rightarrow \mathbb{Q}$. Then, using \mathbb{Q} as a predictive set for (p, q') , we have:

- item *a*: $p\Downarrow^! = \emptyset \implies \mathbb{Q}\Downarrow^! = \emptyset$ (since \mathbb{Q} is a predictive set for $p \preceq q^*$);
- item *b*: $\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^?$ and $\mathbb{Q}\Downarrow^? = \emptyset \implies p\Downarrow^? = \emptyset$ (since \mathbb{Q} is a predictive set for $p \preceq q^*$);
- item *c* let $p \xrightarrow{\tau} p'$. Since $p \preceq q^*$, we have that $\exists q'' . \mathbb{Q} \Rightarrow q'' \wedge p' \preceq q''$, which is the thesis;
- item *d* let $p \xrightarrow{!a} p'$. Since $p \preceq q^*$, we have that $\exists q'' . \mathbb{Q} \xrightarrow{!a} q'' \wedge p' \preceq q''$, which is the thesis;
- item *e* let $p \xrightarrow{?a} p' \wedge \mathbb{Q} \xrightarrow{??a}$. Since $p \preceq q^*$, we have that $\exists q'' . \mathbb{Q} \xrightarrow{?a} q'' \wedge p' \preceq q''$, which is the thesis.

Therefore, $\ddot{\mathbb{R}}$ is an I/O simulation; we conclude by observing that $(p, q') \in \ddot{\mathbb{R}}$. \square

Lemma 6.8. *If $p \preceq q$, $p \Rightarrow p'$ and $q' \Rightarrow q$, then $p' \preceq q'$.*

Proof. By Lemma D.2 we have that, whenever $p \preceq q$ holds, then $p \Rightarrow p'$ implies $p' \preceq q$; therefore, by Lemma D.3, $q' \Rightarrow q$ implies $p' \preceq q'$. \square

D.2 Properties of predictive sets

Proposition D.4. *For all $\mathbb{Q} \subseteq \mathbb{U}$, if $?a \in \mathbb{Q}\Downarrow^{??}$ then:*

- (i) $\forall q \in \mathbb{Q} . q \xrightarrow{??a}$
- (ii) $\forall q' . \mathbb{Q} \Rightarrow q'$ implies $q' \xrightarrow{??a}$.

Proof. Direct consequences of Definition 2.5. \square

Proposition D.5. *If $p \preceq q$ with pred. set \mathbb{Q} , then $\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^{??}$.*

Proof. Assume $\mathbb{Q} \xrightarrow{??a}$, and let p' such that $p \Rightarrow p'$. By Lemma D.2, we have $p' \preceq q$, with a predictive set \mathbb{Q}' such that $\mathbb{Q} \Rightarrow \mathbb{Q}'$. This, by Proposition D.4, means $\mathbb{Q}' \xrightarrow{??a}$, and therefore (by item *b* of Definition 6.4), $p' \xrightarrow{?a}$. Hence, by Definition 2.5, we conclude $p \xrightarrow{??a}$. \square

Proposition D.6. *Let $p \preceq q$ with predictive set \mathbb{Q} . Then, $q \xrightarrow{??a}$ implies $\mathbb{Q} \xrightarrow{??a}$.*

Proof. By Definition 6.4, $\forall q' \in \mathbb{Q} . q \Rightarrow q'$. Therefore, by Definition 2.5, we have $q' \xrightarrow{??a}$, and we conclude $\mathbb{Q} \xrightarrow{??a}$. \square

Lemma D.7 (“Neutral elements” in a predictive set). *Let $p \preceq q$, for some predictive set \mathbb{Q} . Then, for all q_0 such that $q \Rightarrow q_0$ and $q_0\Downarrow^! \subseteq \mathbb{Q}\Downarrow^!$, we have that $\mathbb{Q}_1 = \mathbb{Q} \cup \{q_0\}$ is still a predictive set for $p \preceq q$.*

Proof. Immediate, by noticing that for all weak barbs of q_0 allowed by the hypotheses, we have:

- $\mathbb{Q}\Downarrow^! = \mathbb{Q}_1\Downarrow^!$, and therefore $p\Downarrow^! = \emptyset \implies \mathbb{Q}_1\Downarrow^! = \emptyset$, thus satisfying clause *a* of Definition 6.4.
- $\mathbb{Q}_1\Downarrow^{??} \subseteq \mathbb{Q}\Downarrow^{??}$ — and so \mathbb{Q}_1 does not require more inputs to p w.r.t. \mathbb{Q} , thus satisfying the first part of clause *b* of Definition 6.4. For the second part, we notice that $\mathbb{Q}_1\Downarrow^? = \emptyset$ implies $\mathbb{Q}\Downarrow^? = \emptyset$, which implies $p\Downarrow^? = \emptyset$;
- all states reachable from \mathbb{Q} are still reachable from \mathbb{Q}_1 with the same transitions, thus satisfying clauses *c–d* of Definition 6.4;

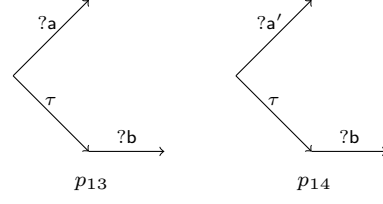


Figure 9: $p_{13} \approx p_{14}$ but $p_{13} \not\approx p_{14}$.

- when $\mathbb{Q}\Downarrow^{??} \ni ?a \notin \mathbb{Q}_1\Downarrow^{??}$, then the premise of clause *e* of Definition 6.4 becomes false; otherwise, when $?a \in \mathbb{Q}_1\Downarrow^{??} \cap \mathbb{Q}\Downarrow^{??}$, we notice (as in the previous point) that all states reachable from \mathbb{Q} are still reachable from \mathbb{Q}_1 with the same transitions. Therefore, in both cases, clause *e* of Definition 6.4 is satisfied. \square

Proposition D.8 (\preceq vs. \approx). $\preceq \subseteq \approx \subseteq \preceq$.

Proof. Consider the following CCS processes, where $a \neq b$:

- let $P = ?a + ?b$ and $Q = ?a$. We have $P \preceq Q$ and $P \not\approx Q$;
- let $P = \tau.?a + \tau.?b$ and $Q = ?a + ?b$. We have $P \approx Q$ and $P \not\preceq Q$. \square

Theorem 6.10. (\mathbb{U}, \preceq) is a preorder.

Proof. Reflexivity follows by Lemma D.1. For transitivity, let:

$$\ddot{\mathbb{R}} = \{(p, r) \mid \exists q . p \preceq q \wedge q \preceq r\}$$

We show that $\ddot{\mathbb{R}}$ is an I/O simulation. Let $(p, r) \in \ddot{\mathbb{R}}$, and let q be such that $p \preceq q$ and $q \preceq r$. Then, let:

- (i) $\ddot{\mathbb{R}}_1$ be an I/O simulation such that $p \ddot{\mathbb{R}}_1 q$, with \mathbb{Q} as predictive set. So, we have $q \Rightarrow \mathbb{Q}$, and:
 - a. $p\Downarrow^! = \emptyset \implies \mathbb{Q}\Downarrow^! = \emptyset$;
 - b. $\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^? \wedge \mathbb{Q}\Downarrow^? = \emptyset \implies p\Downarrow^? = \emptyset$;
 - c. $p \xrightarrow{\tau} p' \implies \exists q' . \mathbb{Q} \Rightarrow q' \wedge p' \ddot{\mathbb{R}}_1 q'$;
 - d. $p \xrightarrow{!a} p' \implies \exists q' . \mathbb{Q} \xrightarrow{!a} q' \wedge p' \ddot{\mathbb{R}}_1 q'$;
 - e. $p \xrightarrow{?a} p' \wedge \mathbb{Q} \xrightarrow{??a} \implies \exists q' . \mathbb{Q} \xrightarrow{?a} q' \wedge p' \ddot{\mathbb{R}}_1 q'$;
- (ii) $\ddot{\mathbb{R}}_2$ be an I/O simulation such that $q \ddot{\mathbb{R}}_2 r$.

In the following, let I index the elements of \mathbb{Q} — i.e., $\mathbb{Q} = \{q_i\}_{i \in I}$. Before proceeding, we highlight some results and definitions that we will reuse throughout this proof.

Proposition D.9. $\forall i \in I$, we have $q_i \preceq r$.

Proof. Since $\forall i \in I . q \Rightarrow q_i$, the statement follows from Lemma D.2. \square

Definition D.10. $\forall i \in I$, we fix \mathbb{R}_i as a predictive set supporting $q_i \preceq r$. Furthermore, we define:

$$\mathbb{R} = \bigcup_{i \in I} \mathbb{R}_i$$

Corollary D.11. $\mathbb{R}\Downarrow^{??} \subseteq \mathbb{Q}\Downarrow^{??}$.

Proof. Let $?a \in \mathbb{R}\Downarrow^{??}$. Then, $?a \in \tilde{r}\Downarrow^{??}$, for all $\tilde{r} \in \mathbb{R}$, and so $?a \in \mathbb{R}_i\Downarrow^{??}$. Since $q_i \preceq r$ (by Proposition D.9) with predictive set \mathbb{R}_i (by Definition D.10), then by Proposition D.5 it follows that $?a \in q_i\Downarrow^{??}$. Since the above holds for all $i \in I$, we conclude that $?a \in \mathbb{Q}\Downarrow^{??}$. \square

We now show that \mathbb{R} is a predictive set for the pair $(p, r) \in \ddot{\mathbb{R}}$, according to Definition 6.4:

item *a*: assume that $p\Downarrow^! = \emptyset$. Then, by item (i)*a*., we have $\mathbb{Q}\Downarrow^! = \emptyset$ — and therefore, $q_i\Downarrow^! = \emptyset$ for all $i \in I$. Since $q_i \leq r$, by item *a* of Definition 6.4, this implies that $\mathbb{R}_i\Downarrow^! = \emptyset$, for all $i \in I$. Therefore $\mathbb{R}\Downarrow^! = \emptyset$.

item *b*: for the first part of the item, from Corollary D.11, we have $\mathbb{R}\Downarrow^{??} \subseteq \mathbb{Q}\Downarrow^{??}$. Furthermore, by item (i)*b*., we have $\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^?$. So, we conclude $\mathbb{R}\Downarrow^{??} \subseteq p\Downarrow^?$. For the second part of item *b*, assume $\mathbb{R}\Downarrow^? = \emptyset$: for all $i \in I$, this implies $\mathbb{R}_i\Downarrow^? = \emptyset$; by item *b* of Definition 6.4, we have $\forall i \in I. q_i\Downarrow^? = \emptyset$, which in turn implies $\mathbb{Q}\Downarrow^? = \emptyset$; and from item (i)*b*., we have $p\Downarrow^? = \emptyset$. Therefore, we conclude $p\Downarrow^? = \emptyset$.

item *c*: we have to show that, whenever $p \xrightarrow{\tau} p'$, then $\exists r'. \mathbb{R} \Rightarrow r' \wedge (p', r') \in \mathcal{R}$. From item (i)*c*., we know that whenever $p \xrightarrow{\tau} p', \exists q'. \mathbb{Q} \Rightarrow q' \wedge (p', q') \in \mathcal{R}_1$; therefore, for some $i \in I$, $q_i \Rightarrow q'$. Now, from Lemma D.3, we have $p' \leq q_i$. Since (by Proposition D.9) we also have $q_i \leq r$, we choose $r' = r$, and we conclude $(p', r') \in \mathcal{R}$.

item *d*: we have to show that, whenever $p \xrightarrow{!a} p'$, then $\exists r'. \mathbb{R} \xrightarrow{!a} r'$ and $(p', r') \in \mathcal{R}$. By item (i)*d*., we know that whenever $p \xrightarrow{!a} p'$, then $\exists i \in I, q_*, q'_*, q'$ such that $\mathbb{Q} \ni q_i \Rightarrow q_* \xrightarrow{!a} q'_* \Rightarrow q'$ and $p' \mathcal{R}_1 q'$. Since $p' \leq q'$ and $q'_* \Rightarrow q'$, then by Lemma D.3 it follows that $p' \leq q'_*$. By Proposition D.9 and Definition D.10, we have $q_i \leq r$ with some predictive set $\mathbb{R}_i \subseteq \mathbb{R}$; and by Lemma D.2, we also have $q_* \leq r$ with some predictive set \mathbb{R}_* such that $\mathbb{R}_i \Rightarrow \mathbb{R}_*$ — and therefore (by item *d* of Definition 6.4):

$$\exists r'' . \mathbb{R}_i \Rightarrow \mathbb{R}_* \xrightarrow{!a} r'' \wedge q'_* \leq r'' \quad (12)$$

Combining $\mathbb{R} \Rightarrow \mathbb{R}_i$ and Equation (12) above, we obtain:

$$\exists r'' . \mathbb{R} \xrightarrow{!a} r'' \wedge q'_* \leq r''$$

Let $r' = r''$. Since $p' \leq q'_*$ and $q'_* \leq r'$, we conclude that $(p', r') \in \mathcal{R}$.

item *e*: we have to show that, whenever $p \xrightarrow{?a} p'$ and $\mathbb{R} \xrightarrow{??a}$, then $\exists r'. \mathbb{R} \xrightarrow{?a} r'$ and $(p', r') \in \mathcal{R}$. Assume that $p \xrightarrow{?a} p'$ and $\mathbb{R} \xrightarrow{??a}$. By Corollary D.11, we have $\mathbb{Q} \xrightarrow{??a}$. Therefore, by item (i)*e*., above, $\exists i \in I, q_*, q'_*, q'$ such that $\mathbb{Q} \ni q_i \Rightarrow q_* \xrightarrow{?a} q'_* \Rightarrow q'$ and $p' \mathcal{R}_1 q'$. By Proposition D.9 and Definition D.10, we have $q_i \leq r$ with some predictive set $\mathbb{R}_i \subseteq \mathbb{R}$; and by Lemma D.2, we also have $q_* \leq r$ with some predictive set \mathbb{R}_* such that $\mathbb{R}_i \Rightarrow \mathbb{R}_*$. Therefore, by item *e* of Definition 6.4:

$$\mathbb{R}_* \xrightarrow{??a} \text{ implies } \exists r'' . \mathbb{R}_i \Rightarrow \mathbb{R}_* \xrightarrow{?a} r'' \wedge q'_* \leq r'' \quad (13)$$

Finally, we notice that since $\mathbb{R} \xrightarrow{??a}$ implies $\mathbb{R}_i \xrightarrow{??a}$, we have $\mathbb{R}_* \xrightarrow{??a}$; this, combined with $\mathbb{R} \Rightarrow \mathbb{R}_i$ and Equation (13) above, gives us:

$$\exists r'' . \mathbb{R} \xrightarrow{?a} r'' \wedge q'_* \leq r''$$

By Lemma D.2, we have $q' \leq r''$. Let $r' = r''$. Since $p' \leq q'$ and $q' \leq r'$, we conclude that $(p', r') \in \mathcal{R}$. \square

Theorem 6.11. $\approx \subseteq \tilde{\approx}$

Proof. To show that $\approx \neq \tilde{\approx}$, consider Figure 9: we have that $p_{13} \tilde{\approx} p_{14}$, but $p_{13} \not\approx p_{14}$. To show that $\approx \subseteq \tilde{\approx}$, since the weak bisimulation relation is symmetric, it is enough to prove that it is an I/O simulation. Let $p \approx q$. We have that $p \xrightarrow{\ell} p'$ implies $\exists q'. q \xrightarrow{\ell} q' \wedge p' \approx q'$, and the converse holds for the

ℓ -transitions emanating from q . From this, it is immediate to see that $p\Downarrow^? = q\Downarrow^?$ and $p\Downarrow^! = q\Downarrow^!$, and using the former in an inductive argument we also obtain $p\Downarrow^{??} = q\Downarrow^{??}$. Hence, for all $(p, q) \in \approx$, we can satisfy clauses *a–e* in Definition 6.4 by taking as predictive set $\mathbb{Q} = \{q\}$. \square

Theorem 6.12. $p \leq q \circ r \implies p \circ r$, for $\circ \in \{\dot{\rightarrow}, \dot{\bowtie}\}$.

Proof. We first prove the statement for $\circ = \dot{\rightarrow}$. Let:

$$\mathcal{R} = \{(p, r) \mid \exists q . p \leq q \wedge q \dot{\rightarrow} r\}$$

We will show that \mathcal{R} is an I/O compliance relation between p and r (in the inverse order). Let $(p, r) \in \mathcal{R}$, via some q such that $p \leq q$ and $r \dot{\rightarrow} q$. Now, let \mathbb{Q} be the predictive set supporting the pair $(p, q) \in \leq$, and let us examine the clauses of Definition 4.4.

item *a* From $p \leq q$ (item *b* of Definition 6.4) we have $\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^?$; furthermore, from $r \dot{\rightarrow} q$ (by Proposition B.3) we have $r\Downarrow^! \subseteq \text{co}(q\Downarrow^{??})$, and from Proposition D.6 we obtain:

$$r\Downarrow^! \subseteq \text{co}(\mathbb{Q}\Downarrow^{??}) \quad (14)$$

Furthermore, since $\forall q' \in \mathbb{Q} . q \Rightarrow q'$, from $r \dot{\rightarrow} q$ and Proposition B.1 we have $\forall q' \in \mathbb{Q} . r \dot{\rightarrow} q'$. Hence, by item *b* of Definition 6.4:

$$\forall q' \in \mathbb{Q} . (r\Downarrow^! = \emptyset \wedge r\Downarrow^? \neq \emptyset \implies \emptyset \neq q'\Downarrow^! \subseteq \text{co}(r\Downarrow^?))$$

and therefore:

$$r\Downarrow^! = \emptyset \wedge r\Downarrow^? \neq \emptyset \implies \emptyset \neq \mathbb{Q}\Downarrow^! \subseteq \text{co}(r\Downarrow^?) \quad (15)$$

From $p \leq q$, by Definition 6.4 we have:

- $p\Downarrow^! = \emptyset \implies \mathbb{Q}\Downarrow^! = \emptyset$ (item *a* of Definition 6.4), and therefore $\mathbb{Q}\Downarrow^! \neq \emptyset \implies p\Downarrow^! \neq \emptyset$;
- $p\Downarrow^! \subseteq \mathbb{Q}\Downarrow^!$ (item *d*)
- $\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^?$ (item *b*).

Summing up:

$$\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^? \text{ and } \mathbb{Q}\Downarrow^! \neq \emptyset \implies \emptyset \neq p\Downarrow^! \subseteq \mathbb{Q}\Downarrow^! \quad (16)$$

and combining Equation (16) with Equations (14) and (15), we conclude:

$$(r\Downarrow^! \subseteq \text{co}(p\Downarrow^?)) \wedge (r\Downarrow^! = \emptyset \wedge r\Downarrow^? \neq \emptyset \text{ implies } \emptyset \neq p\Downarrow^! \subseteq \text{co}(r\Downarrow^?))$$

item *b* We have to show that $p \xrightarrow{\ell} p'$ and $r \xrightarrow{\text{co}(\ell)} r'$ implies $(p', r') \in \mathcal{R}$. Assuming the premise, we have two cases, depending on whether ℓ is an input or an output action:

- $\ell = !a$. Then, by item *d* of Definition 6.4, $\exists q'. \mathbb{Q} \xrightarrow{!a} q' \wedge p' \leq q'$. Now, from $q \dot{\rightarrow} r$ and Proposition B.2 we have $q' \dot{\rightarrow} r'$: we conclude $(p', r') \in \mathcal{R}$;
- $\ell = ?a$. Then, $r \xrightarrow{!a} r'$; hence, from $q \dot{\rightarrow} r$ and Proposition B.3 we have $q \xrightarrow{??a}$, and by Proposition D.6 we obtain $\mathbb{Q} \xrightarrow{??a}$. Thus, by item *e* of Definition 6.4, $\exists q'. \mathbb{Q} \xrightarrow{?a} q' \wedge p' \leq q'$. Now, from $q \dot{\rightarrow} r$ and Proposition B.2 we have $q' \dot{\rightarrow} r'$: we conclude $(p', r') \in \mathcal{R}$;

item *c* From $p \leq q$ and Lemma D.2, we have that $p \xrightarrow{\tau} p'$ implies $p' \leq q$: we conclude $(p', r) \in \mathcal{R}$;

item *d* From $q \dot{\rightarrow} r$, by item *d* of Definition 4.4, we know that $r \xrightarrow{\tau} r'$ implies $q \dot{\rightarrow} r'$: we conclude $(p, r') \in \mathcal{R}$.

This concludes the proof for $\circ = \dot{\rightarrow}$.

The proof for $\circ = \dot{\bowtie}$ follows a similar approach, but this time we let:

$$\mathcal{R} = \{(p, r) \mid \exists q . p \leq q \wedge q \dot{\bowtie} r\} \quad (17)$$

Now, we prove that \mathcal{R} is a symmetric I/O compliance relation. Since $\dot{\bowtie} = \dot{\succ} \cap \dot{\prec}$ (by Lemma 4.5), most of the proof is already developed above, when $\circ = \dot{\succ}$. The only difference is that, in the *symmetric* version of item *a* of Definition 4.4, we have the following *additional* clause, introduced by $\dot{\prec}$, that needs to be satisfied:

$$(p\Downarrow^! \subseteq \text{co}(r\Downarrow^?) \wedge (p\Downarrow^! = \emptyset \wedge p\Downarrow^? \neq \emptyset \text{ implies } \emptyset \neq r\Downarrow^! \subseteq \text{co}(p\Downarrow^?))) \quad (18)$$

We proceed by proving such a clause, for each $(p, r) \in \mathcal{R}$ and corresponding q from Equation (17). From $p \leq q$ (item *d* of Definition 6.4) we have $p\Downarrow^! \subseteq \mathbb{Q}\Downarrow^! \subseteq q\Downarrow^!$, and from $q \dot{\prec} r$ (item *a* of Definition 4.4) we have $q\Downarrow^! \subseteq \text{co}(r\Downarrow^?)$; therefore,

$$p\Downarrow^! \subseteq \text{co}(r\Downarrow^?) \quad (19)$$

Furthermore, from $q \dot{\prec} r$ (item *a* of Definition 4.4) we have:

$$q\Downarrow^! = \emptyset \wedge q\Downarrow^? \neq \emptyset \implies \emptyset \neq r\Downarrow^! \subseteq \text{co}(q\Downarrow^?) \quad (20)$$

If the premise holds, from $q \dot{\prec} r$ and Proposition B.5, we have $r\Downarrow^! \subseteq \text{co}(q\Downarrow^{??})$, and from Proposition D.6 we have $q\Downarrow^{??} \subseteq \mathbb{Q}\Downarrow^{??}$ — and therefore $r\Downarrow^! \subseteq \text{co}(\mathbb{Q}\Downarrow^{??})$. Combining these observations with Equation (20), we obtain:

$$q\Downarrow^! = \emptyset \wedge q\Downarrow^? \neq \emptyset \implies \emptyset \neq r\Downarrow^! \subseteq \text{co}(\mathbb{Q}\Downarrow^{??}) \quad (21)$$

Since $\forall q' \in \mathbb{Q}. q \Rightarrow q'$, by Proposition B.1 we have $\forall q' \in \mathbb{Q}. q' \dot{\prec} r$. Then, from Equation (21) we have:

$$\forall q' \in \mathbb{Q}. q'\Downarrow^! = \emptyset \wedge q'\Downarrow^? \neq \emptyset \implies \emptyset \neq r\Downarrow^! \subseteq \text{co}(\mathbb{Q}\Downarrow^{??}) \quad (22)$$

and therefore:

$$\mathbb{Q}\Downarrow^! = \emptyset \wedge \mathbb{Q}\Downarrow^? \neq \emptyset \implies \emptyset \neq r\Downarrow^! \subseteq \text{co}(\mathbb{Q}\Downarrow^{??}) \quad (23)$$

From $p \leq q$, by Definition 6.4 we have:

- $p\Downarrow^! = \emptyset \implies \mathbb{Q}\Downarrow^! = \emptyset$ (item *a*);
- $\mathbb{Q}\Downarrow^{??} \subseteq p\Downarrow^?$ (item *b*), and therefore $\text{co}(\mathbb{Q}\Downarrow^{??}) \subseteq \text{co}(p\Downarrow^?)$;
- $\mathbb{Q}\Downarrow^? = \emptyset \implies p\Downarrow^? = \emptyset$ (item *b*), and therefore $p\Downarrow^? \neq \emptyset \implies \mathbb{Q}\Downarrow^? \neq \emptyset$.

Summing up:

$$p\Downarrow^! = \emptyset \wedge p\Downarrow^? \neq \emptyset \implies \mathbb{Q}\Downarrow^! = \emptyset \wedge \mathbb{Q}\Downarrow^? \neq \emptyset \wedge \text{co}(\mathbb{Q}\Downarrow^{??}) \subseteq \text{co}(p\Downarrow^?) \quad (24)$$

and combining Equation (24) with Equations (16) and (19), we conclude:

$$(p\Downarrow^! \subseteq \text{co}(r\Downarrow^?) \wedge (p\Downarrow^! = \emptyset \wedge p\Downarrow^? \neq \emptyset \text{ implies } \emptyset \neq r\Downarrow^! \subseteq \text{co}(p\Downarrow^?)))$$

which corresponds to Equation (18).

The proofs for the remaining items *b–d* can be obtained from the ones provided for $\circ = \dot{\succ}$, simply by replacing all occurrences of $\dot{\succ}$ with $\dot{\bowtie}$. We conclude that the statement also holds for $\circ = \dot{\bowtie}$. \square

Theorem 6.13. (a) $\dot{\leq} \subseteq \sqsubseteq$. (b) $T \dot{\leq} U$ iff $T \sqsubseteq U$.

Proof. For item (a), if $p \dot{\leq} q$, then by Theorem 6.12 we have that $\forall r. q \dot{\bowtie} r \implies p \dot{\bowtie} r$, i.e. $p \sqsubseteq q$ according to Definition 6.1. To prove that the inclusion is strict, recall the behaviour p_5 from Example 4.7. Since p_5 does not admit I/O compliant behaviours, it vacuously follows that $r \sqsubseteq p_5$, for all r . In particular, let r be the behaviour which loops onto itself via a !a -transition, and assume, by contradiction, that $r \dot{\leq} p_5$ (say, via predictive set \mathbb{Q}). By clause *d* of Definition 6.4, there exists p' such that $\mathbb{Q} \xrightarrow{\text{!a}} p'$ — contradiction, since $\mathbb{Q}\Downarrow^! \subseteq p_5\Downarrow^! = \emptyset$.

For item (b), direction \Leftarrow , let $T \sqsubseteq U$. By Lemma 6.3, we have $T \sqsubseteq_{\text{U}_{\text{ST}}} U$, hence by Lemma 6.2 we can reason (up-to unfolding) by cases on the syntax T and U , verifying that (T, U) satisfies all clauses in Definition 6.4, via predictive set $\mathbb{Q} = \{U\}$. The \Rightarrow direction is already proved by item (a). \square

Lemma 6.15. $T \dot{\leq} U \dot{\prec} V$ implies $T \dot{\prec} V$.

Proof. We adapt the proof of Theorem 6.12 as follows:

- for clause *a* of Definition 4.4, we can proceed as in the proof of Equation (18) on page 34, which only relies on $\dot{\prec}$;
- for clause *b*, the proof of Equation (18) for the sub-case $\ell = ?\text{a}$ (page 33) does not hold for $\dot{\prec}$, because Proposition B.3 cannot be applied. We then deal with such case as follows:
 - Since $V \xrightarrow{\text{!a}} V'$, by Proposition A.3 (item *iii*) then V is equivalent to an internal choice. Then, by Theorem 4.9, $U \dot{\prec} V$ implies $U \dashv V$, and so Proposition 4.3 we have that U is a external choice. By Theorem 6.13 and Lemma 6.3, $T \dot{\leq} U$ implies $T \sqsubseteq_{\text{U}_{\text{ST}}} U$, and so by Lemma 6.2 we have that T is a larger (possibly empty) external choice. Since $T \xrightarrow{?\text{a}}$, such choice is not empty, and so by Lemma 6.2 we have that also U is not empty. Since $V \xrightarrow{\text{!a}}$, then by the second part of clause *a* of Definition 4.4 it follows that $U \xrightarrow{?\text{a}}$. From Definition 2.10, this implies $U \xrightarrow{??\text{a}}$ — which in turn implies $\mathbb{Q} \xrightarrow{??\text{a}}$. Thus, by item *e* of Definition 6.4, $\exists U'. \mathbb{Q} \xrightarrow{??\text{a}} U' \wedge T' \dot{\leq} U'$. Now, from $U \dot{\prec} V$ and Proposition B.2 we have $U' \dot{\prec} V'$: we conclude $(T', V') \in \mathcal{R}$.
 - the proofs of clauses *c* and *d* are unmodified. \square

D.3 I/O simulation and asynchrony

Proposition D.12. Let $p \dot{\leq} q$. Then, for all $w \in (A^!)^*$, $p \xrightarrow{w} p'$ implies $\exists q'. q \xrightarrow{w} q'$ and $p' \dot{\leq} q'$.

Proof. By induction on w . In the base case (when w is empty) we need to prove that $p \Rightarrow p'$ implies $\exists q'. q \Rightarrow q'$ and $p' \dot{\leq} q'$: this follows from Lemma D.2, letting $q' = q$.

For the inductive step, let $w = w'\text{!a}$ (where w' is a sequence of outputs). We have $p \xrightarrow{w'} p''$ and $q \xrightarrow{w'} q''$, with $p'' \dot{\leq} q''$ (by the induction hypothesis); we need to show that:

$$\forall p_0, p_1. p'' \Rightarrow p_0 \xrightarrow{\text{!a}} p_1 \Rightarrow p' \text{ implies } \exists q'. q'' \xrightarrow{\text{!a}} q' \text{ and } p' \dot{\leq} q'$$

Now, by Lemma D.2, we have $p_0 \dot{\leq} q''$; by clause *d* of Definition 6.4, we have that for some predictive set \mathbb{Q} , $p_0 \xrightarrow{\text{!a}} p_1$ implies $\exists q''' . \mathbb{Q} \xrightarrow{\text{!a}} q'''$ and $p_1 \dot{\leq} q'''$; since (by Definition 6.4) $q'' \Rightarrow \mathbb{Q}$, we have $q'' \xrightarrow{\text{!a}} q'''$; finally, by Lemma D.2, we obtain $p' \dot{\leq} q'''$. We conclude by letting $q' = q'''$. \square

Proposition D.13. Let $p \dot{\preceq} q$. Then, $\forall w. p \xrightarrow{w} p'$ and $q \xrightarrow{w} q'$ implies $p' \dot{\preceq} q'$.

Proof. We prove the contrapositive. We have that $\exists w. p \xrightarrow{w} p'$ and $q \xrightarrow{w} q'$ but $p' \not\dot{\preceq} q'$. This means that $\exists w', p''', q'''$, a such that:

- $p' \xrightarrow{w'} p'''$ and $q' \xrightarrow{w'} q'''$ (item 2a of Definition 6.18), and
- $\forall q'' . q''' \xrightarrow{\text{!}^*} q''$ implies $q'' \xrightarrow{\text{!}^*} ?\text{a}$ (item 2b of Definition 6.18),

and $p''' \xrightarrow{\text{!}^*} ?\text{a}$ is false. Then, under the same existential quantifications, we also have:

- $p \xrightarrow{ww'} p'''$ and $q \xrightarrow{ww'} q'''$ (item 2a of Definition 6.18), and
- $\forall q'' . q''' \xrightarrow{\text{!}^*} q''$ implies $q'' \xrightarrow{\text{!}^*} ?\text{a}$ (item 2b of Definition 6.18),

and $p''' \xrightarrow{\text{!}^*} ?\text{a}$ is false. Therefore, we conclude $p \not\dot{\preceq} q$. \square

Proposition D.14. *For all $\sigma, T, T', w \in (A^!)^*$: $T \xrightarrow{w} T'$ implies $\exists \sigma'. T[\sigma] \Rightarrow T'[\sigma']$.*

Proof. We proceed by induction on the length of the sequence of transitions in $T \xrightarrow{w} T'$. The base case is trivial: since we have no transitions, then $T' = T$ and we conclude by letting $\sigma' = \sigma$. For the inductive cases, we have:

- $T \xrightarrow{w} T'' \xrightarrow{\tau} T'$. By the induction hypothesis, we have $\exists \sigma''. T[\sigma] \Rightarrow T''[\sigma'']$. By item (ii) of Proposition A.4, $\exists b. T''[\sigma''] \xrightarrow{\tau} T'[\sigma''.!b]$. We conclude by letting $\sigma' = \sigma''.!b$;
- $T \xrightarrow{w'} T'' \xrightarrow{!b} T'$. By the induction hypothesis, we have $\exists \sigma''. T[\sigma] \Rightarrow T''[\sigma'']$. By item (iii) of Proposition A.4, $T''[\sigma''] \xrightarrow{\tau} T'[\sigma''.!b]$. Choosing $\sigma' = \sigma''.!b$ concludes. \square

Proposition D.15. *If $T[\sigma] \Rightarrow T'[\sigma']$, then $\exists w \in (A^!)^*. T \xrightarrow{w} T'$.*

Proof. We proceed by induction on the length of the sequence of transitions in $T[\sigma] \Rightarrow T'[\sigma']$. The base case is trivial: since we have no transitions, then $T' = T$ and $\sigma' = \sigma$ — and we conclude by letting w be the empty sequence. For the inductive case, let $T[\sigma] \Rightarrow T''[\sigma''] \xrightarrow{\tau} T'[\sigma']$. By the induction hypothesis, there exists a sequence of outputs w' such that $T \xrightarrow{w'} T''$. Now, from $T''[\sigma''] \xrightarrow{\tau} T'[\sigma']$, by Definition 2.11 we have that T'' is equivalent to a non-empty internal choice with some !b-guarded branch, and $\sigma' = \sigma''.!b$; moreover, by Proposition A.4 (items (ii) and (iii)), we have either $T'' \xrightarrow{\tau} T'$ or $T \xrightarrow{!b} T'$ — and therefore, $T'' \xrightarrow{!b} T'$. Hence, from $T \xrightarrow{w'} T'' \xrightarrow{!b} T'$, we conclude by letting $w = w'!b$. \square

Theorem 6.20. *If $T(\circ \cap \preceq?) U$ then $T[] \circ U[]$, for $\circ \in \{\lesssim, \sqsubseteq\}$.*

Proof. Let us define:

$$\ddot{R} = \{(T[\sigma], U[\sigma]) \mid T(\lesssim \cap \preceq?) U\}$$

By the coinduction proof principle, we show that \ddot{R} is an I/O simulation. For each $(T[\sigma], U[\sigma]) \in \ddot{R}$, we define $\mathbb{U} = \{U[\sigma]\}$ as predictive set, and verify the clauses of Definition 6.4:

item a assume $T[\sigma]\Downarrow^? = \emptyset$. Then, by Definition 2.11 we have $\sigma = \epsilon$, and T can only be a (possibly empty) external choice. Since $T \lesssim U$, we have that U is also an external choice (by Theorem 6.13, Lemma 6.3 and Lemma 6.2) — and thus, since $\mathbb{U} = \{U[\sigma]\}$, we conclude $\mathbb{U}\Downarrow^? = \emptyset$;

item b since $T \lesssim U$, from Theorem 6.13, Lemma 6.3 and Lemma 6.2 we can determine that (up-to unfolding) T and U can be either:

- both empty choices, i.e. $T = U = \mathbf{0}$. Then, since $\mathbb{U} = \{U[\sigma]\}$, we have $\emptyset = \mathbb{U}\Downarrow^? \subseteq T[\sigma]\Downarrow^? = \emptyset$ and ($\mathbb{U}\Downarrow^? = \emptyset \implies T[\sigma]\Downarrow^? = \emptyset$);
- both non-empty external choices, with all branches of U included in T . We notice that, by Proposition A.4 (item (i)), we have:
 - $\forall a. T \xrightarrow{?a} \text{iff } T[\sigma] \xrightarrow{?a}$, and
 - $\forall a. U \xrightarrow{?a} \text{iff } U[\sigma] \xrightarrow{?a}$.

Therefore, $\forall a. U[\sigma] \xrightarrow{?a} \text{implies } T[\sigma] \xrightarrow{?a}$. We also notice that, by Definition 2.11, we have:

- $\forall a. U[\sigma] \xrightarrow{?a} \text{iff } U[\sigma] \xrightarrow{??a}$ (i.e., all inputs of external choices are persistent);
- $\forall a. T[\sigma] \xrightarrow{?a} \text{iff } T[\sigma] \xrightarrow{?a}$ (i.e., all weakly reachable inputs are also immediately enabled).

Summing up, $\forall a$ we have:

$$\begin{array}{c} U[\sigma] \xrightarrow{??a} \iff U[\sigma] \xrightarrow{?a} \iff U \xrightarrow{?a} \\ \Downarrow \\ T[\sigma] \xrightarrow{?a} \iff T[\sigma] \xrightarrow{?a} \iff T \xrightarrow{?a} \end{array}$$

and therefore, since $\mathbb{U} = \{U[\sigma]\}$,

$$U[\sigma]\Downarrow^{??} = \mathbb{U}\Downarrow^{??} \subseteq T[\sigma]\Downarrow^?$$

Furthermore, we also have that $\mathbb{U}\Downarrow^? = \emptyset$ implies $T[\sigma]\Downarrow^? = \emptyset$ (vacuously);

- both non-empty internal choices. We first prove $\mathbb{U}\Downarrow^{??} \subseteq T[\sigma]\Downarrow^?$. If $\mathbb{U}\Downarrow^{??} = \emptyset$, the thesis is immediate. Otherwise, assume $\exists a. \mathbb{U} \xrightarrow{??a}$; since $\mathbb{U} = \{U[\sigma]\}$, this holds iff $U[\sigma] \xrightarrow{??a}$ — and by Definition 2.5, this means:

$$\begin{array}{l} \forall U', \sigma'. U[\sigma] \Rightarrow U'[\sigma'] \\ \text{implies } \exists U'', \sigma''. U'[\sigma'] \Rightarrow U''[\sigma''] \xrightarrow{?a} \end{array}$$

By Proposition D.15, each τ -sequence between $U[\sigma]$ and $U'[\sigma']$ above corresponds to some sequence of outputs w such that $U \xrightarrow{w} U'$; similarly, the τ -sequence between $U'[\sigma']$ and $U''[\sigma'']$ above corresponds to some sequence of outputs w' such that $U' \xrightarrow{w'} U''$. Thus,

$$\begin{array}{l} \forall U', w = !b_1, \dots, !b_n. U \xrightarrow{w} U' \\ \text{implies } \exists U'', w' = !c_1, \dots, !c_{n'}. U' \xrightarrow{w'} U'' \xrightarrow{?a} \end{array}$$

and therefore, by Notation 2.1,

$$\forall U'. U \xrightarrow{!}^* U' \text{ implies } \exists U''. U' \xrightarrow{!}^* U'' \xrightarrow{?a} \quad (25)$$

Now, since $T \preceq? U$, we have:

- an empty sequence of actions w_0 matches item 2a of Definition 6.18 for $T \xrightarrow{w_0} T$ and $U \xrightarrow{w_0} U$;
- from Equation (25), we have that $?a$ is “persistent” in U by item 2b of Definition 6.18;
- so, by item 2c of Definition 6.18, $\exists T'. T \xrightarrow{!}^* T' \xrightarrow{?a}$.

Hence, $\exists w'' = !d_1, \dots, !d_m$ such that $T \xrightarrow{w''} T' \xrightarrow{?a}$; and then, by Proposition D.14, we have $\exists \sigma'. T[\sigma] \Rightarrow T'[\sigma'] \xrightarrow{?a}$. Summing up, we have shown that $\forall a, \mathbb{U} \xrightarrow{??a}$

implies $T[\sigma] \xrightarrow{?a}$; we conclude $\mathbb{U}\Downarrow^{??} \subseteq T[\sigma]\Downarrow^?$.

We are left to prove that $\mathbb{U}\Downarrow^? = \emptyset$ implies $T[\sigma]\Downarrow^? = \emptyset$. We first observe that since $\mathbb{U} = \{U[\sigma]\}$, then $\mathbb{U}\Downarrow^? = \emptyset$ implies $U[\sigma]\Downarrow^? = \emptyset$, which in turn gives:

$$\forall U', \sigma''. U[\sigma] \Rightarrow U'[\sigma''] \text{ implies } U'[\sigma'']\Downarrow^? = \emptyset \quad (26)$$

Let us now examine the possible weak transitions of $T[\sigma]$. From Proposition D.15 we know that $\forall T', \sigma'. T[\sigma] \Rightarrow T'[\sigma']$ implies $\exists w = !b_1, \dots, !b_n. T \xrightarrow{w} T'$. Moreover, since $T \lesssim U$, by Proposition D.12 we have that for all such w , $\exists U'. U \xrightarrow{w} U'$ and $T' \lesssim U'$; and by Proposition D.14, $U \xrightarrow{w} U'$ implies $\exists \sigma''. U[\sigma] \Rightarrow U'[\sigma'']$. From this, Equation (26), gives $U'[\sigma'']\Downarrow^? = \emptyset$ — i.e., by item (i) of Proposition A.4, $U'\Downarrow^? = \emptyset$. But then, $T' \lesssim U'$ is supported by some predictive set \mathbb{U}' such that $\mathbb{U}'\Downarrow^? = \emptyset$; and so, by item b of Definition 6.4, we have $T'\Downarrow^? = \emptyset$ — and by item (i) of Proposition A.4, $T'[\sigma']\Downarrow^? = \emptyset$. Thus, we conclude $T[\sigma]\Downarrow^? = \emptyset$.

item c assume $T[\sigma] \xrightarrow{\tau} T'[\sigma']$. Notice that, by Definition 2.11, $T[\sigma]$ can only generate a τ -transition when T is equivalent to an internal choice with some $!a$ -branch, and $!a$ is appended to σ : hence, $\sigma' = \sigma. !a$. Then, by Definition 2.10, we have $T \xRightarrow{!a} T'$. Since $T \dot{\leq} U$, by Proposition D.12 we have $\exists U'', U', U'''. U \Rightarrow U'' \xrightarrow{!a} U' \Rightarrow U'''$ and $T' \dot{\leq} U'''$. By Proposition A.3, U'' is a single-branch internal choice, and by item (iii) of Proposition A.4, $U[\sigma] \xrightarrow{\tau} U'[\sigma. !a] = U'[\sigma']$. Moreover, by Lemma D.3 we have $T' \dot{\leq} U'$, and by Proposition D.13 we have $T' \dot{\leq}_? U'$. Therefore, from the definition of $\dot{\mathbb{U}}$ above, we conclude $\exists U'. \dot{\mathbb{U}} \Rightarrow U'[\sigma'] \wedge T'[\sigma'] \dot{\mathbb{R}} U'[\sigma']$;

item d assume $T[\sigma] \xrightarrow{!a} T'[\sigma']$. Notice that, by the semantics in Definition 2.11, $T[\sigma]$ can only generate a $!a$ -transition when an output is removed from the head of σ , turning it into σ' , without changing T : so, $T' = T$. The same observation holds for $U[\sigma]$. Since $\dot{\mathbb{U}} = \{U\}$, we have $\exists U'. \dot{\mathbb{U}} \xRightarrow{!a} U'[\sigma']$ with $U' = U$, from which we obtain $T = T' \dot{\leq} U'$; moreover, by Proposition D.13 we have $T' \dot{\leq}_? U'$: we conclude $T'[\sigma'] \dot{\mathbb{R}} U'[\sigma']$;

item e assume $T[\sigma] \xrightarrow{?a} T'[\sigma'] \wedge \dot{\mathbb{U}} \xrightarrow{??a}$. Notice that, by the semantics in Definition 2.11, $T[\sigma]$ can only generate a $?a$ -transition when T is an external choice, and σ is unchanged by the reduction — i.e., $\sigma' = \sigma$. Now, since $T \dot{\leq} U$, by Theorem 6.13, Lemma 6.3 and Lemma 6.2 we have that U is an external choice, too — and by Definition 2.10, we can verify that $U \xrightarrow{?a}$ iff $U \xrightarrow{??a}$, and since U is an external choice, by Definition 2.11 we have $U[\sigma] \xrightarrow{?a}$ implies $U[\sigma] \xrightarrow{??a}$. Moreover, since $T \dot{\leq} U$, again by Theorem 6.13, Lemma 6.3 and Lemma 6.2 we also have $\forall U'. T \xrightarrow{?a} T' \wedge U \xrightarrow{?a} U'$ implies $T' \dot{\leq} U'$. Finally, we observe that by Proposition D.13, $\forall U'. T \xrightarrow{?a} T' \wedge U \xrightarrow{?a} U'$ implies $T' \dot{\leq}_? U'$. Therefore, by definition of $\dot{\mathbb{R}}$, we conclude $\exists U'. \dot{\mathbb{U}} \xrightarrow{?a} U'[\sigma'] \wedge T'[\sigma'] \dot{\mathbb{R}} U'[\sigma']$.

Hence, $\dot{\mathbb{R}}$ is an I/O simulation. Thus, $\forall \sigma, T (\dot{\leq} \cap \dot{\leq}_?) U$ implies $T[\sigma] \dot{\mathbb{R}} U[\sigma]$, and so $T[\sigma] \dot{\leq} U[\sigma]$. Then, $T[\dot{\sigma}] \dot{\leq} U[\dot{\sigma}]$. \square

E Proofs for Section 7

Lemma 7.1. *The relations in Table 4 hold in \mathbb{U}_{aCCS} .*

Proof. The transition diagrams of the LHS and RHS of each relation are isomorphic, by Definition 2.16. Then, they are also (strongly/weakly) bisimilar: we conclude by Theorem 6.11. \square

Remark E.1. *In the following, we will only consider restricted I/O simulation relations such that for each pair of elements $(P[\sigma], Q[\rho])$, we have $\sigma = \rho$. This property is assumed for the I/O simulations given by hypothesis, and guaranteed when new I/O simulations are produced as part of a thesis.*

Notation E.2. *We write $\{P[\sigma], Q[\sigma]\}_{\forall \sigma}$ for: $\bigcup_{\sigma} \{P[\sigma], Q[\sigma]\}$*

Lemma E.3. *For all σ , let $P[\sigma] \dot{\leq} Q[\sigma]$ and $R[\sigma] \dot{\leq} S[\sigma]$, with $\text{ins}(P) \cap \text{ins}(R) = \text{ins}(Q) \cap \text{ins}(S) = \emptyset$. Then, $P \mid R[\sigma] \dot{\leq} Q \mid S[\sigma]$.*

Proof. From the hypothesis, let $\dot{\mathbb{R}}_1, \dot{\mathbb{R}}_2$ be I/O simulations such that for all σ , $P[\sigma] \dot{\mathbb{R}}_1 Q[\sigma]$ and $R[\sigma] \dot{\mathbb{R}}_2 S[\sigma]$. Let:

$$\dot{\mathbb{R}} = \left\{ (P' \mid R'[\rho], Q' \mid S'[\rho]) \mid \begin{array}{l} (P'[\rho'], Q'[\rho']) \in \dot{\mathbb{R}}_1 \\ \wedge (R'[\rho''], S'[\rho'']) \in \dot{\mathbb{R}}_2 \end{array} \right\}_{\forall \rho}$$

Note that $\dot{\mathbb{R}}$ is an I/O simulation, where the predictive set for each pair $(P' \mid R'[\rho], Q' \mid S'[\rho])$ is:

$$\{Q'' \mid S''[\rho] \mid Q''[\rho] \in \mathbb{Q} \wedge S''[\rho] \in \mathbb{S}\}$$

where \mathbb{Q}, \mathbb{S} are respectively the predictive sets supporting $P'[\rho'] \dot{\mathbb{R}}_1 Q'[\rho']$ and $R'[\rho''] \dot{\mathbb{R}}_2 S'[\rho'']$. The key observation is that by Proposition D.12, P' 's (resp. R' 's) τ and output moves are matched by Q' (resp. S') — and the corresponding pairs of reducts, being contained in $\dot{\mathbb{R}}_1$ (resp. $\dot{\mathbb{R}}_2$), are also contained in $\dot{\mathbb{R}}$. The same matching also holds for input moves shared by P', Q' (resp. R', S') under clause e of Definition 6.4. The only problematic case is the following:

- $P'[\rho] \xrightarrow{?a}$ and $Q'[\rho] \not\xrightarrow{?a}$;
- $R'[\rho] \xrightarrow{?a}$ and $S'[\rho] \xrightarrow{??a}$;
- hence, $P' \mid R'[\rho] \xrightarrow{?a}$;
- $Q' \mid S'[\rho] \not\xrightarrow{??a}$.

In this case, by clause e of Definition 6.4, the $?a$ -transition of $P' \mid R'[\rho]$ (arising from P') would need to be I/O simulated by some weak $?a$ -transition of $Q' \mid S'[\rho]$ (arising from S') — albeit the $?a$ -transition of $P'[\rho]$ was *not* I/O simulated by $Q'[\rho]$! However, by hypothesis, we have that the inputs of P', R' and Q', S' cannot overlap; therefore, a persistent input of S' (resp. Q'), which is weakly reachable in R' (resp. P'), cannot be enabled in P' (resp. R'): we conclude that the case is impossible. \square

Lemma 7.3. *The rules in Table 5 hold in \mathbb{U}_{aCCS} .*

Proof. Recall that all related behaviours in the statement are paired with a generic buffer $[\sigma]$, and that $P \dot{\leq} Q$ in \mathbb{U}_{aCCS} means: $\forall \sigma. P[\sigma] \dot{\leq} Q[\sigma]$.

For rule (+CTX), we first prove that, when $P[\sigma] \dot{\leq} Q[\sigma]$ (for all σ), then $\ell_{\tau}.P[\sigma] \dot{\leq} \ell_{\tau}.Q[\sigma]$ (for all σ). We have three cases:

- $\ell_{\tau} = \tau$. Let $\sigma \in (A^!)^*$. Then, $\ell_{\tau}.P[\sigma] \dot{\leq} \ell_{\tau}.Q[\sigma]$ is proved by the I/O simulation $\{(\ell_{\tau}.P[\sigma], \ell_{\tau}.Q[\sigma])\} \cup \dot{\mathbb{R}}$, where $\dot{\mathbb{R}}$ is an I/O simulation such that $P[\rho] \dot{\mathbb{R}} Q[\rho]$ (for all ρ), and $\{\ell_{\tau}.Q[\sigma]\}$ is the predictive set for the additional pair;
- $\ell_{\tau} = ?a$. The proof is similar to the previous case;
- $\ell_{\tau} = !a$. Let $\sigma \in (A^!)^*$. Then, $\ell_{\tau}.P[\sigma] \dot{\leq} \ell_{\tau}.Q[\sigma]$ is proved by the I/O simulation $\{(\ell_{\tau}.P[\sigma], \ell_{\tau}.Q[\sigma])\} \cup \dot{\mathbb{R}}$, where $\dot{\mathbb{R}}$ is an I/O simulation such that $P[\rho. !a] \dot{\mathbb{R}} Q[\rho. !a]$ (for all ρ), and $\{\ell_{\tau}.Q[\sigma]\}$ is the predictive set for the additional pair.

Therefore, when for all σ we have $\forall i \in I. P_i[\sigma] \dot{\leq} Q_i[\sigma]$ for some I/O simulation $\dot{\mathbb{R}}_i$ (by the premise of rule (+CTX)), we have that $\sum_{i \in I} \ell_{\tau i}.P_i[\sigma] \dot{\leq} \sum_{i \in I} \ell_{\tau i}.Q_i[\sigma]$ (in the conclusion of rule (+CTX)) holds by the following I/O simulation:

$$\left\{ \left(\sum_{i \in I} \ell_{\tau i}.P_i[\sigma], \sum_{i \in I} \ell_{\tau i}.Q_i[\sigma] \right) \right\}_{\forall \sigma} \cup \bigcup_{i \in I} \dot{\mathbb{R}}_i$$

where each additional pair is supported by the predictive set $\{\sum_{i \in I} \ell_{\tau i}.Q_i[\sigma]\}$.

To prove rule (+ τ), we simply observe that, for all σ , when the premise holds, $\{Q[\sigma]\}$ is a predictive set supporting the conclusion.

For rule (!L), by hypothesis we have $Q \equiv \sum_{i \in I} !c_i.Q_i$, we have two I/O simulations $\dot{\mathbb{R}}_1, \dot{\mathbb{R}}_2$ such that $\forall \sigma'. P'[\sigma'] \dot{\mathbb{R}}_1 !a[\sigma']$ and $\forall \sigma'. P''[\sigma'] \dot{\mathbb{R}}_2 ?b[\sigma']$. Then, for all σ , we have that $P' \mid P''[\sigma] \dot{\leq} !a. ?b + Q[\sigma]$ is proved by the following I/O simulation:

$$\{(P'''[\sigma], !a?b+Q[\sigma])\}_{P''' \in \{P_1|P_2 \mid P' \Rightarrow P_1 \Rightarrow !a+\dots \wedge P' \Rightarrow P_2 \Rightarrow ?b+\dots\}} \cup \ddot{\mathfrak{R}}_1 \cup \ddot{\mathfrak{R}}_2$$

Intuitively, P''' ranges over all the intermediate CCS^- terms that $P' \mid P'''[\sigma]$ may reach via τ transitions, before reaching respectively an $!a$ -summation and $?b$ -summation (which must exist — otherwise, one of the two relations in the hypotheses would be false); then, all such intermediate terms are paired with the RHS of the relation we are proving — and the predictive set for all these additional pairs is $\{?b[\sigma].!a\}$.

For rule (|R), when $P[\sigma] \dot{\leq} ?b.Q[\sigma]$ (for all σ) and $R \equiv \sum_{i \in I} !c_i.R_i$, then we have that $!a.P[\sigma] \dot{\leq} !a + R \mid ?b.Q[\sigma]$ is proved by the following I/O simulation:

$$\{(!a.P[\sigma], !a + R \mid ?b.Q[\sigma])\}_{\forall \sigma} \cup \ddot{\mathfrak{R}}$$

where $\ddot{\mathfrak{R}}$ is an I/O simulation such that $P[\sigma].!a \ddot{\mathfrak{R}} ?b.Q[\sigma].!a$ (which exists by hypothesis). The predictive set for each additional pair is $\{!a + R \mid ?b.Q[\sigma]\}$.

For rule (|LR), when $P \dot{\leq} Q$ and $R \dot{\leq} S$ with $\text{ins}(P) \cap \text{ins}(R) = \text{ins}(Q) \cap \text{ins}(S) = \emptyset$, then $P \mid R[\sigma] \dot{\leq} Q \mid S[\sigma]$ holds by Lemma E.3.

For rule (+INT), where Q is an *output-guarded choice*, let:

$$\ddot{\mathfrak{R}} = \{(P[\sigma], Q[\sigma])\}_{\forall \sigma} \cup \bigcup_{i \in I} \ddot{\mathfrak{R}}_i$$

where $\ddot{\mathfrak{R}}_i$ is an I/O simulation such that $\forall \sigma. P_i[\sigma] \dot{\leq} Q_i[\sigma]$ (which exists by the rule premises), and for a given σ , $\mathbb{Q} = \{Q[\sigma]\}$ is the predictive set for the additional pair $(P[\sigma], Q[\sigma])$. We can verify that $\ddot{\mathfrak{R}}$ is an I/O simulation, which ignores the new branch $!b.Q'$, since its initial τ -move (for enqueueing $!b$) is not matched in the LHS.

For rule (+EXT), Q is an *input-guarded choice*. Note that whenever $j = i$, since (by premises) there exists an I/O simulation such that $P_j[\sigma] \dot{\leq} Q_i[\sigma]$, by rule (+CTX) there is also an I/O simulation such that $?a_j.P_j[\sigma] \dot{\leq} ?a_i.Q_i[\sigma]$: therefore, let $\ddot{\mathfrak{R}}_1$ be the union of all such I/O simulations. Finally, let us consider, for each $k \in K$, an I/O simulation such that $R_k \dot{\leq} Q$, and let $\ddot{\mathfrak{R}}_2$ be the union of these I/O simulations. Then, the relation in the conclusion is proved by the following I/O simulation:

$$\ddot{\mathfrak{R}} = \left\{ \left(\sum_{j \in J} (?a_j.P_j) + \sum_{k \in K} \tau.P_k[\sigma], Q[\sigma] \right) \right\}_{\forall \sigma} \cup \ddot{\mathfrak{R}}_1 \cup \ddot{\mathfrak{R}}_2$$

where the predictive set for each additional pair is $\{Q[\sigma]\}$. \square

Proposition E.4. *Let P be a CCS^- term with some occurrence of X that is output-guarded, and not input-guarded. Let Q be a sequential CCS^- term, with $Q[\sigma] \Downarrow^{??} \neq \emptyset$ (for any σ). Then, $P[Q/X] \dot{\leq} Q$.*

Proof. We prove the statement by contradiction, assuming $P[Q/X] \dot{\not\leq} Q$. This, in particular, implies $P[Q/X][\epsilon] \dot{\not\leq} Q[\epsilon]$. Let $Q_0 = Q$. From the hypotheses on P , there exists a sequence of outputs $!a_1, \dots, !a_n$ and some P' such that $P[Q_0/X][\epsilon] \xrightarrow{!a_1} \dots \xrightarrow{!a_n} Q_0 \mid P'[\epsilon]$. Since $P[Q_0/X][\epsilon] \dot{\not\leq} Q_0[\epsilon]$, by clauses c and d of Definition 6.4, $Q_0[\epsilon]$ must simulate such transitions: hence, reminding Remark E.1, there exists Q_1 such that $Q_0[\epsilon] \xrightarrow{!a_1} \dots \xrightarrow{!a_n} Q_1[\epsilon]$ and $Q_0 \mid P'[\epsilon] \dot{\leq} Q_1[\epsilon]$. But then, the same sequence of transitions can be fired by the LHS of the latter relation, and the RHS must simulate it:

hence, there exists Q_2 such that $Q_1[\epsilon] \xrightarrow{!a_1} \dots \xrightarrow{!a_n} Q_2[\epsilon]$ and $Q_1 \mid P'[\epsilon] \dot{\leq} Q_2[\epsilon]$. By iterating this reasoning, we can see that there exists an infinite sequence of processes $(Q_m)_{m \geq 0}$ such that $Q_m[\epsilon] \xrightarrow{!a_1} \dots \xrightarrow{!a_n} Q_{m+1}[\epsilon]$ and $Q_m \mid P'[\epsilon] \dot{\leq} Q_{m+1}[\epsilon]$: this implies that $Q = Q_0$ is recursive, and for some $k \geq 0$, the outputs being fired by $(Q_m)_{m \geq k}$ occur under some recursive subterms of Q . Notice that the persistent inputs of Q (that exist by the hypothesis $Q[\sigma] \Downarrow^{??} \neq \emptyset$) are never fired throughout the transitions above — i.e., $\forall m \geq 0, Q_m[\epsilon] \Downarrow^{??} \supseteq Q[\epsilon] \Downarrow^{??} \neq \emptyset$. But then, from the sequentiality hypothesis, Q must contain a recursive subterm $\mu_Y Q'$, which in turn must contain a subterm $\sum_{i \in I} Q'_i$ where, for some $j, j' \in I$, Q'_j contains some inputs, and $Q'_{j'}$ has an occurrence of Y that is *not* input-guarded. Hence, we must conclude that Q does *not* respect clause b of Definition 7.6, i.e., Q is *not* a CCS^- term: contradiction. \square

Lemma 7.7. *Let $\mu_X P$ and Q be sequential CCS^- terms. If $P[Q/X] \dot{\leq} Q$, then $\mu_X P \dot{\leq} Q$.*

Proof. If $X \notin \text{fv}(P)$, the thesis trivially holds. Otherwise, when $X \in \text{fv}(P)$, we need to produce an I/O simulation containing the pairs $\{(\mu_X P[\sigma], Q[\sigma])\}_{\forall \sigma}$. Before proceeding, we introduce some technical machinery:

1. we write \underline{Q} (i.e., Q underlined) to “tag” the occurrences of term Q arising in $P[Q/X]$ due to variable substitution: e.g., if $P = ?a + !b.X$ and $Q = ?a$, we have $P[Q/X] = ?a + !b.\underline{?a}$;
2. $P[R/\underline{Q}]$ is the term substitution which replaces each occurrence of \underline{Q} in P with the tagged term \underline{R} : for instance, if $P = ?a + !b.\underline{?a}$ we have $P[R/\underline{?a}] = ?a + !b.\underline{R}$ (notice that the first, untagged occurrence of $?a$ is unchanged);
3. we naturally extend the semantics of async CCS so that such syntactic tags are preserved along transitions, without altering the labels. E.g., taking P and Q from item 1. above, we have $P[Q/X][\epsilon] \xrightarrow{\tau} ?a[!b] \xrightarrow{?a} \mathbf{0}[!b]$ (note that the tag is discarded when the occurrence of $\underline{Q} = ?a.\mathbf{0}$ is reduced).

Now, let $\ddot{\mathfrak{R}} = \ddot{\mathfrak{R}}' \cup \ddot{\mathfrak{R}}''$, where:

$$\ddot{\mathfrak{R}}' = \left\{ (P'[\mu_X P/\underline{Q}][\sigma'], Q'[\sigma']) \mid \begin{array}{l} \exists \sigma_0, \sigma'. P[Q/X][\sigma_0] \rightarrow^* P'[\sigma'] \\ \wedge P'[\sigma'] \dot{\leq} Q'[\sigma'] \end{array} \right\}$$

$$\ddot{\mathfrak{R}}'' = \left\{ (P'[\sigma'], Q'[\sigma']) \mid \begin{array}{l} \exists P'', Q''. P'' \neq \mu_X P \text{ and} \\ P'[\sigma'] \dot{\leq} P''[\sigma'] \ddot{\mathfrak{R}}' Q''[\sigma'] \dot{\leq} Q'[\sigma'] \end{array} \right\}$$

Intuitively, $\ddot{\mathfrak{R}}$ contains:

- from $\ddot{\mathfrak{R}}'$: for all σ' , all pairs $(P'[\sigma'], Q'[\sigma'])$, given by the following procedure:
 1. take $P''[\sigma'], Q'[\sigma']$ such that $P[Q/X][\sigma] \rightarrow^* P''[\sigma']$ and $P''[\sigma'] \dot{\leq} Q'[\sigma']$
 2. obtain P' by replacing all occurrences of \underline{Q} in P'' with $\mu_X P$ (note that Q' is taken as-is);
- from $\ddot{\mathfrak{R}}''$: the closure of $\ddot{\mathfrak{R}}'$ w.r.t. $\dot{\leq}$, for all pairs in $\ddot{\mathfrak{R}}'$ such that the LHS is *not* yielded by an instance of \underline{Q} replaced by $\mu_X P$.

We prove that $\ddot{\mathfrak{R}}$ is an I/O simulation. Let:

$$F(X) = \left\{ \left(\begin{array}{c} P''[\sigma] \\ Q''[\sigma] \end{array} \right) \mid \begin{array}{l} \exists Q'' \\ Q''[\sigma] \Rightarrow Q'' \\ \text{and} \end{array} \left\{ \begin{array}{l} a. P''[\sigma]\Downarrow^! = \emptyset \text{ implies} \\ \quad Q''\Downarrow^! = \emptyset; \\ b. Q''\Downarrow^{??} \subseteq P''[\sigma]\Downarrow^? \wedge \\ \quad (Q''\Downarrow^? = \emptyset \\ \quad \text{implies } P''[\sigma]\Downarrow^? = \emptyset); \\ c. P''[\sigma] \xrightarrow{\tau} P''[\sigma] \text{ implies} \\ \quad \exists Q'''[\sigma]. Q'' \Rightarrow Q'''[\sigma] \\ \quad \wedge (P'''[\sigma], Q'''[\sigma]) \in X; \\ d. P''[\sigma] \xrightarrow{!a} P''[\sigma] \text{ implies} \\ \quad \exists Q'''[\sigma]. Q'' \xrightarrow{!a} Q'''[\sigma] \\ \quad \wedge (P'''[\sigma], Q'''[\sigma]) \in X; \\ e. P''[\sigma] \xrightarrow{?a} P''[\sigma] \wedge Q'' \xrightarrow{?a} \\ \quad \exists Q'''[\sigma]. Q'' \xrightarrow{?a} Q'''[\sigma] \\ \quad \wedge (P'''[\sigma], Q'''[\sigma]) \in X. \end{array} \right\} \right\}$$

By the coinduction proof principle, we have to show that $\mathfrak{R} \subseteq F(\mathfrak{R})$. When $(P''[\sigma], Q''[\sigma]) \in \mathfrak{R}$, we have two cases:

A. if $(P''[\sigma], Q''[\sigma]) \in \mathfrak{R}'$, then $P'' = P'[\mu_X P/Q]$, for some $\sigma_0, P'[\sigma]$ such that $P[Q/X][\sigma_0] \rightarrow^* P'[\sigma] \leq Q''[\sigma]$. Regarding such P' , we have two possibilities:

(i) $P' \neq Q$. By clause *a* of Definition 7.6, X is always guarded in some subterm $\ell.X$ of P , and thus, all occurrences of Q in P' are guarded by some input or output ℓ , and the same holds for all occurrences of $\mu_X P$ in $P'[\mu_X P/Q] = P''$. Now, let Q'' be a predictive set supporting $P'[\sigma] \leq Q''[\sigma]$: we show that Q'' is also a predictive set for the pair $(P'[\mu_X P/Q][\sigma], Q''[\sigma]) = (P''[\sigma], Q''[\sigma])$. In detail:

– clause *a*. Assume $P''[\sigma]\Downarrow^! = P'[\mu_X P/Q][\sigma]\Downarrow^! = \emptyset$. It suffices to show that this implies $P'[\sigma]\Downarrow^! = \emptyset$, and therefore $Q''\Downarrow^! \neq \emptyset$ (since $P'[\sigma] \leq Q''[\sigma]$ with predictive set Q''). Note that $P''[\sigma]\Downarrow^! = \emptyset$ implies $\sigma = \epsilon$. By contradiction, assume $P'[\epsilon]\Downarrow^! \neq \emptyset$: this means that the weakly reachable outputs of $P'[\epsilon]$ must occur in Q , but are removed when replacing it with $\mu_X P$ in P'' . For such outputs to be part of $P'[\epsilon]\Downarrow^!$, we have two possibilities:

- some Q in P' is unguarded. Then, since $P' \neq Q$, we must have $P' \equiv Q \mid P'_0$ (for some P'_0), which implies that $P[Q/X][\sigma_0]$ can reduce to a parallel term, i.e., $\mu_X P$ is not sequential: contradiction;
- some Q in P' is guarded by τs only. Then, some X in P is not guarded in some subterm $\ell.X$, thus violating clause *a* of Definition 7.6. Hence, $\mu_X P$ is not a CCS⁻ term: contradiction;

– clause *b*, first part. By Proposition D.5 we have $Q''\Downarrow^{??} \subseteq P'[\sigma]\Downarrow^{??}$. By contradiction, let $Q''\Downarrow^{??} \not\subseteq P'[\sigma]\Downarrow^{??}$, and therefore, $Q''\Downarrow^{??} \not\subseteq P'[\mu_X P/Q][\sigma]\Downarrow^{??}$. It means that some persistent input of Q'' that is also persistent in $P'[\sigma]$ is not weakly reachable in $P'[\mu_X P/Q][\sigma]$. Since P' is sequential, it follows that such an input occurs in Q as persistent (i.e., it belongs to $Q[\sigma]\Downarrow^{??}$), but it is not weakly reachable in P . Hence, we have $\emptyset \neq Q[\sigma]\Downarrow^{??} \not\subseteq P[\sigma]\Downarrow^?$. Then, considering $P[Q/X][\sigma]$ and the sequentiality of P , we have two possibilities:

- all X in P are input-guarded. Then, we have $P[Q/X][\sigma]\Downarrow^? = P[\sigma]\Downarrow^?$. Now, note that for any set Q such that $Q[\sigma] \Rightarrow Q$, we also have $Q[\sigma]\Downarrow^{??} \subseteq Q\Downarrow^{??} \not\subseteq P[\sigma]\Downarrow^? = P[Q/X][\sigma]\Downarrow^?$. Thus, we obtain the contradiction $P[Q/X][\sigma] \not\leq Q[\sigma]$, because no predictive set could satisfy the first part of clause *b* of Definition 6.4;

– some X in P is output-guarded and not input-guarded. Since $Q[\sigma]\Downarrow^{??} \neq \emptyset$, by Proposition E.4 we get the contradiction $P[Q/X][\sigma] \not\leq Q[\sigma]$;

– clause *b*, second part. Assume $Q''\Downarrow^? = \emptyset$: this implies $P'[\sigma]\Downarrow^? = \emptyset$. By contradiction, assume $P''[\sigma]\Downarrow^? = P'[\mu_X P/Q][\sigma]\Downarrow^? \neq \emptyset$. This implies that $Q[\sigma]\Downarrow^? = \emptyset$, while $\mu_X P[\sigma]\Downarrow^? = P[\sigma]\Downarrow^? \neq \emptyset$. This means $P[Q/X][\sigma]\Downarrow^? \neq \emptyset$. Moreover, since $Q[\sigma]\Downarrow^? = \emptyset$, for any set Q such that $Q[\sigma] \Rightarrow Q$ we have $Q\Downarrow^? = \emptyset$. But then, we obtain the contradiction $P[Q/X][\sigma] \not\leq Q[\sigma]$, since no predictive set could satisfy the second part of clause *b* of Definition 6.4.

For the remaining clauses of F , reminding that $P'[\sigma] \leq Q''[\sigma]$, we take all pairs $P'''[\sigma'] \leq Q'''[\sigma']$ that satisfy Definition 6.4 after a transition of $P'[\sigma]$, apply the substitution $P'''[Q/X][\sigma']$, and show that the resulting pair satisfies clauses *c–e* of F . More precisely, take all $\ell_\tau, P'''[\sigma']$ such that $P'[\sigma] \xrightarrow{\ell_\tau} P'''[\sigma']$, and all $Q'''[\sigma']$ such that:

$$P'[\sigma] \xrightarrow{\ell_\tau} P'''[\sigma'] \quad \text{and} \quad Q''[\sigma] \Rightarrow Q'' \xRightarrow{\ell_\tau} Q'''[\sigma'] \\ \text{and} \quad P'''[\sigma'] \leq Q'''[\sigma']$$

Observe that, since

$$P[Q/X][\sigma_0] \rightarrow^* P'[\sigma] \xrightarrow{\ell_\tau} P'''[\sigma'] \quad \text{and} \quad P'''[\sigma'] \leq Q'''[\sigma']$$

\mathfrak{R}' contains a pair $(P'''[\mu_X P/Q][\sigma'], Q'''[\sigma'])$, and thus:

$$P''[\sigma] = P'[\mu_X P/Q][\sigma] \xrightarrow{\ell_\tau} P'''[\mu_X P/Q][\sigma'] \\ \text{and} \quad (P'''[\mu_X P/Q][\sigma'], Q'''[\sigma']) \in \mathfrak{R}' \subseteq \mathfrak{R}$$

We can verify that such pairs $(P'''[\mu_X P/Q][\sigma'], Q'''[\sigma'])$ satisfy the existentials in clauses *c–e* of F (depending on whether ℓ_τ is a τ , an output, or an input). Therefore, we conclude $(P''[\sigma], Q''[\sigma]) \in F(\mathfrak{R})$;

(ii) $P' = Q$. Then, we have $P'' = \mu_X P$ and $P'[\sigma] = Q[\sigma] \leq Q''[\sigma]$. We observe:

1. since by hypothesis $P[Q/X] \leq Q$, then from the pair $(P[Q/X][\sigma], Q[\sigma]) \in \leq$, the set \mathfrak{R}' contains:

$$(P[Q/X][\mu_X P/Q][\sigma], Q[\sigma]) = (P[\mu_X P/X][\sigma], Q[\sigma]) \in \mathfrak{R}'$$

2. since $P''[\sigma]$ and $P[\mu_X P/X][\sigma]$ are bisimilar, then by Theorem 6.11 we have $P''[\sigma] \leq P[\mu_X P/X][\sigma]$. Summing up:

$$P''[\sigma] = \mu_X P[\sigma] \leq P[\mu_X P/X][\sigma] \mathfrak{R}' Q[\sigma] \leq Q''[\sigma]$$

Hence, noticing that $P[\mu_X P/X] \neq \mu_X P$ (since X is guarded in P), we also have $(P''[\sigma], Q''[\sigma]) \in \mathfrak{R}''$: this is studied in case *B*. below;

B. if $(P''[\sigma], Q''[\sigma]) \in \mathfrak{R}''$, then there exist $P' \neq \mu_X P$ and Q' such that:

- $P''[\sigma] \leq P'[\sigma]$, for some predictive set P' ;
- $P'[\sigma] \mathfrak{R}' Q'[\sigma]$, for some predictive set Q' , determined as per case *A.(i)* above (since $P' \neq \mu_X P$);
- $Q'[\sigma] \leq Q''[\sigma]$, for some predictive set Q'' .

Now, applying the technique in the proof of Theorem 6.10 (transitivity, page 32), we can:

- use P' and Q' to build a predictive set Q''' such that the pair $(P''[\sigma], Q'[\sigma])$ satisfies clauses *a–e* of F (note that the substitutions $[\mu_X P/Q]$ in P'' are handled similarly to case *A.(i)* above);

– use \mathbb{Q}''' and \mathbb{Q}'' to build a predictive set \mathbb{Q}^* such that the pair $(P''[\sigma'], Q''[\sigma'])$ satisfies clauses $a-e$ of F .
E.g., for clause e of F , we have:

$$P''[\sigma] \xrightarrow{?a} P'''[\sigma] \quad \text{and} \quad \mathbb{Q}^* \xrightarrow{??a} \text{ implies } \exists P_0''', Q_0''', Q'''.$$

$$\mathbb{P}' \xrightarrow{?a} P_0''' \quad \text{and} \quad \mathbb{Q}' \xrightarrow{?a} Q_0''' \quad \text{and} \quad \mathbb{Q}^* \xrightarrow{?a} Q'''[\sigma] \quad \text{and}$$

$$P'''[\sigma] \dot{\leq} P_0'''[\sigma] \dot{\mathfrak{R}}_1 Q_0'''[\sigma] \dot{\leq} Q'''[\sigma]$$

and if $P''' \neq \underline{\mu_X} P$, then $(P'''[\sigma], Q'''[\sigma]) \in \dot{\mathfrak{R}}'' \subseteq \dot{\mathfrak{R}}$; otherwise, $P''' = \underline{\mu_X} P$ is bisimilar to $P[\underline{\mu_X} P/X]$, and we have:

$$P'''[\sigma] \dot{\leq} P[\underline{\mu_X} P/X][\sigma] \dot{\mathfrak{R}}' Q_0'''[\sigma] \dot{\leq} Q'''[\sigma]$$

and thus, since $P[\underline{\mu_X} P/X] \neq \underline{\mu_X} P$, we get $(P'''[\sigma], Q'''[\sigma]) \in \dot{\mathfrak{R}}'' \subseteq \dot{\mathfrak{R}}$. Hence, we conclude $(P''[\sigma], Q''[\sigma]) \in F(\dot{\mathfrak{R}})$.

Then, $\dot{\mathfrak{R}}$ is an I/O simulation; further, since $\dot{\leq}$ is the *largest* I/O simulation, we have $\dot{\mathfrak{R}} \subseteq \dot{\leq}$. Finally, we are left to prove that $\forall \sigma. \underline{\mu_X} P[\sigma] \dot{\leq} Q[\sigma]$: we simply observe that $\forall \sigma. (\underline{\mu_X} P[\sigma], Q[\sigma]) \in \dot{\mathfrak{R}}' \subseteq \dot{\mathfrak{R}} \subseteq \dot{\leq}$. \square